

Rhein Tech Laboratories, Inc.
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Client: Alinco, Inc.
Model: DR-06T
Standards: FCC 15.121
& IC RSS-215
Report: 2007223

Appendix C: FCC Attestation Letter

Please refer to the following page.



INCORPORATED

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Osaka 530-0004, Japan Fax: 06 (4797) 2156 Phone: 06 (4797) 2134

Aug, 1, 2007

Federal Communications Commissions

RE:PH3DR06T / 800MHz analog cellular telephone band blocking

Dear Sir or Madam.

This is to declare that the device in application PH3DR06T has been blocked for any and all access of 824.000 to 849.9975MHz and 869.000 to 894.9975MHz.

The device uses double super heterodyne as a receiver circuit and frequency is generated by a PLL synthesizer circuitry. The first local oscillation frequencies are determined by the N value data of the CPU. The 40.000 – 69.995MHz range is determined by N-value data of 50.700 – 80.695MHz, and such values are not able to be changed by any means.

The 4 low-pass filters and 4 tuning circuits are used to filter out the unwanted bands. The CPU used in this device, our parts code XA1170B, vendor's code M38268MCA-085GP is exclusively programmed and burned for this US export model. ALINCO, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the declared cellular frequencies. Moreover, the entire circuitry of this device is not designed to cover the cellular frequencies anyway.

To my best of knowledge being informed by the chief-engineer in charge of PH3DR06T, above declared is true.

Sincerely

A handwritten signature in cursive script, appearing to read "Kusuhara".

Kazuhiro Kusuhara

General manager, Production Section
Alinco, Inc. Electronics Division