Appendix D: FCC Attestation Letter

Please refer to the following page.

21/11/2006

Federal Communications Commissions

RE: DJ-X30T /800MHz analog cellular telephone band blocking

To Whom It May Concern,

This is to declare that the device in application DJ-X30T has been blocked for any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz.
The device uses triple super heterodyne circuit for a narrow –FM/AM and double super heterodyne for a wide-FM receiver and frequency is generated only by PLL synthesizer circuit.
The first local oscillation frequencies are as follows and it can't be altered other than these values:

Receiving frequency	:	VCO frequency	:	Image frequency
0.1~ 244.145MHz	:	244.050 ~ 488.095MHz	:	488.000 ~ 732.045MHz
244.150 ~ 487.995MHz	:	244.050 ~ 365.975MHz	:	732.050 ~ 975.895MHz
488.000 ~ 732.045MHz	:	244.050 ~ 488.095MHz	:	0.1 ~ 244.145MHz
732.050 ~ 823.995MHz	:	244.050 ~ 290.025 MHz	:	244.150 ~ 336.095 MHz
850.000 ~ 868.995MHz	:	303.025 ~ 312.525 MHz	:	362.100 ~ 381.095MHz
895.000 ~ 1299.995MHz	:	325.525 ~ 528.025MHz	:	407.100 ~ 812.095MHz

As shown, none of them are in the cell-phone frequency.

In addition, band-pass filters are used to filter-out unwanted signals. The CPU used in this device, our parts code IC203 XA1232, vender's code M30620FCPGP is exclusively programmed, burned as above and ALINCO, Inc exports solely this version to the US market. This component is a one-time chip therefore it can't be modified or reprogrammed by any means.

To my best of knowledge, above declared is true

Sincerely,

Kazuhiro Kusuhara
General manager
Alinco,Inc. Electronics Div.