



Alinco, Inc. Electronics Division

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Federal Communications Commissions

RE: PH3 DJV57T / 800MHz analog cellular telephone band blocking

Dear Sir or Madam,

This is to declare that the device in application PH3 DJV57T has been blocked for any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz.

For the cellular-blocking, the device uses double super heterodyne PLL synthesizer circuitry as a receiver circuit. Its first oscillation frequencies to compose receiving frequency are as follows and follows and they can't be tuned to other frequencies.

Receiving Frequency (MHz)	1st local oscillation freq. (MHz)	Image freq. (MHz)
136.000~173.995	97.150~135.145	58.3000~ 96.295
400.000~511.995	361.150~473.145	322.300~434.295

In addition, a bandpass-filter is used to receive above-mentioned frequencies therefore 800MHz range is filtered out.

The CPU used in this device, our parts code XA1496, vender's code R5F2L3ACANFP#U1 is exclusively programmed and burned for this US export model. Alinco, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the declared cellular frequencies.

To my best of knowledge being informed by the chief-engineer in charge of PH3 DJV57T, above declared is true.

Respectfully,



Kazuhiro Kusuhara
General manager, Production Section,
Alinco, Inc. Electronics Division