Rhein Tech Laboratories, Inc.
360 Herndon Parkway
Suite 1400
Herndon, VA 20170
http://www.rheintech.com

Client: Alinco, Inc.
Model: DJ-V17T
Standards: FCC 15.121
& IC RSS-215
Report: 2006012

Appendix C: FCC Attestation Letter

Please refer to the following page.

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Feb. 1, 2006

Federal Communications Commissions

<u>PH3 DJ-V17T / 800MHz analog cellular telephone band blocking declaration</u>

Dear Sir or Madam,

This is to declare that the device in application PH3 DJ-V17T has been blocked for any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz.

For the cellular-blocking, the device uses double super heterodyne PLL synthesizer circuitry as a receiver circuit. Its first oscillation frequencies to compose receiving frequency are as follows and they can't be tuned to other frequencies:

Receiving frequency (MHz) :	VCO frequency (MHz) :	Image frequency (MHZ):
135.000 - 173.995 :	113.300 – 152.295 :	91.600 – 130.595
130.000 – 134.995 :	151.700 – 156.695 :	173.400 – 178.395

In addition, a bandpass-filter is used to receive above-mentioned frequencies therefore 800MHz range is filtered out. These oscillation frequencies can't be altered because the CPU used in this device, our parts code XA1121, vender's code M38268MCA-076GP#UO is exclusively programmed for DJ-V17T and Alinco, Inc exports this version only to the US market. This CPU can't be modified by any means to receive the cellular frequencies declared above because its memory is a one-time ROM.

Respectfully,

Kusuhara

Kazuhiro Kusuhara
General manager, Production Section,
Alinco, Inc. Electronics Division