APPENDIX B: ATTESTATION LETTER

Please refer to the following page for an updated attestation concerning this revised EUT and cellular blocking.

ALINCO INCORPORATED
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April 28, 2003.

Federal Communications Commissions

RE: PH3 DJ-S40T / 800MHz analog cellular telephone band blocking

Dear Sir or Madam,

This is to declare that the device in application PH3 DJ-S40T(revised model) has been blocked for any and all access of 824.00 to 849.9975MHz ranges.

The device uses PLL Synthesizer for a receiver circuit. The first local oscillation frequencies are determined by N data generated by the CPU, and they are 378.300 to 458.295MHz. Actual receiving frequency range of this device is 400.00 to 479.995MHz.

In addition, 5 low-pass filters as well as 4 synchronize circuit are used to eliminate unwanted signals to be received. The CPU used in this device, our parts code XA0903, vender's code M38224M6M233HP is exclusively programmed and burned for US export model. ALINCO, INC exports solely this version to the US market, and this CPU can't be modified by any means to receive the cellular frequencies declared above. Moreover, the entire circuitry of this device is not designed to cover the cellular frequencies anyway.

To my best of knowledge informed from the chief-engineer in charge of PH3 DJ-S40T, above declared is true.

Sincerely,

Kazuhiro Kusuhara
Manager, Production & Engineering Section
Alinco,Inc. Electronics Div.