Appendix C: FCC Attestation Letter

Please refer to the following page.

ALINCO

Alinco, Inc. Electronics Division
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October 28, 2011

Federal Communications Commissions

RE: PH3 DJG29T / 800MHz analog cellular telephone band blocking

Dear Sir or Madam,

This is to declare that the device in application PH3 DJG29T has been blocked for any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz.

The device uses a double super heterodyne for FM and NFM as receiver circuits and frequencies are generated by a PLL synthesizer circuitry. The first local oscillation frequencies are determined by the N value data of the CPU, and such values can't be changed by any means.

MAIN band

Receiving Freq.range (MHz)	1 st local oscillation freq. (MHz)	Image freq. (MHz)
216.000~249.995	267.650~301.645	319.300~ 353.295
902.000~927.995	953.65~979.645	1005.300~1031.295

SUB band

Receiving Freq.range (MHz)	1 st local oscillation freq. (MHz)	Image freq. (MHz)
216.000~249.995	266.750~300.745	317.500~ 351.495
902.000~927.995	952.750~978.745	1003.500~1029.495

The CPU used in this device, our parts code XA1387, vender's code M30625FGPG is exclusively programmed and burned for this US export model. Alinco, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the declared cellular frequencies.

To my best of knowledge being informed by the chief-engineer in charge of PH3 DJG29T, above declared is true.

Sincerely,

Kusuhara

Kazuhiro Kusuhara
General manager, Production Section,
Alinco, Inc. Electronics Division