

Rhein Tech Laboratories  
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Client: Alinco, Inc  
Model: DJ-596TMkII  
Standards: FCC 15.121/IC RSS-215  
Report #: 2002219  
Date: December 31, 2002

## APPENDIX C: ATTESTATION LETTER

Please refer to the following page.



**ALINCO**

INCORPORATED

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11/11/2002

Federal Communications Commissions

RE: PH3 DJ-596TMkII / 800MHz analog cellular telephone band blocking

Dear Sir or Madam,

This is to declare that the device in application PH3 DJ-596TMkII has been designed to block any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz. The device uses double super heterodyne as a receiver system and the frequencies are generated by PLL synthesizer circuitry. The first local oscillation frequencies are:

VHF circuit: 175.150 to 213.145MHz

UHF circuit: 360.850 to 472.845MHz

Actual operating frequencies of this device are:

VHF TX: 144.00 to 147.995MHz VHF RX: 136.00MHz to 173.995MHz

UHF TX: 420.00 to 449.995MHz UHF RX:400.00MHz to 511.995MHz

VHF circuit uses upper side of local oscillation while UHF circuit uses Lower side. In addition, band-pass filters are used to receive both VHF/UHF frequencies and to filter-out the unwanted signals. The CPU used in this device, our parts code IC6 XA0942, vender's code M38268MCL053GP is exclusively programmed and burned for this USA model. We export solely this version to the US market, and this CPU can't be modified by any means to receive the above-declared cellular-phone frequencies.

To my best of knowledge being informed by the chief-engineer in charge of PH3 DJ-596TMkII, above declared is true.

Sincerely,

Kazuhiro Kusuvara

Vice-Chief, Production Section

Electronics Div., Alinco,Inc.