APPENDIX C: CELL BLOCK ATTESTATION LETTER(S) (IF APPLICABLE)

Feb/7/2002

Federal Communications Commissions

<u>RE: PH3 DJ-296T / 800MHz analog cellular telephone band blocking</u>

Dear Sir or Madam,

This is to declare that the device in application PH3 DJ-296T has been blocked for any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz.

The device uses double super heterodyne as a receiver circuit and frequency is generated with PLL synthesizer circuitry. The first local oscillation frequency is between 185.150 and 219.145Mhz range, and actual receiving frequencies of this device is 216.000 to 249.995MHz. In addition, the harmonics are filtered out before the signal goes into the mixer circuit, therefore 800MHz range can't be received. The low-pass filters are set for 280MHz as a cut-off frequency.

The CPU used in this device, our parts code IC9 XA0916, vender's code M38267M8LGP is exclusively programmed and burned for US export model. Alinco, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the cellular frequencies declared above. Moreover, the entire circuitry of this device is not designed to cover the cellular frequencies anyway.

To my best of knowledge informed from the chief-engineer in charge of PH3 DJ-296T, above declared is true.

Respectfully,

Kazuhiro Kusuhara
General manager, Production Section,
Alinco, Inc. Electronics Division