

Figure 1: Main PCB – Top View

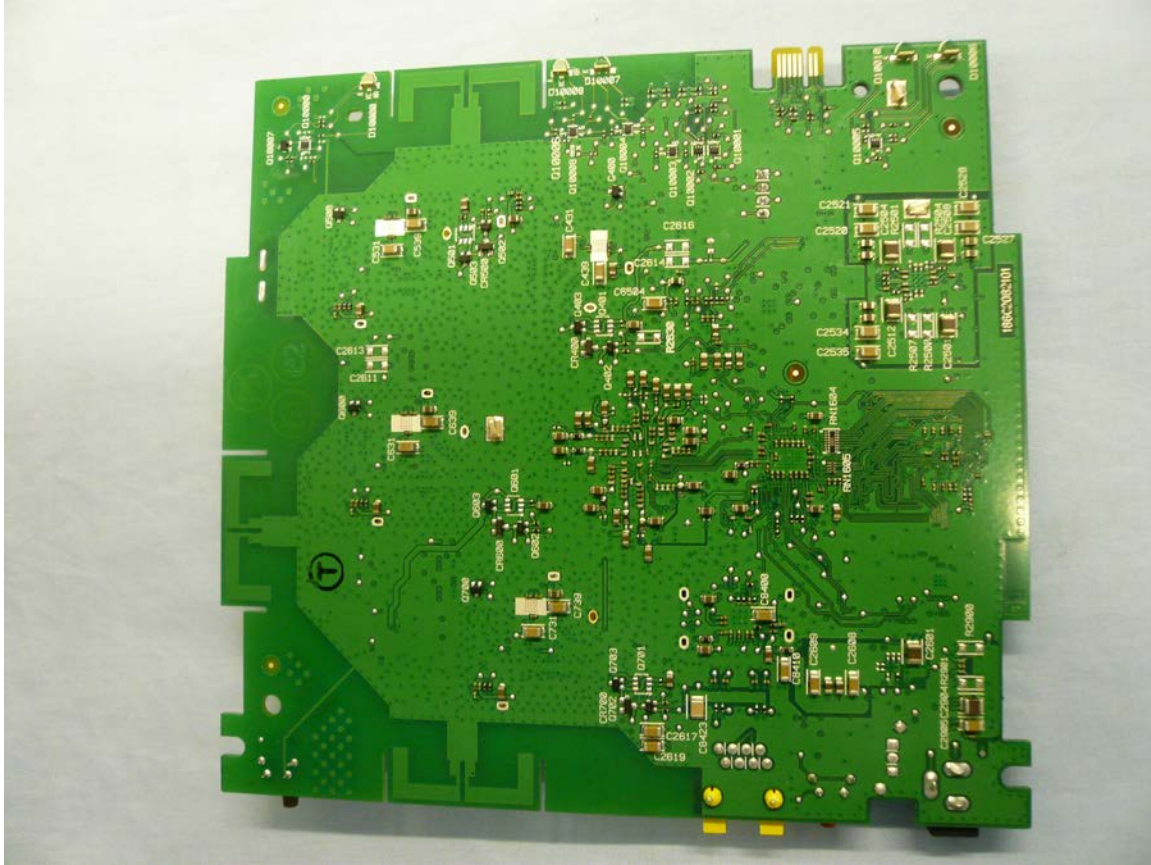


Figure 2: Main PCB – Bottom View



Figure 3: Chipset Photo without RF Shielding