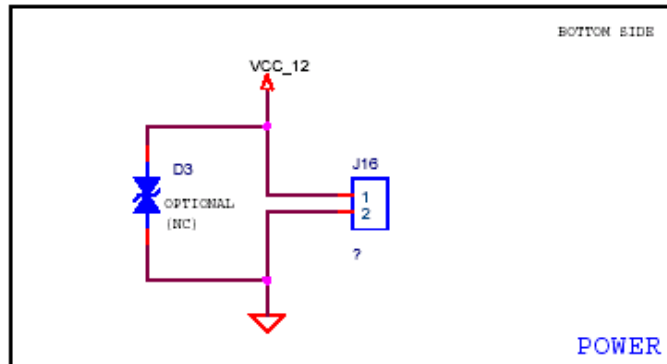
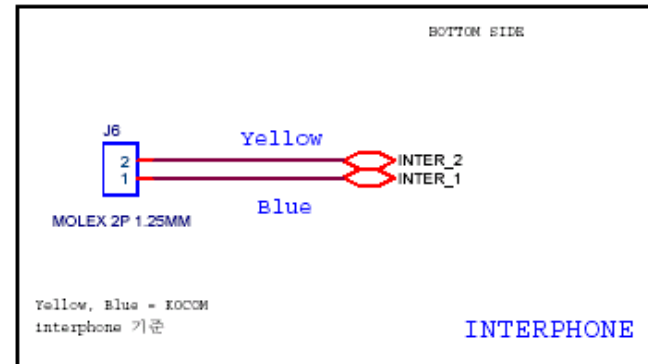


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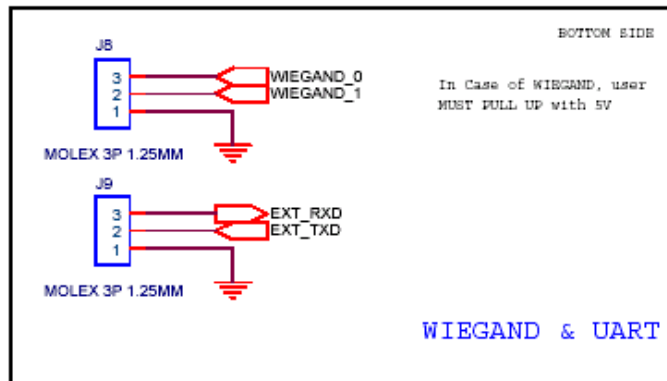
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2. Connector inside 1
3. Connector inside 2
4. Power
5. Power 2
6. CPU
7. Memory
8. FPGA
9. Network
10. Reset, RTC, Buzzer, Relay
11. Voice & Amp
12. RF
13. INTERPHONE
14. AUTO ON B/D



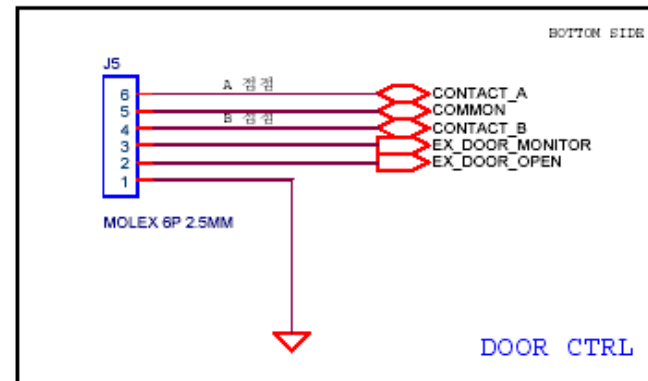
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Interphone Group

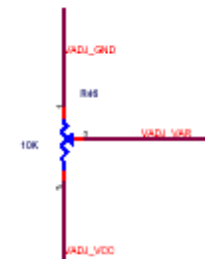
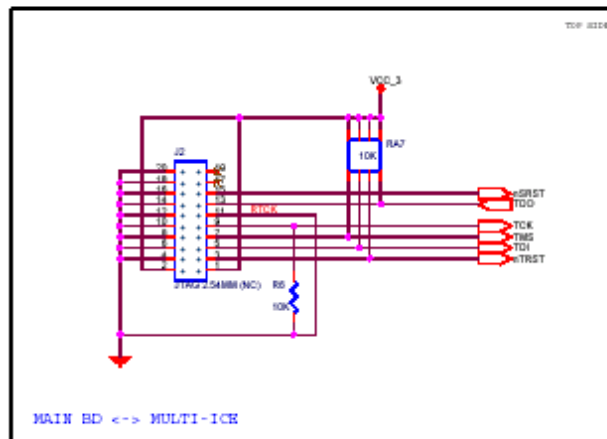
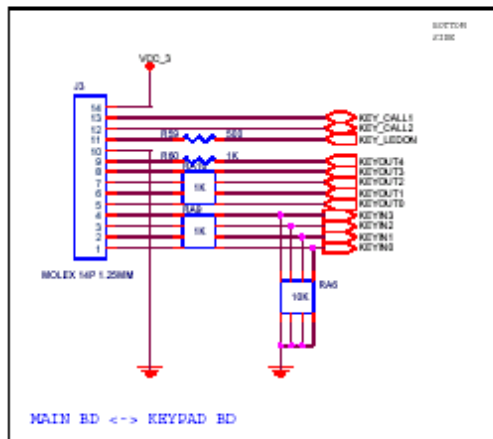
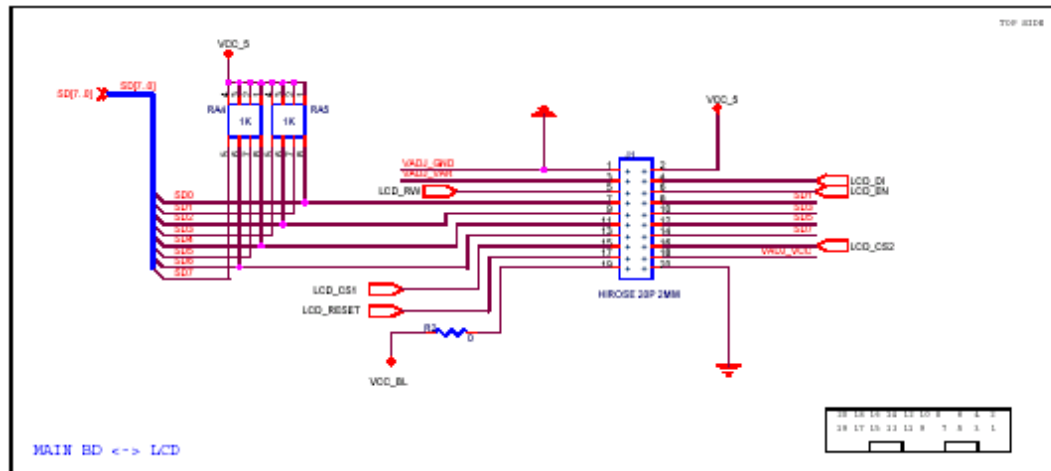
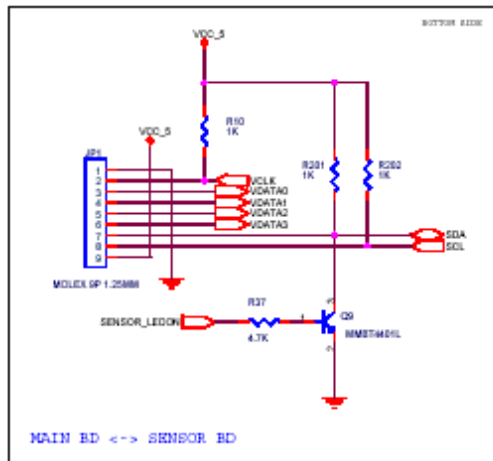


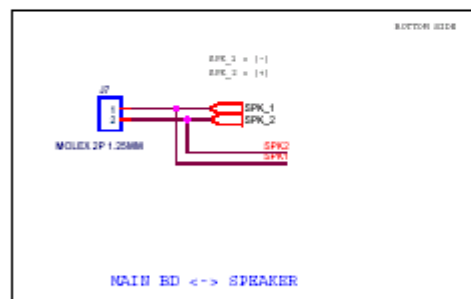
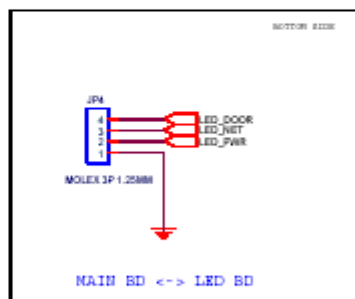
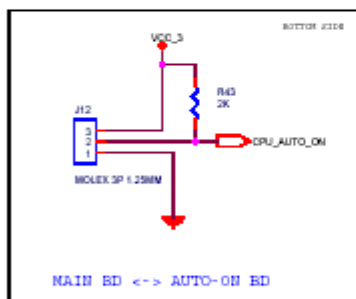
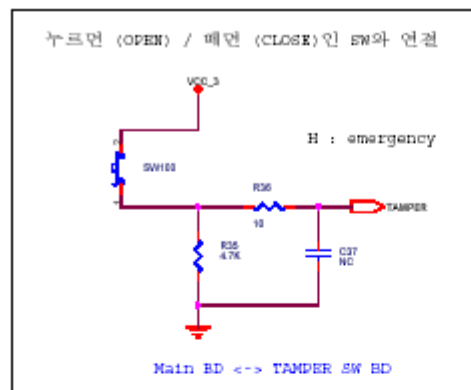
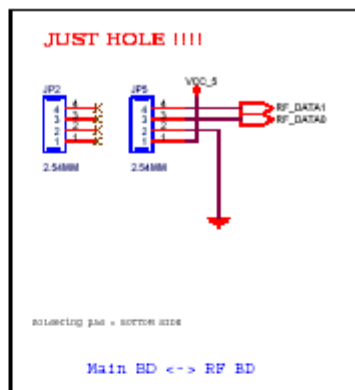
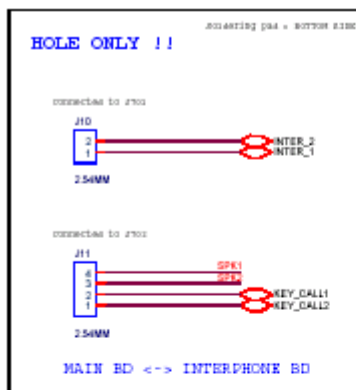
Comm Group



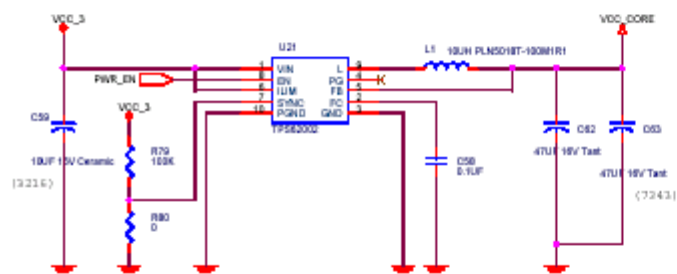
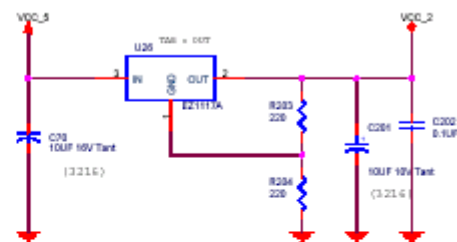
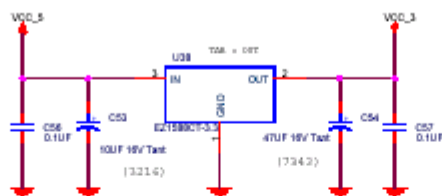
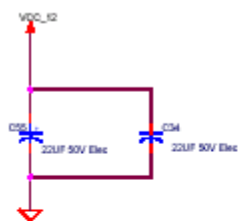
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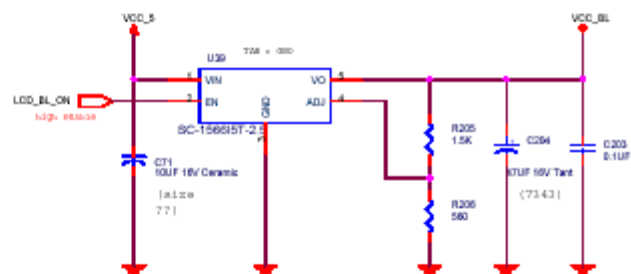




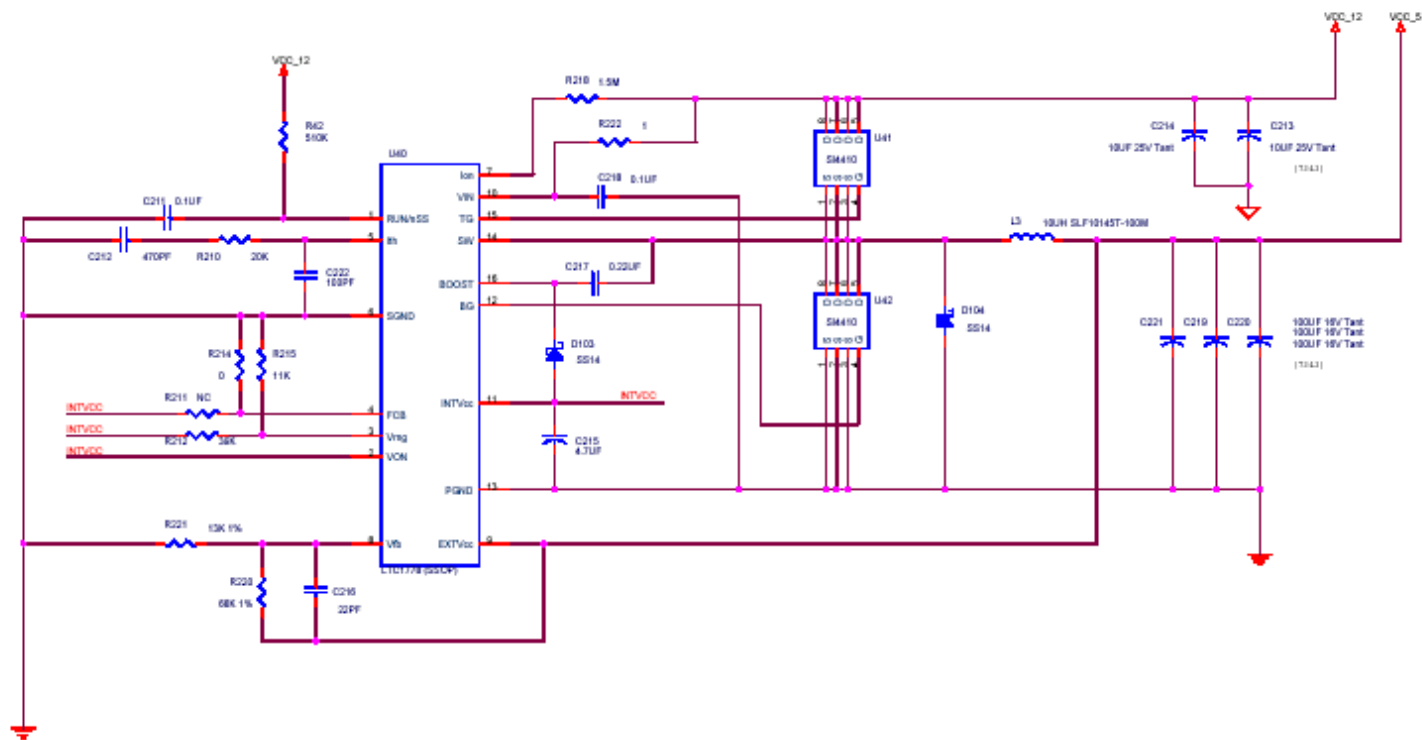
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Ver: Ver000400, June 01, 2001, Sheet 3 of 11		



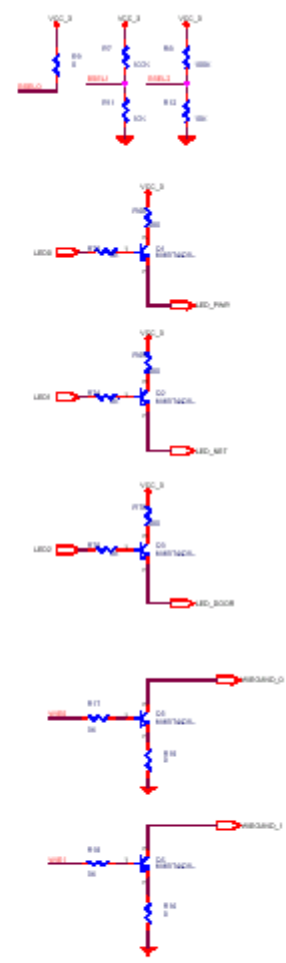
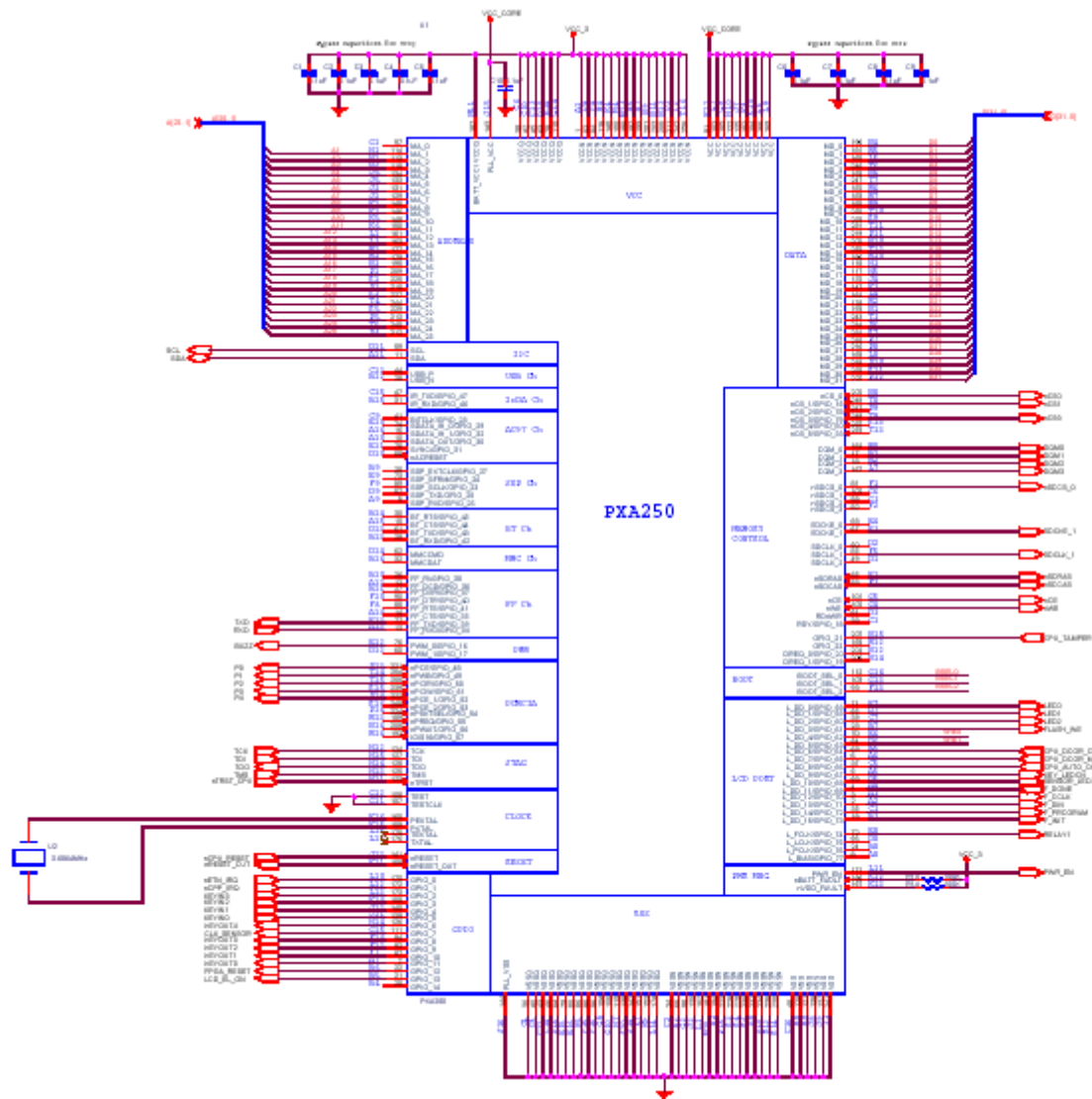
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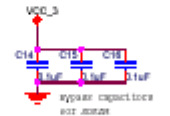
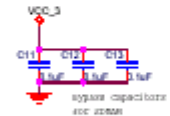
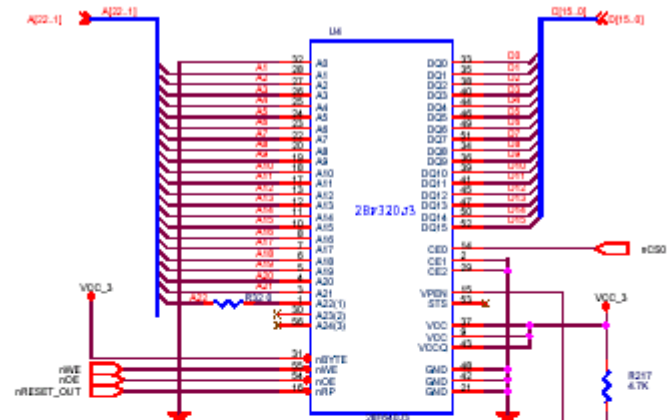
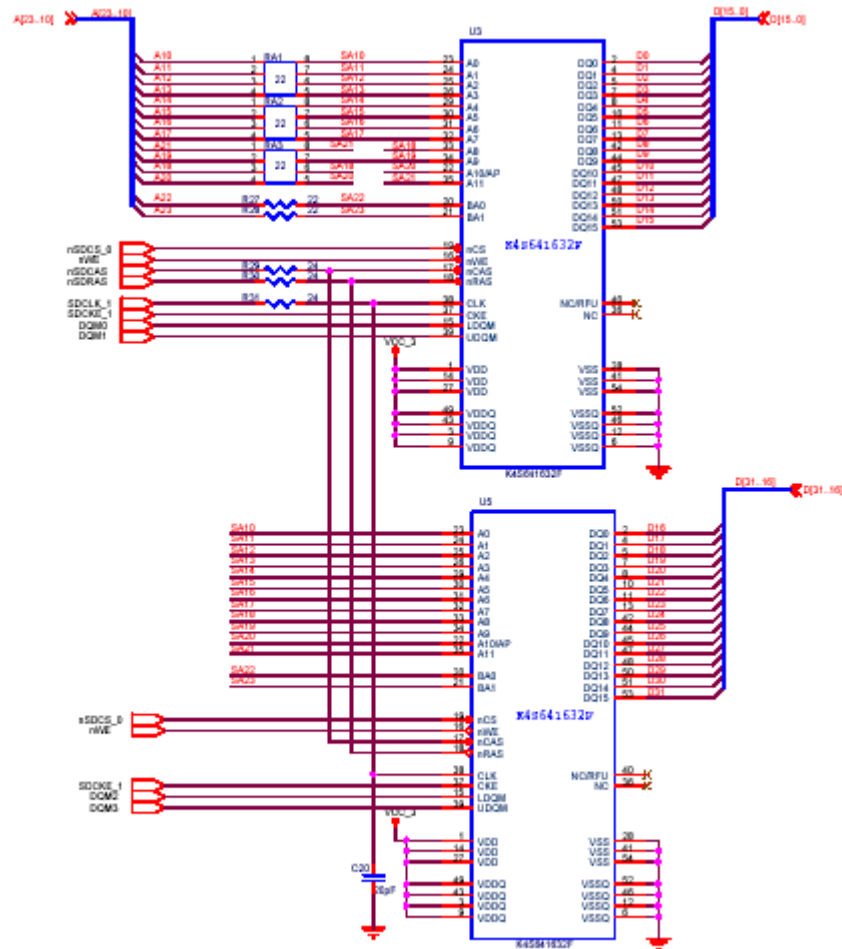
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Rev. 1.0	Doc#	1.0



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Date	Wednesday, 2010-04-20	Page 5 of 11



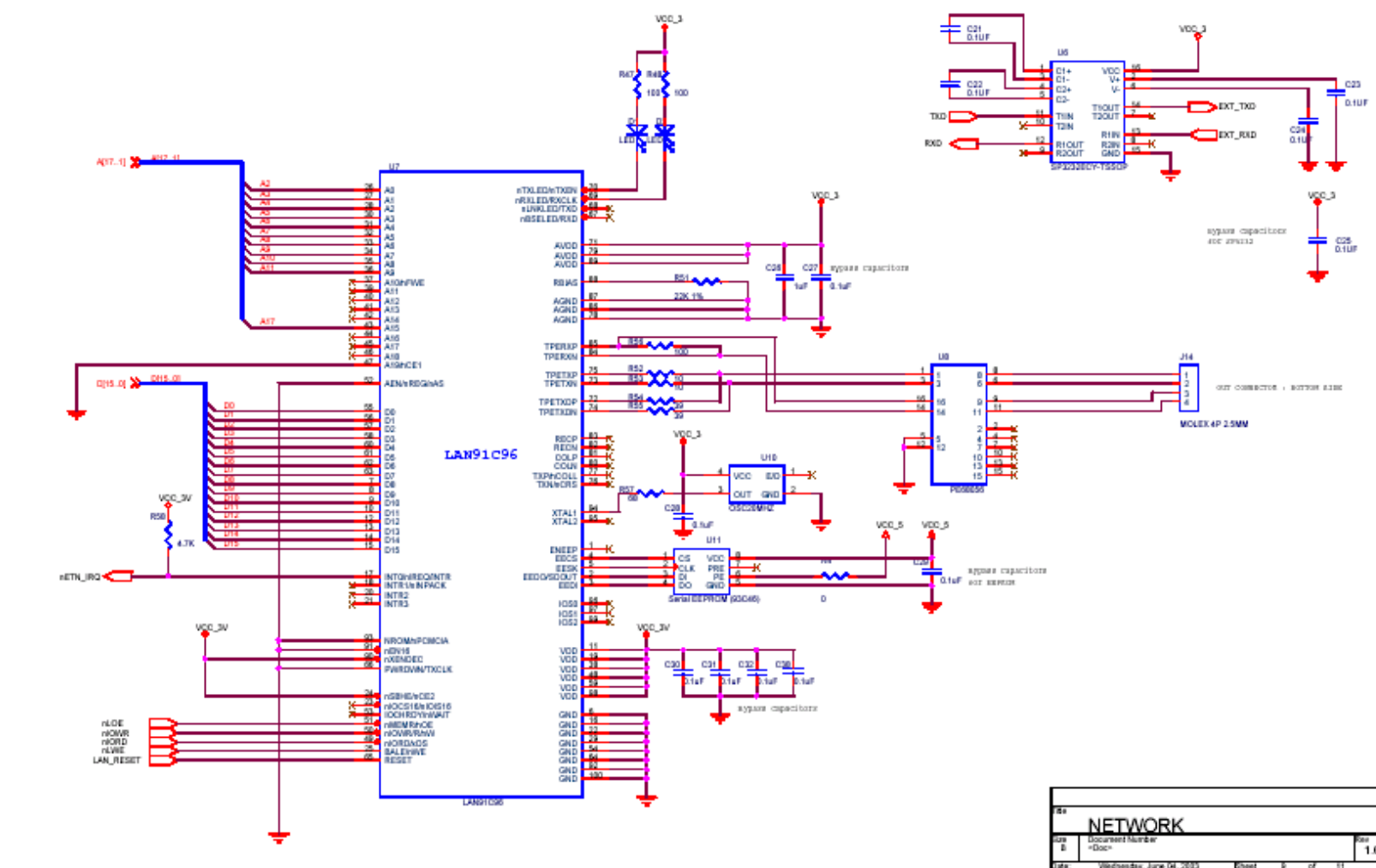
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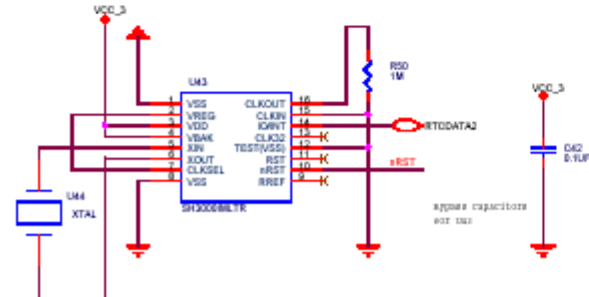
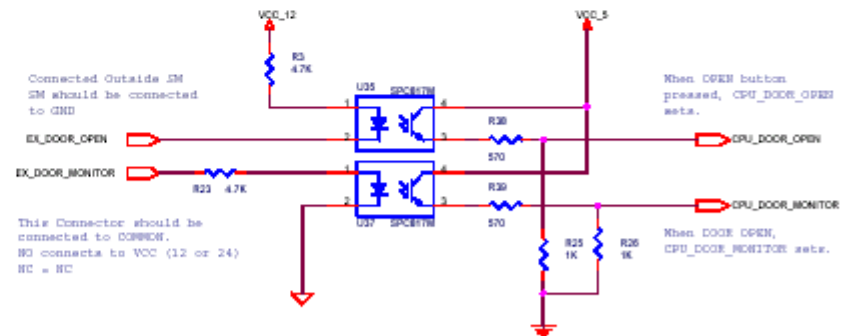
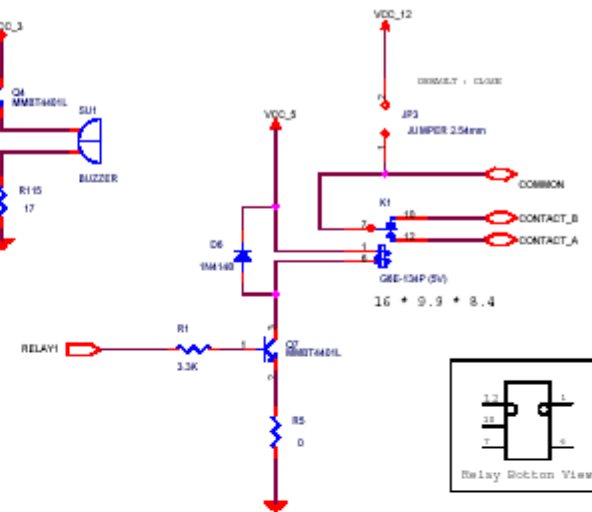
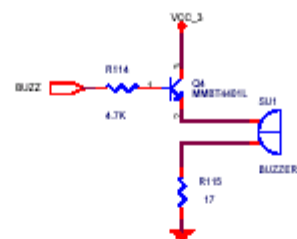


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A22 : 64Mbit (8Mbyte)

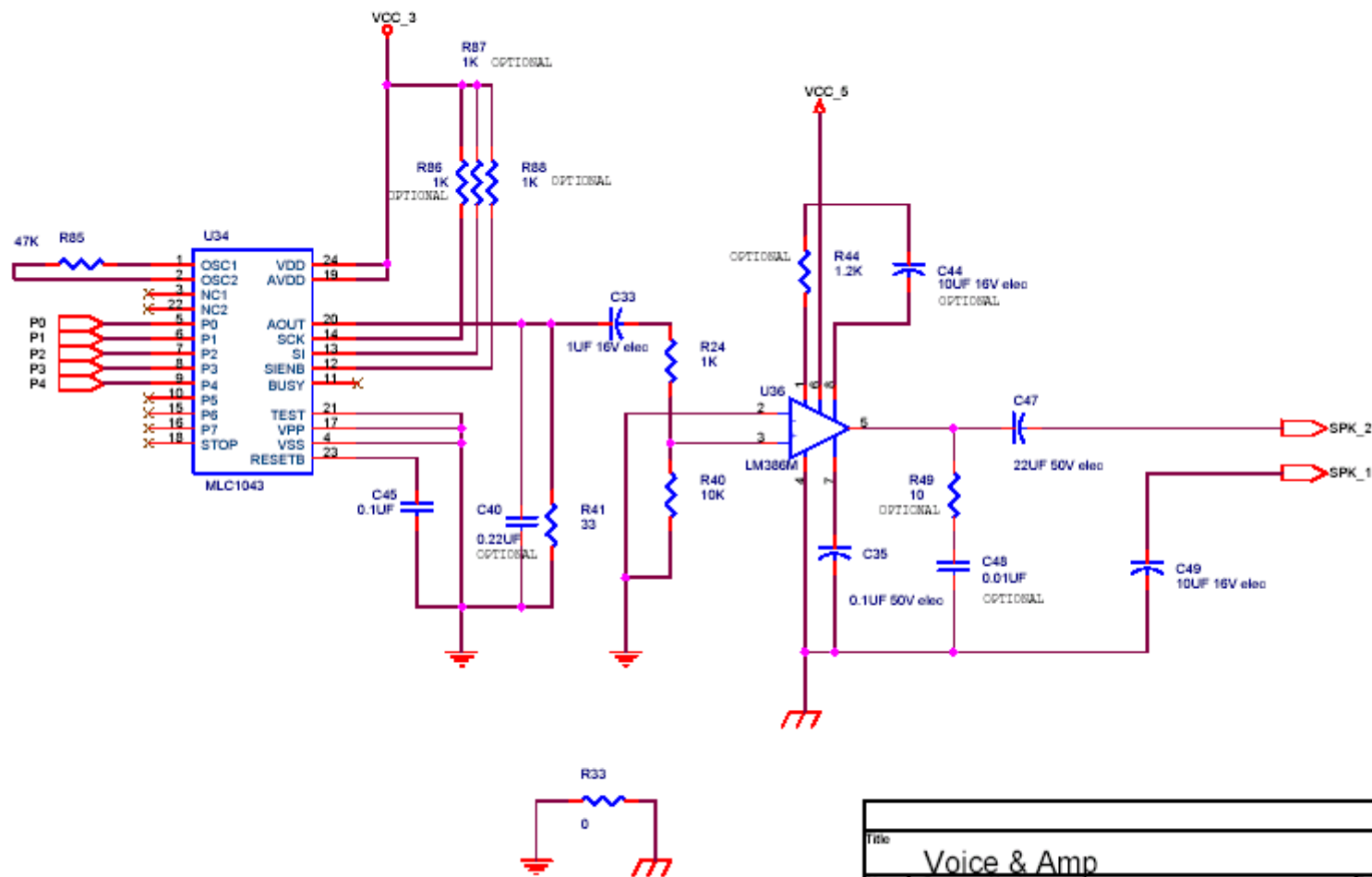
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Date: Wednesday, July 04, 2001		
Sheet 7 of 11		





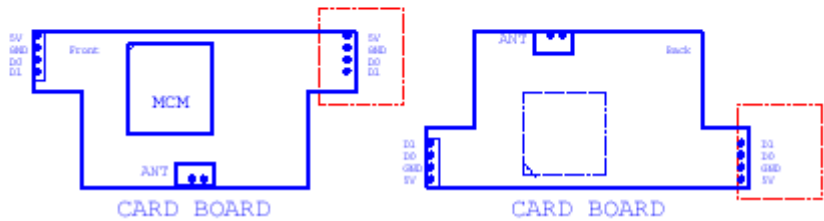
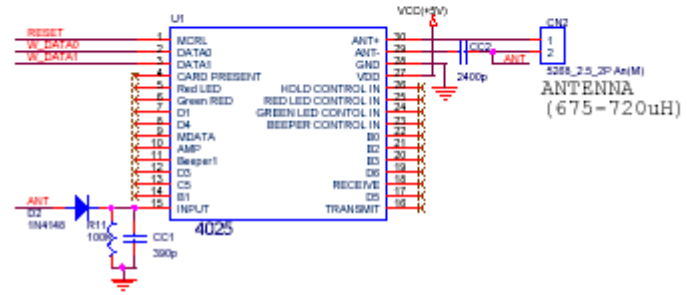
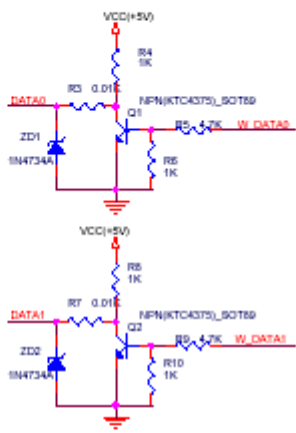
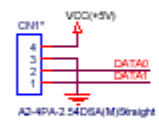
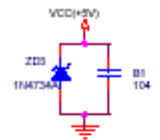
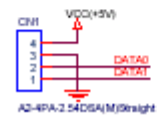
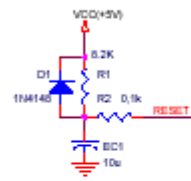


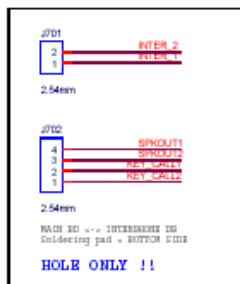
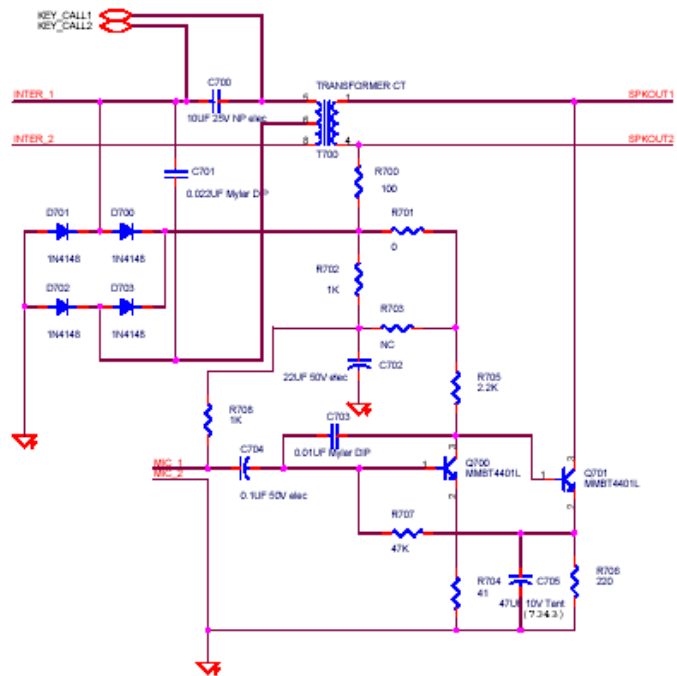
RESET & RTC & BUZ & RELAY		
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Sheet 10	of 11	



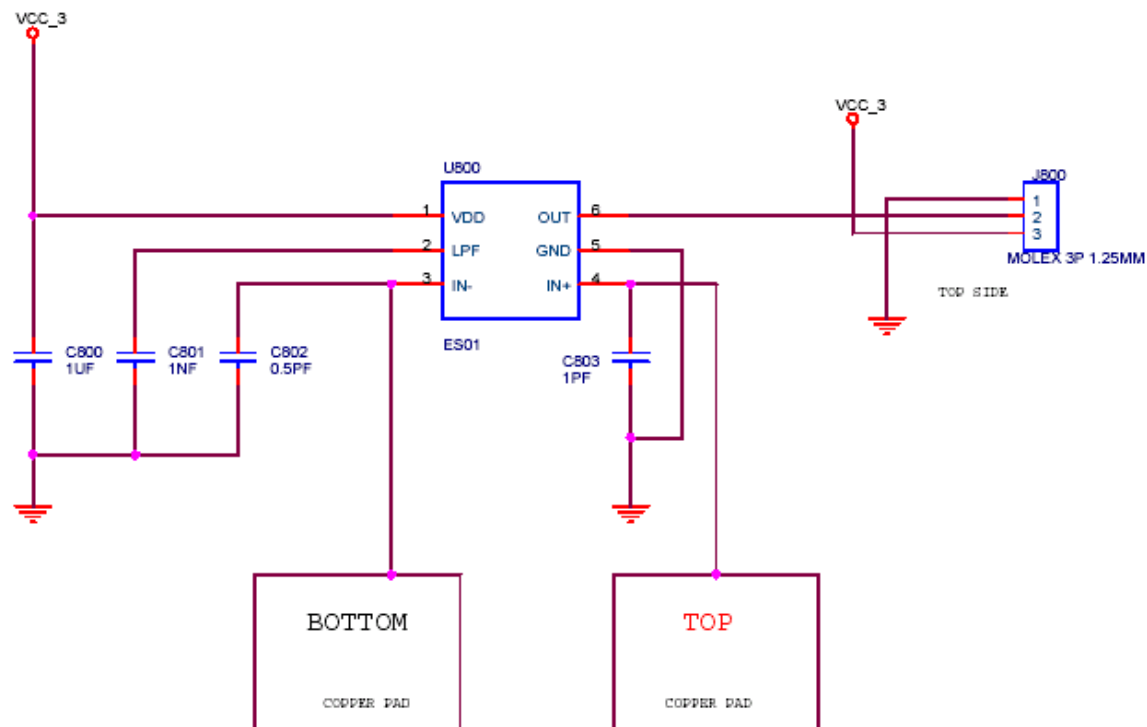
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Date:	Wednesday, June 04, 2003	Sheet 11 of 11

- RF





Title		
INTERPHONE		
Size	Document Number	Rev
B	<Doc>	1.0
Date	Thursday, June 15, 2006	Sheet 1 of 1



Top View

Title		
AUTO-ON B/D		
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