AVP-2000B Circuit Description 1.



Digital Logic Part

In Logic Part, The Main Processor is comfiguration of T2U, Memory and LCD, Key Pad, Codec, PCMCIA Interface.

Voiced signal from MIC converts to Digital, and transfer 802.11b MAC. Signal from MAC converts to voice and SPFAN uses Speaker communicate.

RF MAC & BBP

FLASH 16Mbit

IEEE802.11b MAC/BBP, Data receive from Logic, converts to I/Q signal and uses RF terminal to transmit RF stage $\frac{1}{2}$ also receive from RF stage, converts I/Q signal to digital signal MAC.



It transmit from ANT connect through Power AMP and uses frequency range of 2.4GHz.

Without any compositions IF frequency, uses direct conversion.

Receive signals from ANT, it makes I/Q

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Board to Board connector

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and send out to MAC/BBP.

HOST INTERFACE

FAR MIC Phone

СIМ

78975JS WS polsnA

DC/DC Converter

Voltade

+1 8V