

5. DESCRIPTION OF RADIO CIRCUIT

5-1. Frequency synthesizer

Frequency synthesizer consists of VCO, loop filter and RFIC. RFIC includes MIX, IF, Compander and PLL. Frequency synthesizer uses the PLL of the RFIC.

a) VCO

VCO is composed of ONE VCO

Oscillation circuit takes Colpitts circuit using variable Diode. The VCO generates GMRS frequencies and consists of R3, D5, C60, L13, C64, C58, R9, R7, R4, C56, C22, R8, Q3, Q8, L11, C120, R44.

VCO control voltage through loop filter adjusts frequency and Microphone Signal through Modulation terminal makes modulation.

$N = VCO \text{ oscillation frequency} / \text{reference frequency}$

If the desired frequency is 462.5625 MHz

1) TX : $N = 462.5625 \text{ MHz} / 0.00625 \text{ MHz} = 74010$

2) RX : $N = [462.5625 \text{ MHz} - 10.7 \text{ MHz}] / 0.00625 \text{ MHz} = 72298$

b) PLL IC

RFIC(PLL) is adjustable IC to produce the wished frequency which VCO provides through loop filter. It has internal counter using 10.25MHz.

reference frequency to make 6.25kHz as reference Signal. VCO frequency from prescaled input is divided signal is compared with Reference signal phase in phase comparator. Built-in charge pump changes voltage (until two signals are in phase) and charged voltage supplies VCO through loop filter to produce the desired frequency.

Frequency data associated with channel goes to PLL IC by CPU through CLOCK, DATA. PLL IC enables by strobe line of CPU.

c) Loop Filter

Loop filter is composed of C37, R77, C71, C66, R19, C65 and Change spulse from U3.46 to DC. And eliminates harmonic component in Pulse. It helps VCO oscillate clearly as DC voltage is supplied into Varicap.

5-2. Receiver

This is composed of Dual Conversion Super Heterodyne. First IF is 10.7MHz.

Local oscillator frequency is lower in 1'st IF than Rx frequency.

It is called low side injection. Second IF is 450kHz. 2'nd local oscillator Frequency comes to 10.25MHz.

a) Rx / Tx Conversion Circuit

Rx signal goes to Rx / Tx conversion circuit through FIXED antenna connector, low pass filter (C1, L9, C2, C9, L1, C75, L10) and receiver resonance circuit composed of L4, C21. When transmitting,

Voltage through R1, D1 supplies D2 of receive input is short and

Tx is on condition. When PIN diode is off in condition of Rx, C87 resonate serially and make impedance matching at receiver bandpass filter. (C43, L14,

C38,C40,C32,C30,L5)

b) Front End

Front-end has Q6 to provide a high sensitivity and low noise feature.

It employs band pass filter to eliminate image frequency and to produce enough pass band by Q6 input and output.

c) 1st Mixer

The receiver which has been amplifier in the RF front-end is provided to the base of the 1st mixer Q7. The 1st L/O signal provide from the VCO is supplied to the emitter of Q7 and Converted to the 1st IF 10.7 MHz

d) 1st IF Filter

The signal covered by Q7 to 10.7 MHz, the 1st frequency, change its impedance through C48, and then is infused to the fundamental Ceramic Filter which has the center frequency of 10.7 MHz and the width of +/- 30KHz. Here, the signal reduces the image and other unwanted signal for the 2nd IF, and changes its impedance again through the R24. This filtered signal flows in the 1st IF amplifier of RFIC(U2).

e) 2nd Mixer, and IF, FM Detect (U2)

The receiver IF signal of 10.7 MHz, which has been infused to U3 is mixed with the 2nd L/O converted to 450 KHz, the 2nd IF frequency. The receiver signal converted to the 2nd IF signal frequency passed through the FL3, the ceramic filter of 450 kHz again.

The squelch circuit is composed to detect the noises from the received signal demodulate in the 40th pin of the U3. For this purpose, the noise filter is using the OP amplifier inside the

f) Audio Amplifier(U10)

Demodulated audio signal enters to pin4 of AF IC (U10).

It comes out to pin5,8 then, it reaches at speaker.

5-3. Transmitt

When Tx develops with pressing PTT switch, VCO output amplifies through Q11,Q2,Q1,Q12,Q14 transmits by antenna through low pass filter.

Tx RF signal produced from Tx VCO is amplified by Buffer Q11, Driver Q2 through C11,R25, C14 and entered Q1,Q12,Q14 Power Transistor input terminal with final amplification. After this stage, the signal is emitted at

antenna through 50 matching circuit to low pass filter (C1,C2,L9,C9,L1,C75 ,L10) to eliminate harmonic.

a) Audio Modulation and Audio Amplification

Audio signal produced by external or internal microphone, limits amplification, low pass filter by R35,C80,R33,C79,R25,C95.

Max. Frequency modulation deviation is adjusted by U3,VR1 keeps noise and audio from entering to VCO at time of Tx. Audio modulation and Audio

Amplification has characteristic of 6 /OCT pre-emphasis by R35,C80,R33,