



March 6, 2007

Mr. Richard Tseng
Federal Communications Commission
7435 Oakland Mills Road
Columbia, Maryland 21046

RE: FCC ID: PD9LEN4965AG
Applicant: Intel Corporation
Correspondence Reference Number: 36498
731 Confirmation Number: TC805512
Date of Original Email: 02/28/2007

Subject: Quick Review

Dear Mr. Tseng:

Please refer to responses below each question:

1) Please submit expanded plots for the channel transmission closing time demonstrating that the device vacates the channel in the required 200 ms. these plots should not have a sweep greater than 600 ms.

Answer from CCS: The requirement for a 600 msec sweep time plot was presented in material referenced in an email announcement from admin@tcbcouncil.org sent on February 27, 2007. This email also included instructions and the password required to download these materials. Attendees at this TCBC training were informed of this requirement during the week of February 22, 2007. The Grant was issued on February 9, 2007, which is prior to either of these dates.

We do not believe that it is appropriate to retroactively apply the 600 msec requirement. We will gladly comply with the additional reporting requirement for all applications submitted on or after February 28, 2007.

We further believe that the existing plots are sufficient to demonstrate compliance with the 200 msec closing time requirement for this particular device, as explained by our response to the questions that were asked prior to issuing the Grant.

2) The device has multiple antenna ports. Can all ports be used at the same time? How do the configurations impact the antenna gain?

Answer: No, this product does not utilize multiple TX ports at the same time. Only 2 antenna connectors (connectors 1 & 2) are on the TX chain and the 3 antenna connectors are on the Rx chain (connectors 1,2 & 3). Tx would operate in diversity mode on this product.

3) All modes of operation must be tested. The DFS test report only conducted tests in 20 MHz. The approved master device used in the test does not have 40 MHz operation. Please submit updated information on the 40 MHz BW operation.

Answer: This product does not include 20 or 40MHz channel bandwidth modes, these are disabled in both the 2.4 and 5GHz frequency in the EEPROM and can not be altered by the end user.

4) Can you clarify how users are prevented from disabling DFS and/or transmitting in frequencies not authorized in United States?

Answer: This information is programmed into the EEPROM and the end user does not have access to the EEPROM and can not override this function.

Please let us know if there are any further questions and/or comments.

Sincerely,

Marissa Faustino
Compliance Engineer
Intel Corporation

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