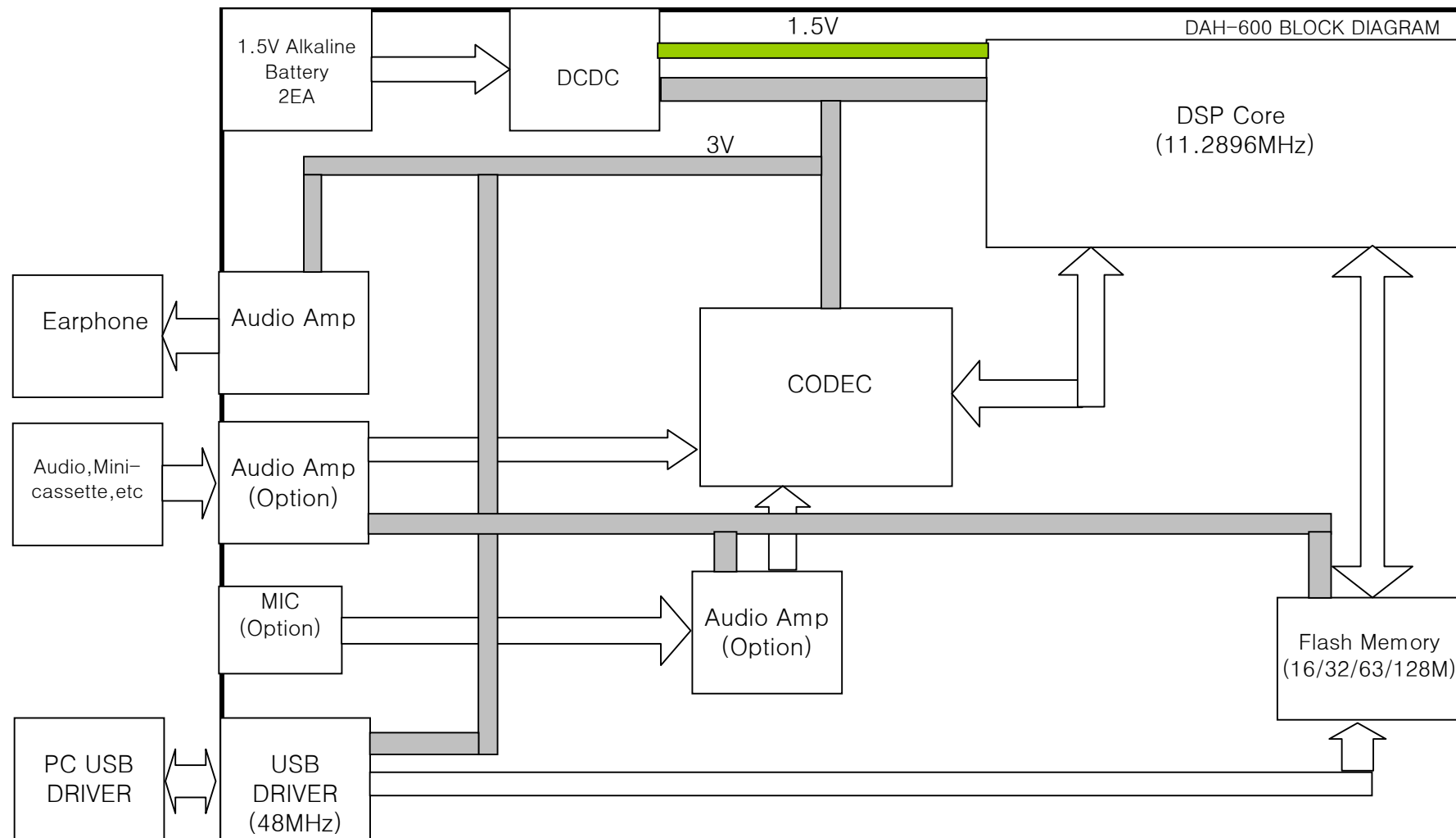


APPENDIX D
: BLOCK DIAGRAM

DAH-600 BLOCK DIAGRAM



D e s c r i p t i o n o f C i r c u i t F u n c t i o n & D e v i c e O p e r a t i o n

1. DC-DC Converter

- 1) This circuit performs DC to DC conversion.
- 2) The circuit converts 1.8V charged voltage (in battery) to 3V Vcc.

2. PC Interface

- 1) Personal computer have the extracted data file.
- 2) data files are saved flash memory by USB(Universal Serial Bus) type connection, using the software by Hyun Won Inc.

3. Data Storage

- 1) This model has a flash memory(32/64/128/256MB), in capable of the extracted data files.

4. DSP (Digital Signal Processor)

- 1) This circuit is DSP unit, processing digital signal.
- 2) Basic clock speed (on normal mode) is 11.2896 MHz.

5. CODEC

- 1) This circuit converts decoded digital to analog signal.

6. Amplifier

- 1) This circuit is a amplifier, matching impedance of output signal with impedance of earphone.

7. Operation Frequency

- 1) Output frequency (Analog signal) : 20 – 20kHz
- 2) CPU clock speed (Digital signal) : 11.2896 MHz

8. Voice recording

- 1) This circuit is amplifier of Human voice and other sound from microphone.

9. Direct Encoding

- 1) This circuit is amplifier, using direct encoding cable from other music device.