

Figure 6: View of Unit Side With A7 TAWS Processor Showing

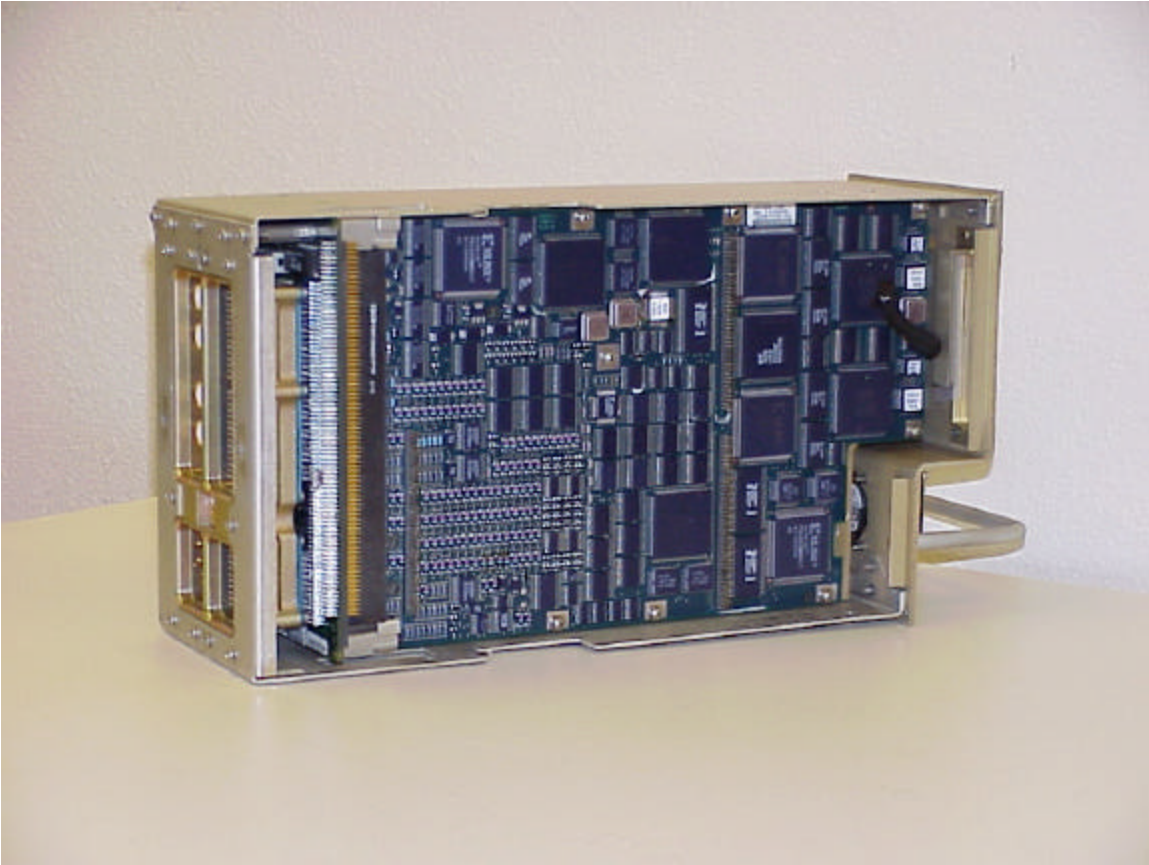


Figure 7: View of Unit Side, A7 TAWS Removed, A2 Processor Assembly Showing

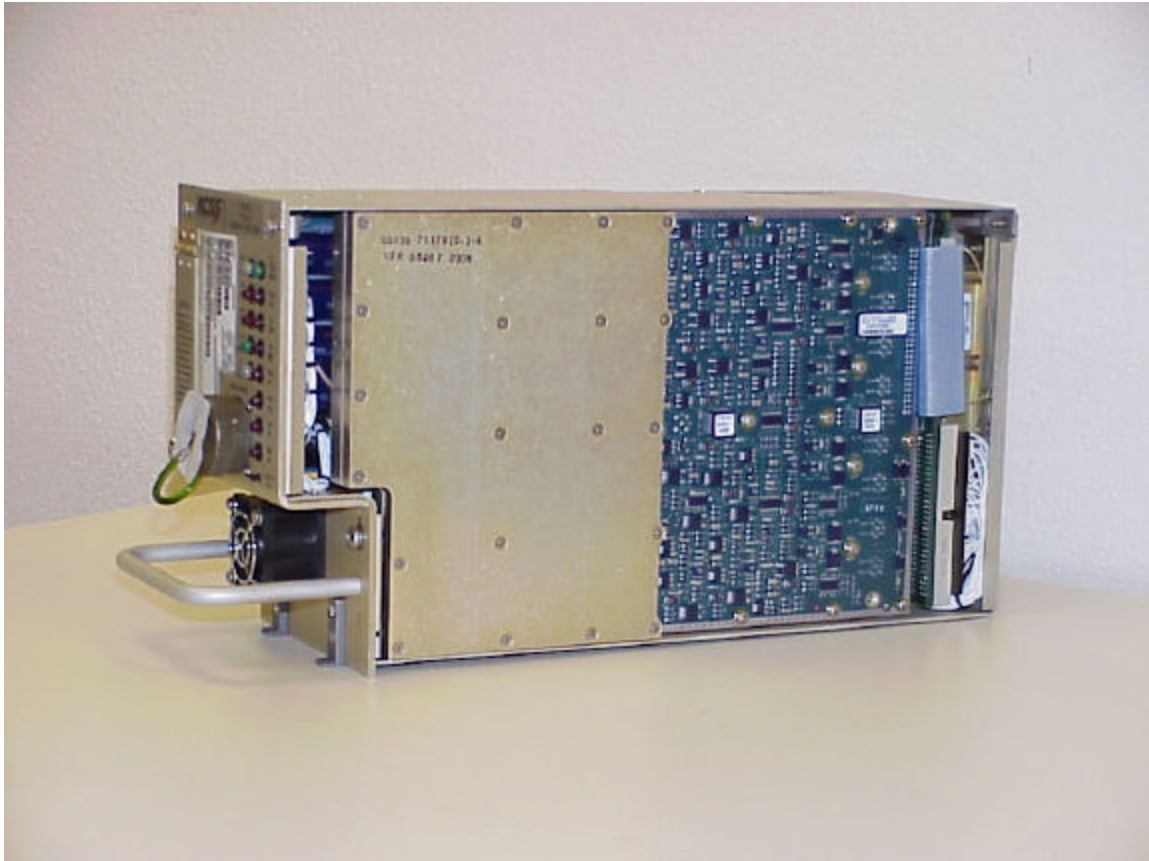


Figure 8: View of Unit Side, Showing A5 Receiver - I/O Assembly with Shield

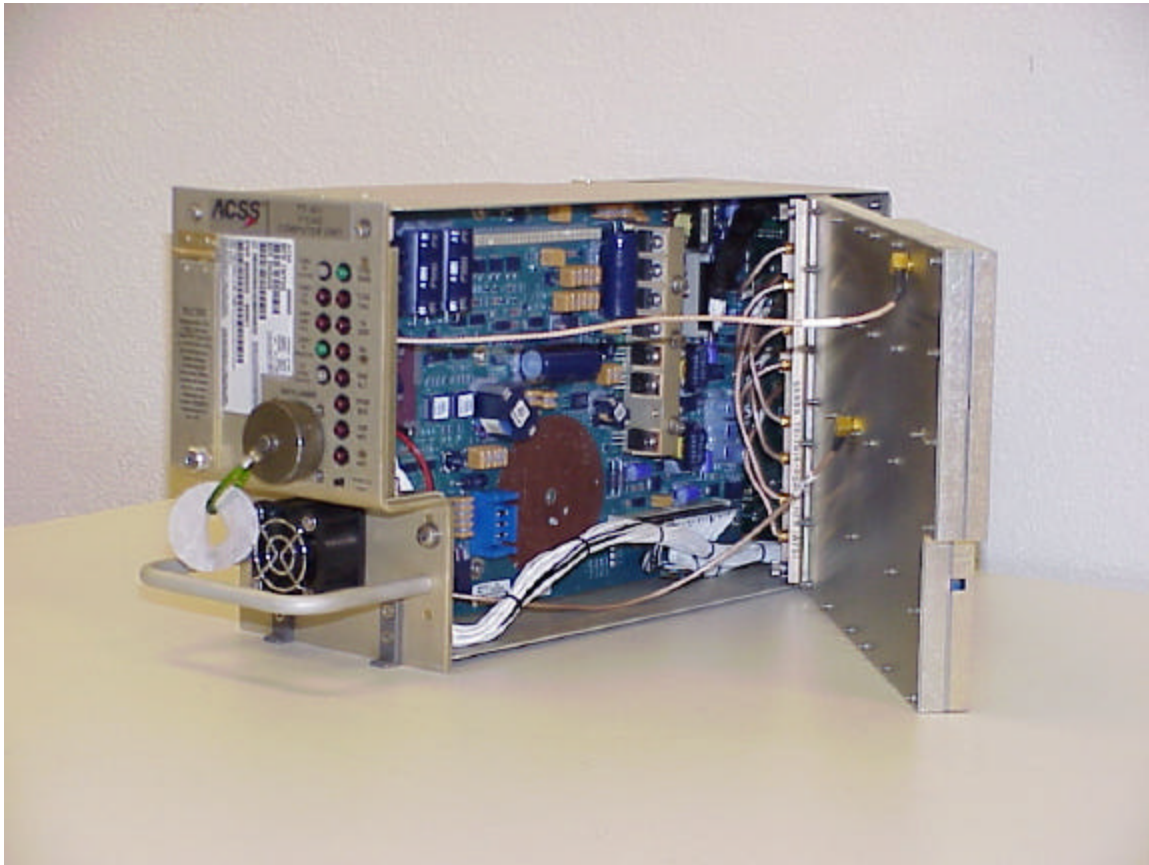


Figure 9: View of Unit Side With A5 Receiver Hinged Outward, Showing A4 Power Supply/Modulator Assembly

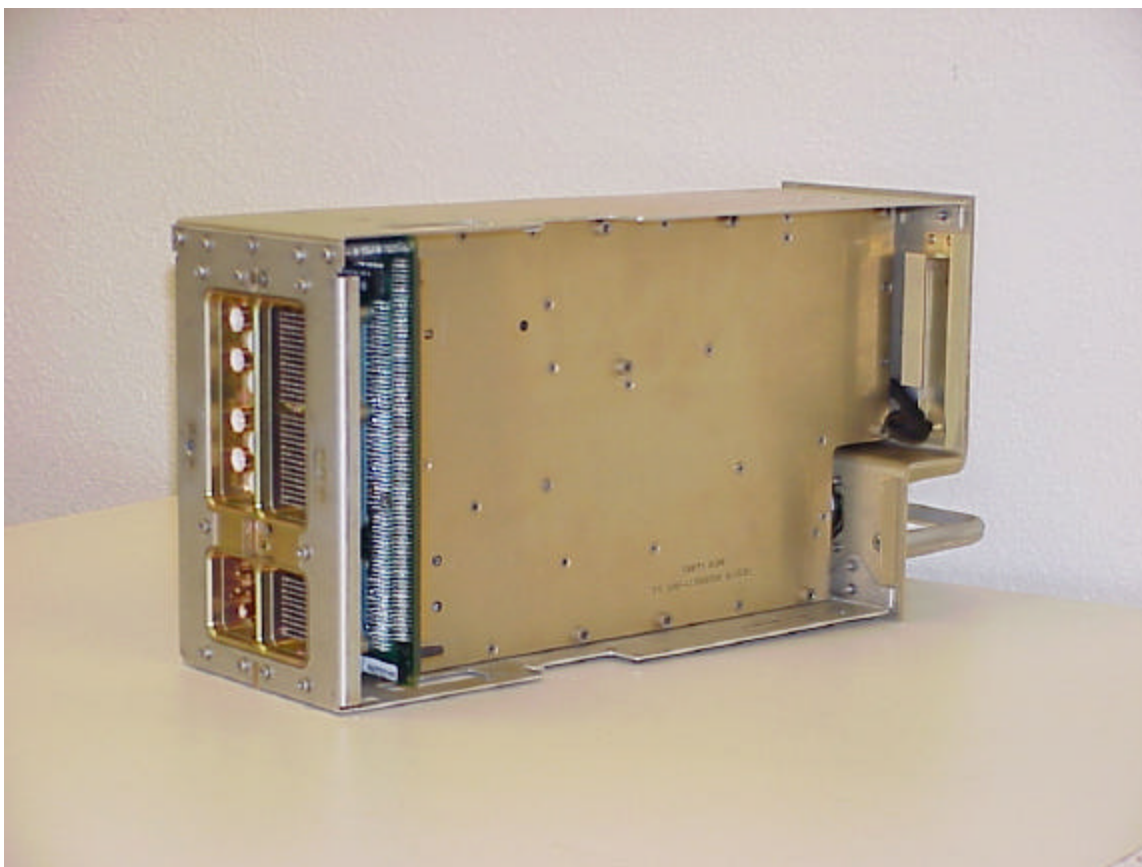


Figure 10: View of Unit Side with A2 Processor Removed Showing A3 Transmitter Assembly With Cover

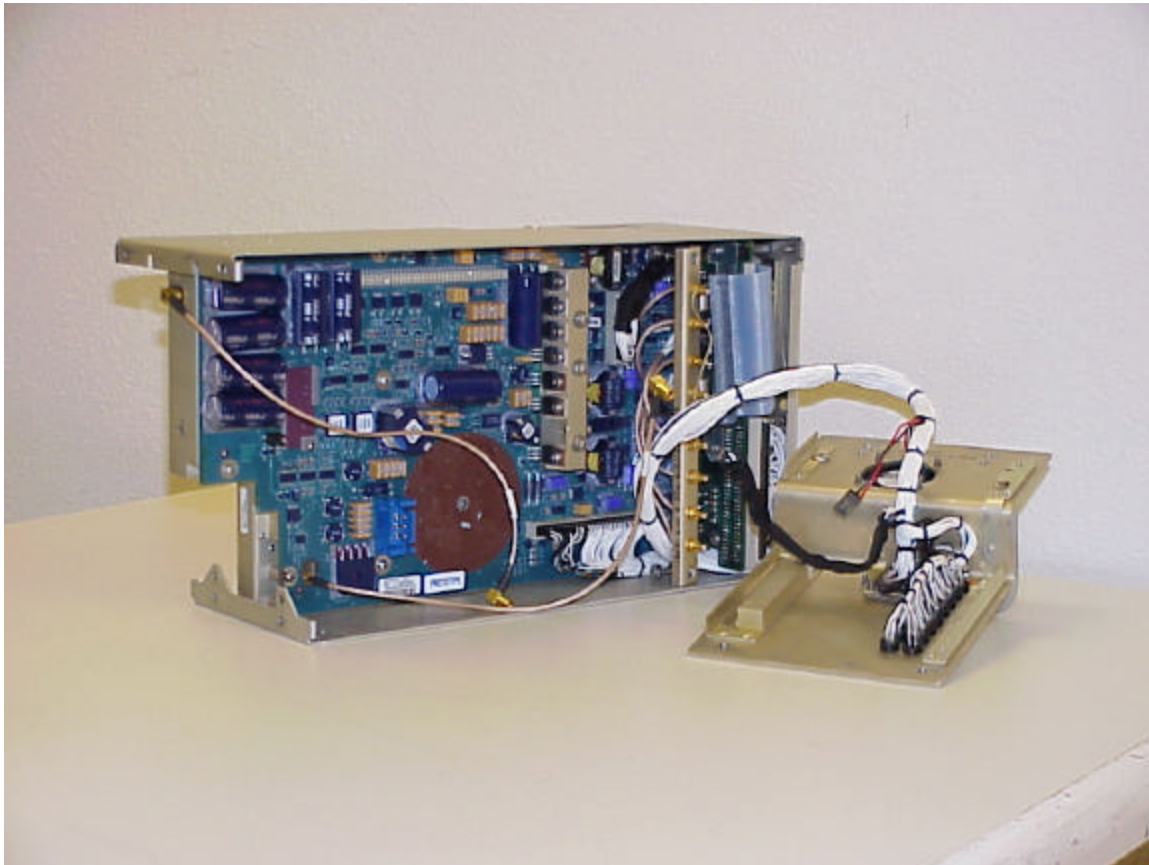


Figure 11: View of Unit Side, A5 Receiver and Front Panel Removed, Showing A4 Power Supply/Modulator Assembly

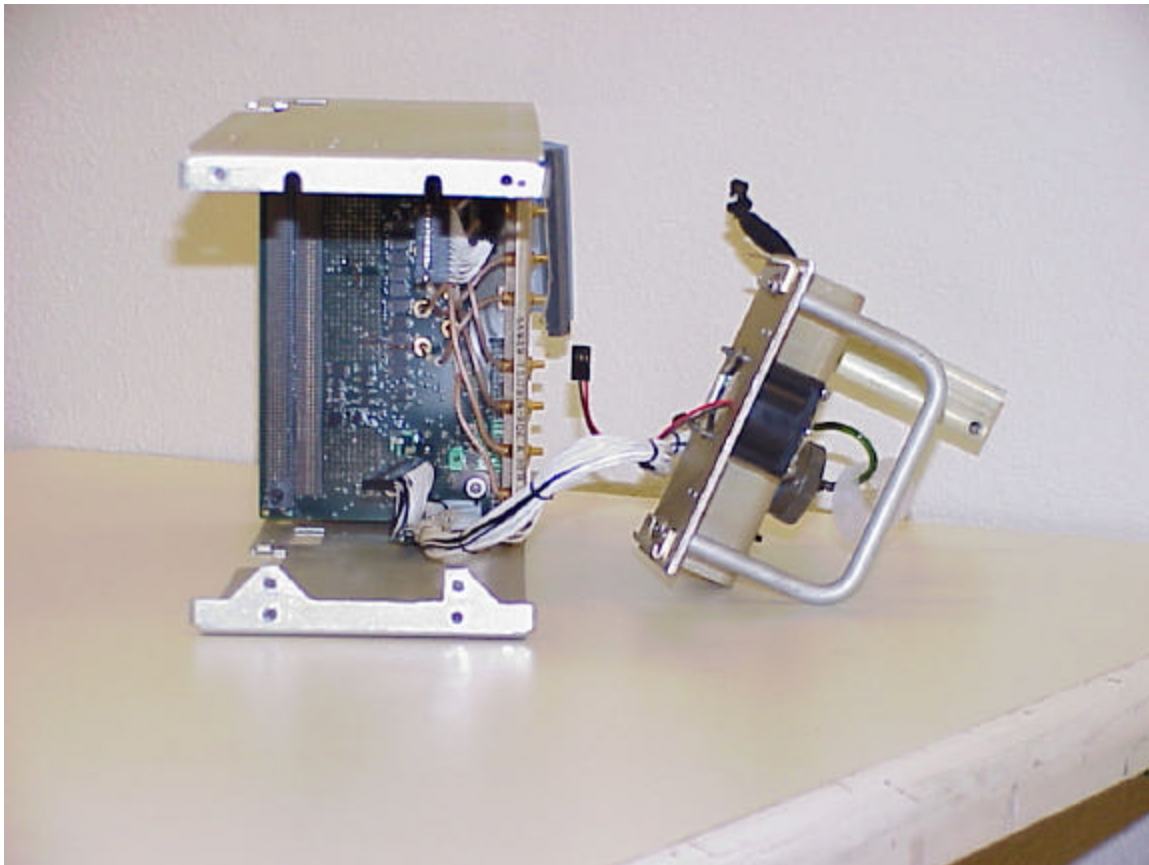


Figure 12: View of Unit Side, A4 Power Supply/Modulator Assembly Removed, Showing A1 Interconnect Assembly

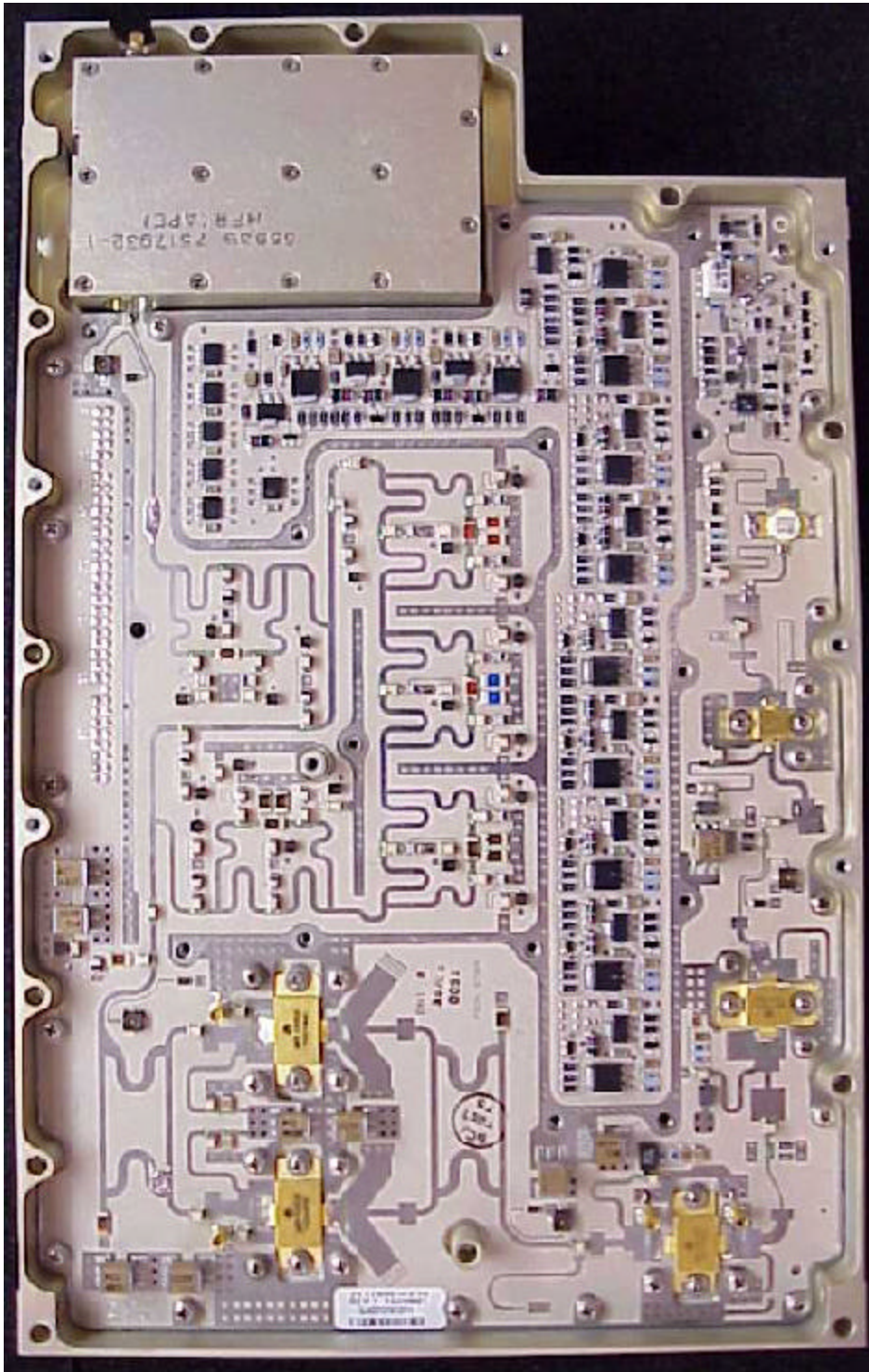


Figure 13: View of A3 Transmitter Assembly With Shield Removed

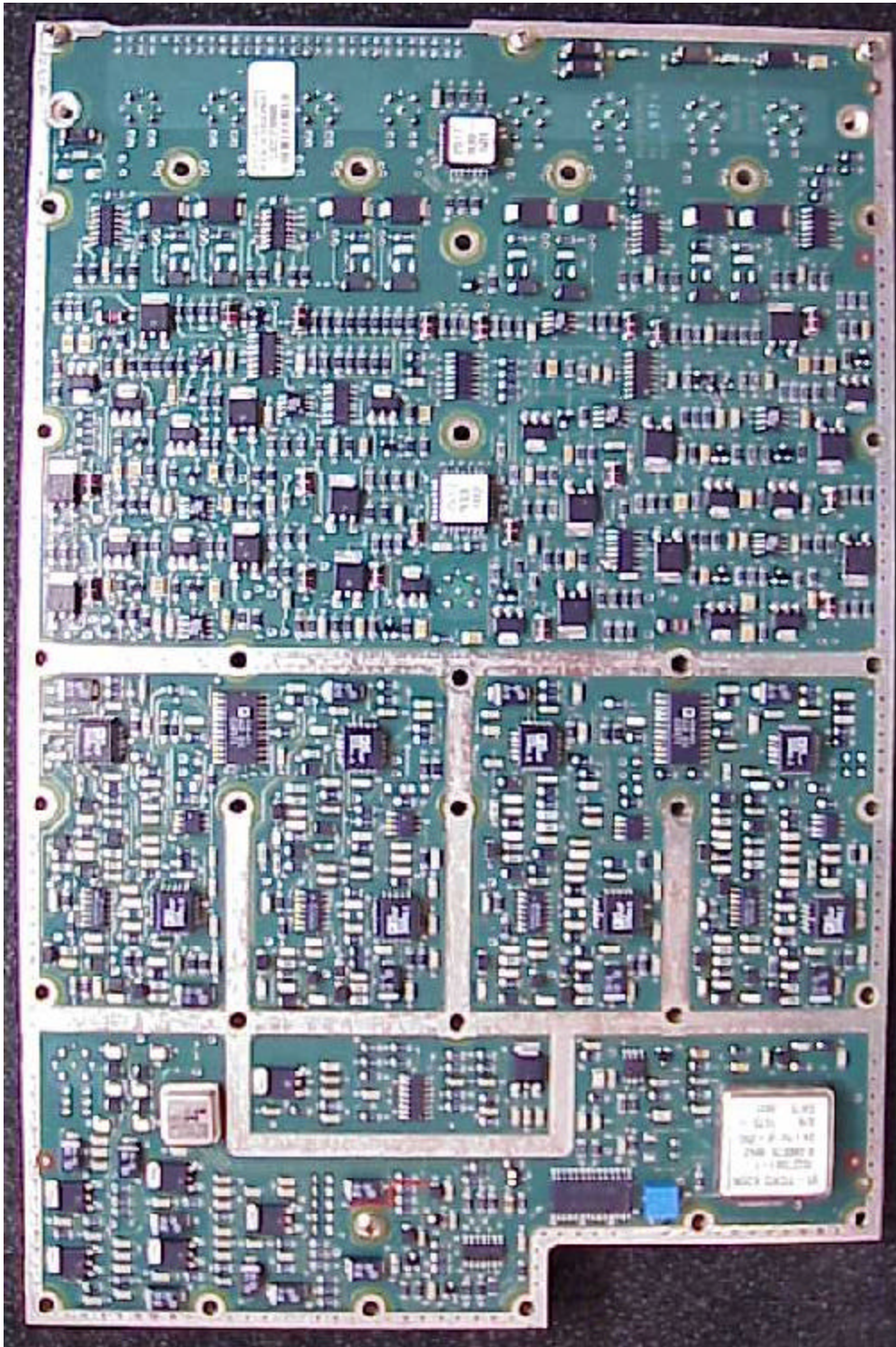


Figure 14: View of A5 Receiver – I/O Assembly with Shield Removed. Side A

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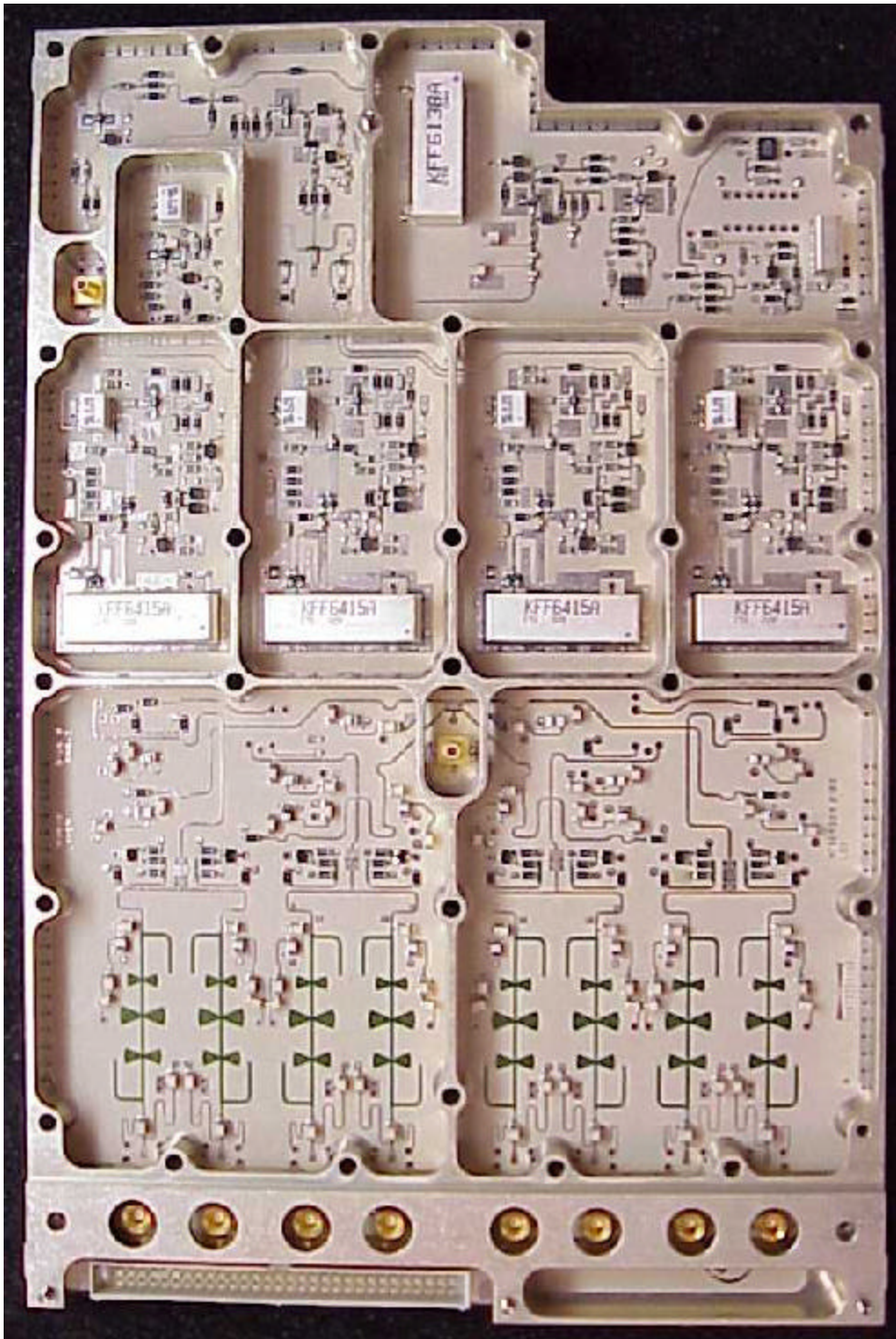


Figure 15: View of A5 Receiver – I/O Assembly with Cover Removed. Side B

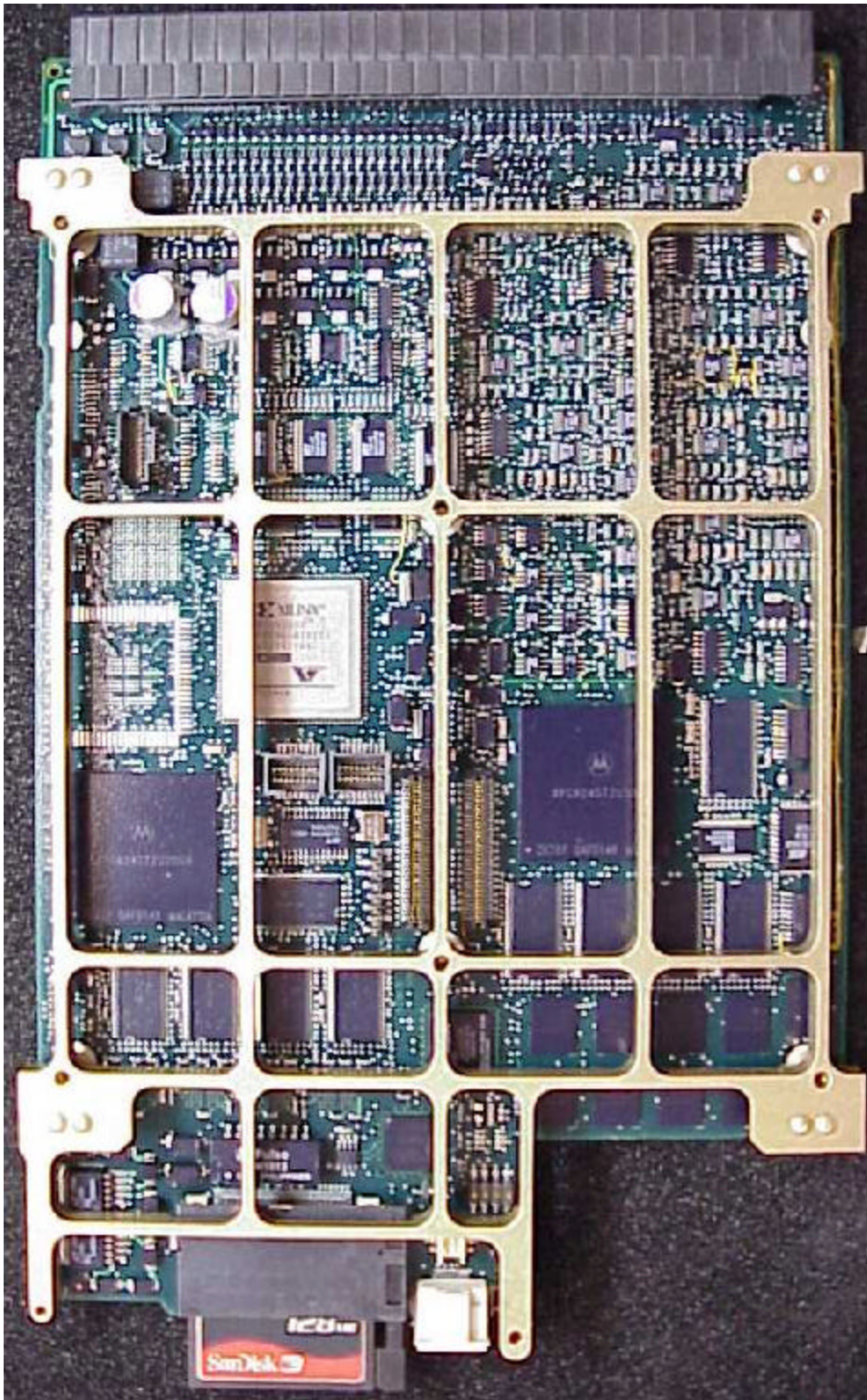


Figure 16: View of A7 TAWS Processor With Bracket Attached

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