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NOTICE
 FREEDOM OF INFORMATION ACT (5 USC 552) AND
 DISCLOSURE OF CONFIDENTIAL INFORMATION GENERALLY
 (18 USC 1905)
 THIS DOCUMENT IS BEING FURNISHED IN CONFIDENCE BY HONEYWELL
 INC. THE INFORMATION DISCLOSED HEREIN FALLS WITHIN EXEMPTION
 (b) (4) OF 5 USC 552 AND THE PROHIBITIONS OF 18 USC 1905.

ZONE	REV	DESCRIPTION	DATE	APPROVED
		INITIAL RELEASE	10MAR93	T. SCHAFER
A		SH IDX REV. PL AFFECTED. USE ALL PARTS IN HOUSE, IN STOCK AND CURRENTLY ON ORDER (EFFECTIVITY IS NEXT BUY).	62698(U) 16DEC93	J. TROXEL
B		SH IDX REV. PL AFFECTED. AFFECTS PARTS IN-HOUSE, IN STOCK AND IN KITS NOT YET IN-BUILD.	89221(M) 20JUN95	M. HOWK M. BROWN
C		SH IDX REV. PL AFFECTED. USE ALL PARTS IN-HOUSE, IN STOCK AND CURRENTLY ON ORDER (EFFECTIVITY IS NEXT BUY).	89413(U) 8JUL98	M. HOWK R. RICHARDS
D		NOTES: NOTE B WAS SECURE C91 TO --- EDGE. SH IDX REV. PL AFFECTED. USE ALL PARTS IN-HOUSE AND IN STOCK (EFFECTIVITY IS ALL OPEN P.O.'S).	78292(U) 6AUG98	M. HOWK R. RICHARDS L. WHISLER
E		SH IDX REV. PL AFFECTED. USE ALL PARTS IN-HOUSE, IN STOCK AND CURRENTLY ON ORDER (EFFECTIVITY IS NEXT BUY).	101867(U) 4NOV98	M. HOWK M. BROWN R. RICHARDS
F		NOTES: NOTE B WAS SECURE ---C91---EDGE. SH IDX REV. PL NOT AFFECTED. USE ALL PARTS IN-HOUSE AND IN STOCK (EFFECTIVITY IS ALL OPEN P.O.'S).	81280(U) 1FEB99	J. ELLER M. CARSON R. RICHARDS
G		SH IDX REV. PL AFFECTED. USE ALL PARTS IN-HOUSE, IN STOCK AND CURRENTLY ON ORDER (EFFECTIVITY IS NEXT BUY).	104112(U) 09NOV99	M. HOWK L. WHISLER J. OLSON
H		NOTES: NOTE B WAS SECURE ---C34, C48, ---EDGE. SH IDX REV. PL NOT AFFECTED. USE ALL PARTS IN-HOUSE, IN STOCK AND CURRENTLY ON ORDER (EFFECTIVITY IS NEXT BUY).	24341(U) 06FEB01	M. PORTS R. RICHARDS J. OLSON
J		NOTES: ADD NOTED 12. SH IDX REV. PL NOT AFFECTED. AFFECTS ONLY ASSEMBLIES NOT YET IN BUILD AS SOON AS MANUFACTURING DOCUMENTATION IS AVAILABLE.	26887(M) JUL 01	M. A. [Signature] R. RICHARDS

NOTES:

- ASSEMBLE AND SOLDER PER ITEM 701.
- THIS ASSEMBLY INCLUDES COMPONENTS WHICH ARE SUBJECT TO DAMAGE BY ELECTROSTATIC CHARGES; THEREFORE, ALL COMPONENTS SHALL BE HANDLED IN ACCORDANCE WITH GUIDELINES FOR ELECTROSTATIC DISCHARGE CONTROL.

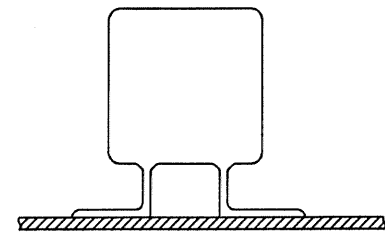
CAUTION
 ELECTROSTATIC SENSITIVE COMPONENTS
- MAXIMUM COMPONENT HEIGHT SIDE B SHALL BE:

	AREA A AND D	AREA B	AREA C	ALL OTHER AREAS
CONDUCTIVE CASE	.200	.065	.300	.480
NONCONDUCTIVE OR GROUND CASE	.200	.115	.300	.530
- COMPONENT LEAD PROTRUSION SHALL BE .060 MAXIMUM; BARE COPPER PERMISSIBLE ON COMPONENT LEADS AT TRIMMED SURFACES.
- SQUARE PADS ON PRINTED WIRING BOARD INDICATE PIN 1.
- COAT CCA USING ITEM 702 (CONFORMAL COAT) PER M7010975, EXCEPT J2 AND J3 SOCKETS, PINS OF J1 AND J1 PIN CAVITY (ON SIDE B) MOUNTING HOLES OF Q17, Q18, Q19, AND MOUNTING HOLE METALIZATION OF PWB SIDE A AND B (7 PLACES). COATING ON C90 AND C91 IS OPTIONAL.
- SOME COMPONENTS ARE SHOWN OVERSIZED TO PROVIDE SPACE FOR REFERENCE DESIGNATORS.
- SECURE C24, C25, C34, C47, C48, C60, C62, C66, C70, C90, AND C91, TO ITEM 1 (PWB) USING ITEM 703 (RTV). ITEM 703 NOT TO EXTEND PAST PWB EDGE.
- BEFORE CLEANING CAPACITORS 7000041-XXX (C90 AND C91) REFER TO COMPONENT SPEC FOR ACCEPTABLE CLEANING SOLVENTS.
- SHADED COMPONENTS NOT INSTALLED.
- C91 AND C90 MAY BE INSTALLED AT NEXT HIGHER ASSEMBLY FOR EASE OF TESTING.
- NOTE TO MANUFACTURING:
 COMPONENT U9 SHOULD BE HAND SOLDERED. IT SHOULD NOT BE CLEANED IN AN AQUEOUS WASH.

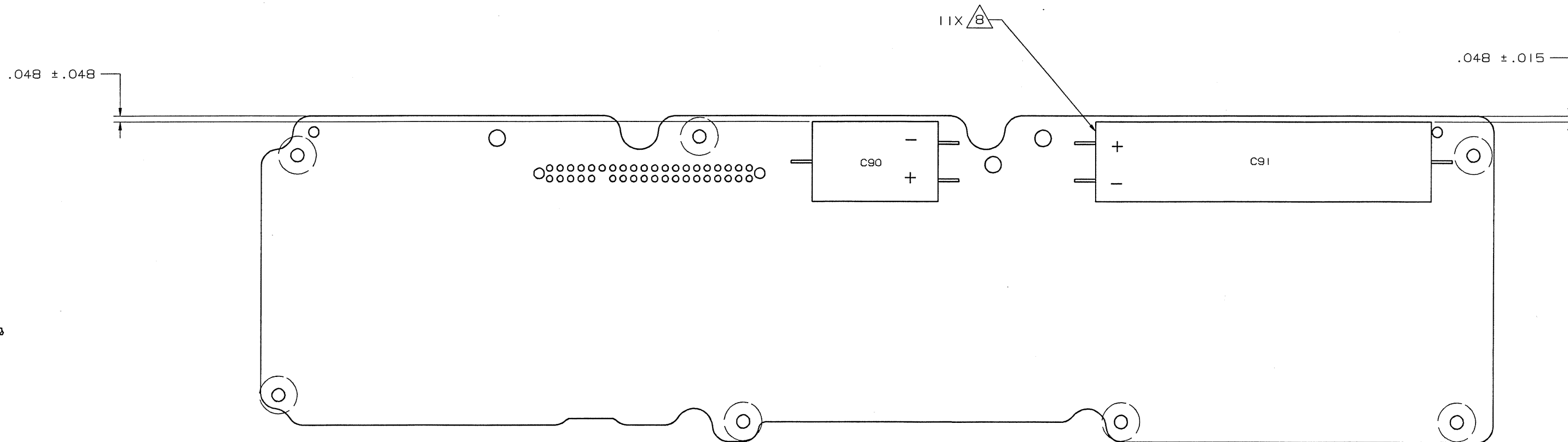
REV STATUS OF PARTS LIST SHALL BE SAME AS BASIC DRAWING NUMBER SEE SEPARATE PARTS LIST

CONTRACT XS-852 DRAWN BY E. THIESSEN DATE 17FEB93 DESIGNED BY CHECKED BY DATE 17FEB93 APVD FOR MFG DATE 01MAY93 APVD FOR QE DATE 01MAY93 APVD FOR ENGRG DATE 22FEB93 APVD FOR ENGRG DATE		OPERATIONAL NOTE SIMILAR ASSEMBLY Honeywell COMMERCIAL FLIGHT SYSTEMS GROUP PHOENIX, ARIZONA TITLE CIRCUIT CARD ASSEMBLY-PWR SPLY/MODULATOR, A6 SIZE/CAGE CODE E 15939 DRAWING NO. 7517470-903 SCALE/UNIT WT SHEET 1 OF 15	
FOR HONEYWELL REFERENCE MFG REF NO. CPA NO. A-13455 PRODUCT LINE NO 3834 AW/PS ANALYSIS EB 7510800 REF DWG REF SPEC		INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994 UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES; TOLERANCES: ANGLES ± 2° 2 PL DEC ± .01 3 PL DEC ± .005 MATERIAL, FINISH, SEE QQ.40. ITEM CODE (PER 8699384) THIRD ANGLE PROJECTION DWG CLASS A FIRST USED ON 7517400	
7517400 NEXT ASSY USED ON (SYS)		APPLICATION 7517400 USED ON (SYS)	

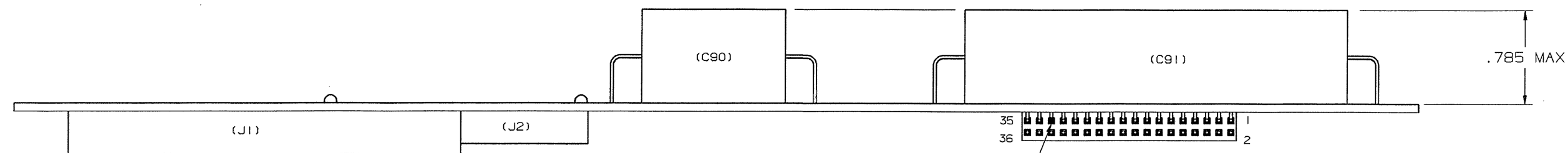
ZONE		REV	DESCRIPTION	DATE	APPROVED
A			UDTD VIEW. ADDED COMPONENTS R173, VR4, AND C501. REMOVED SHADING FROM CB6. 62699 (U)	16DEC93	J. TROKEL ECT
			SEE SHEET 1.		DA
D			UDTD VIEW. ADDED VIEW A-A. RELOCATED DIM. 1.000. 78292 (U)	6AUG98	M. HOWK R. RICHARDS L. WHISLER
			SEE SHEET 1.		
E			UDTD VIEW. CHANGE CASE SIZE OF C47. 101867 (U)	4NOV98	M. HOWK M. BROWN L. WHISLER
			SEE SHEET 1.		
F			UDTD VIEW. ADD IXX TO NOTE B CALLOUT. 81280 (U)	01FEB99	J. ELLER M. CARSON R. RICHARDS
			SEE SHEET 1.		
H			UDTD VIEW. NOTE B CALLOUT WAS IXX. UDTD TEXT ON VIEW A-A. 24341 (U)		<i>6 Feb 01</i> M. Howk R. Rich R. J. OLSON
			SEE SHEET 1.		



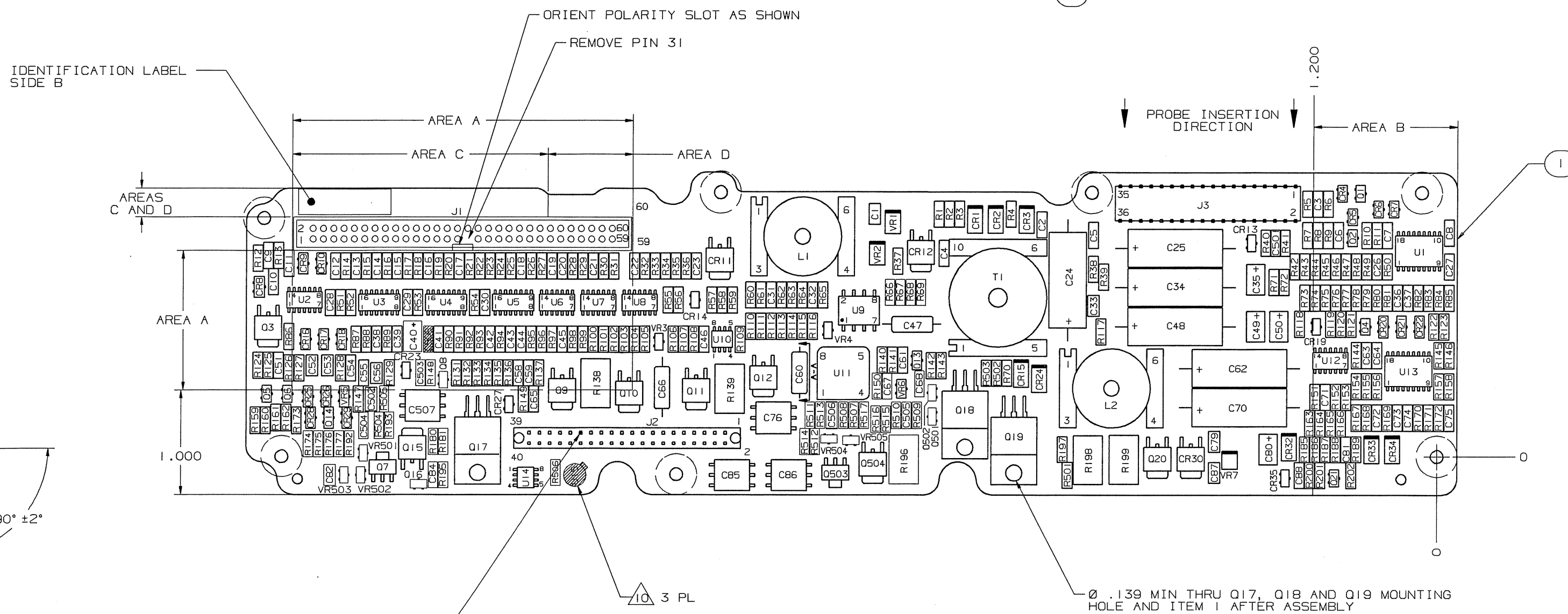
VIEW A-A
3X
(C47, C60 AND C66)



VIEWING SIDE A



J3-31 (2)

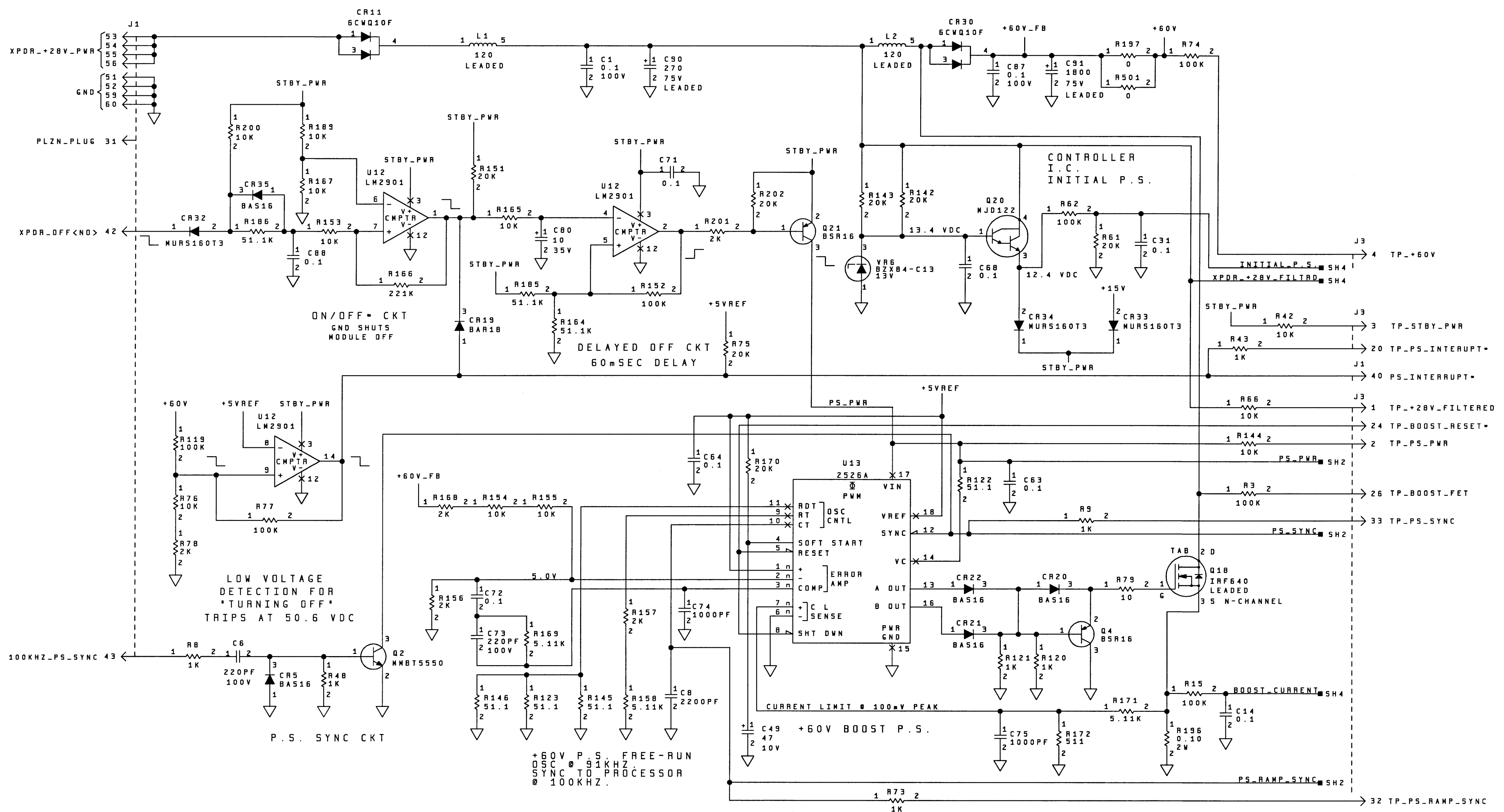


VIEWING SIDE B

THIS SURFACE OF J1 & J2 SHALL BE FLUSH TO WITHIN .015 TO SIDE B OF ITEM 1

Ø .139 MIN THRU Q17, Q18 AND Q19 MOUNTING HOLE AND ITEM 1 AFTER ASSEMBLY

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
C7	A	UDDT SCHEM. REF DES R48 VALUE WAS 5.11K. 62699(U1)	16 DEC 93	J. TROXEL T. N. N. DM
	E	PARTS LIST DATA BASE CHANGE ONLY. 101867(U)	1998	H. NOREAU
SEE DWG SHEET 1				



- NOTES:
 UNLESS OTHERWISE SPECIFIED:
 1. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 2. ALL RESISTANCE VALUES ARE IN OHMS AND ARE .25W, 1%.
 3. ALL INDUCTANCE VALUES ARE IN MICROHENRIES.
 4. ALL PARTS ARE SURFACE MOUNT.
 5. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS PREFIX PART DESIGNATION WITH SUBASSEMBLY DESIGNATION.
 6. INTERPRET DRAWING PER ANSI Y32.2-1975 AND ANSI/IEEE STD 991-1986. LOGIC SYMBOLS CONFORM TO ANSI/IEEE STD 91-1984.

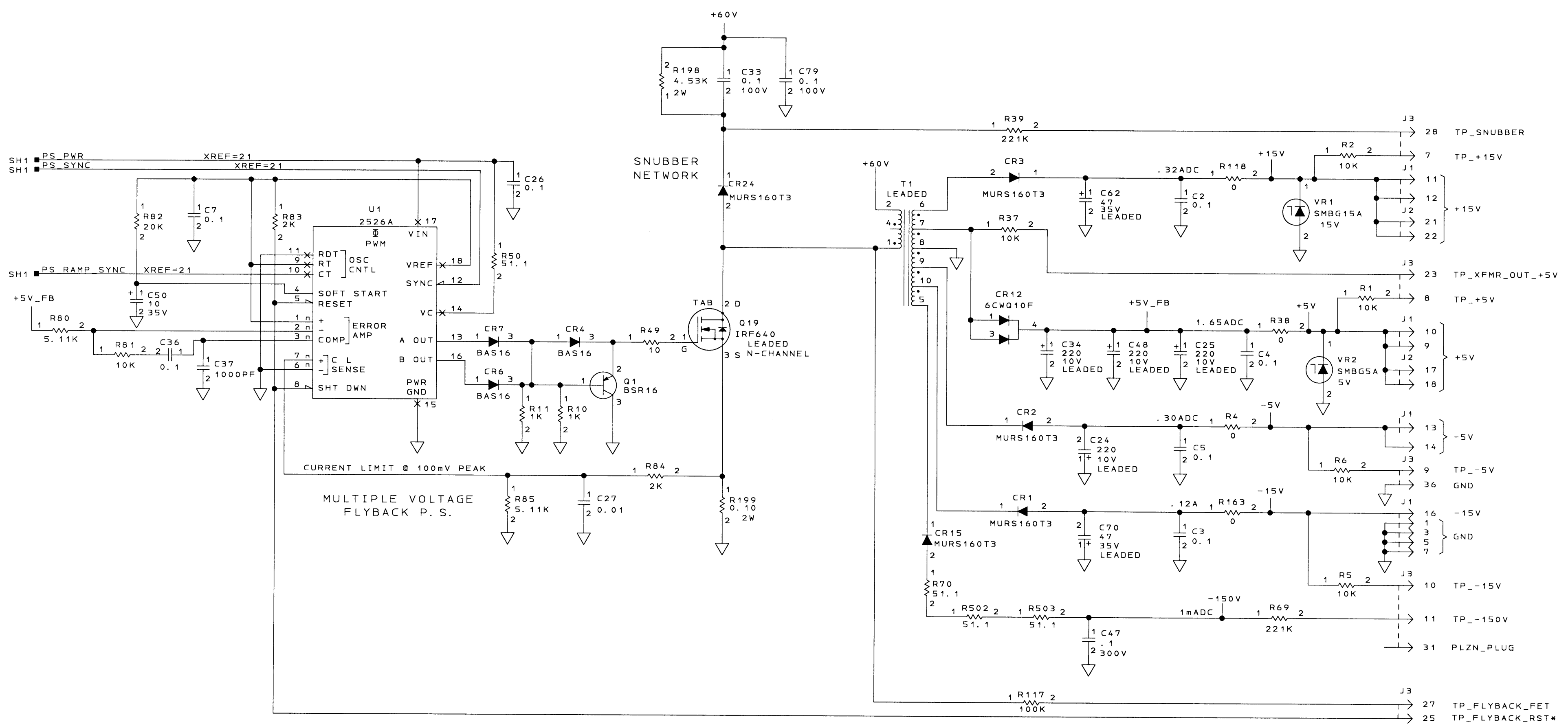
EMI FILTER / ON-OFF / 60V BOOST SUPPLY
 SCHEMATIC/BLOCK DIAGRAM SHEET NO. 1 OF 13

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE CASE CODE E 55939	DRAWING NO. 7517470-903	REV E
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7517470-903 (13)

ZONE		REV	DESCRIPTION	DATE	APPROVED
B			PARTS LIST DATA BASE CHANGE ONLY.	20 JUN 95	M. HOWK
			89221 (M)		M. BROWN
C			PARTS LIST DATA BASE CHANGE ONLY.	08 JUL 98	M. HOWK
			99413 (M)		R. RICHARDS
C3	E		UDTD PICTORIAL OF REF DES C47.	04 NOV 98	M. HOWK
			101867 (U)		M. BROWN
G			PARTS LIST DATA BASE CHANGE ONLY.	9 NOV 99	M. HOWK
			104112 (U)		R. RICHARDS



FLYBACK SUPPLY: +15V, +5V, -5V, -15V, -150V

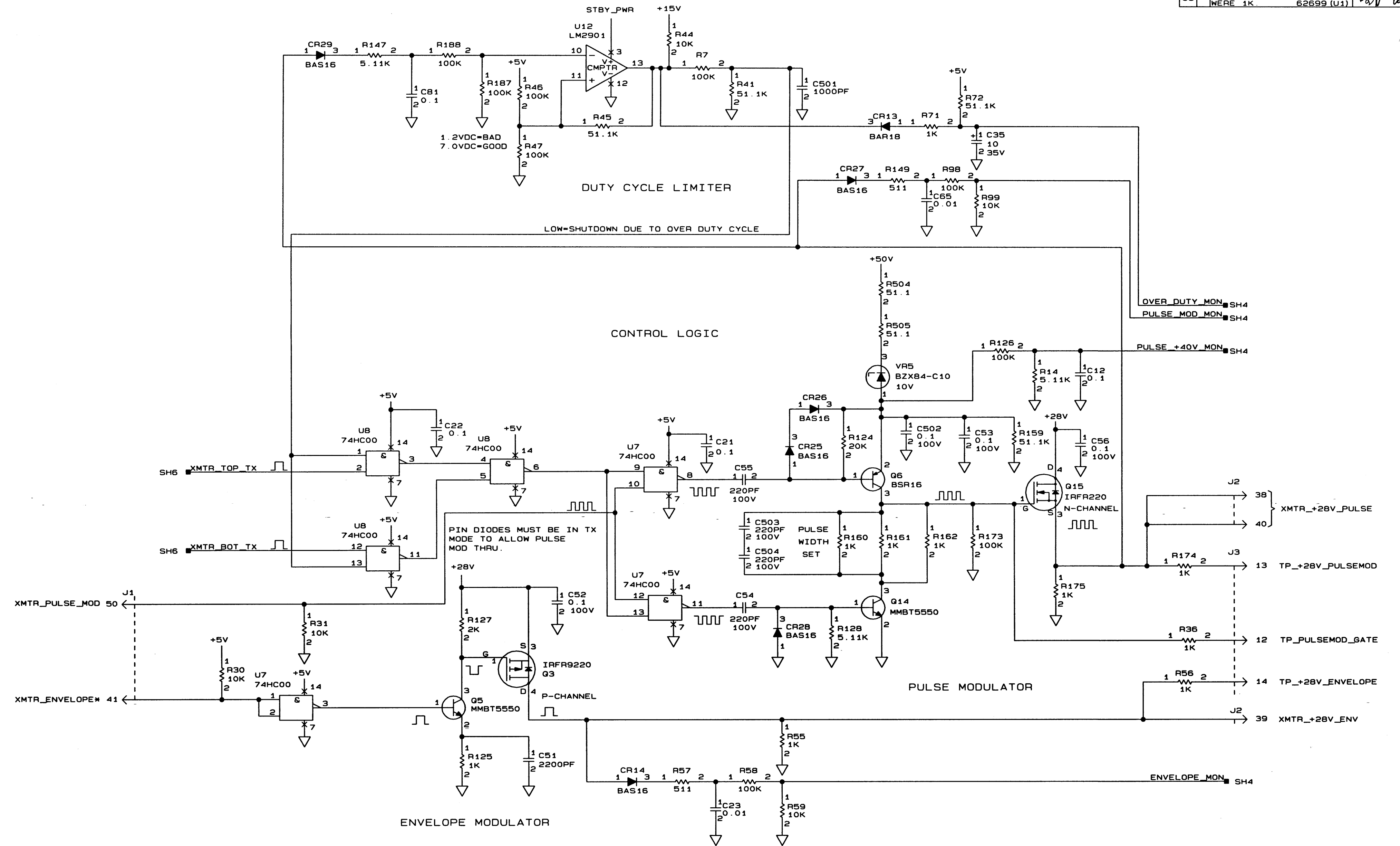
SCHEMATIC DIAGRAM SHEET NO. 2

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER, USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	E	CAGE CODE	55939	DRAWING NO.	7517470-903	REV	G
	SCALE			SHEET	4				

REV 7517470-903 SHEET 4

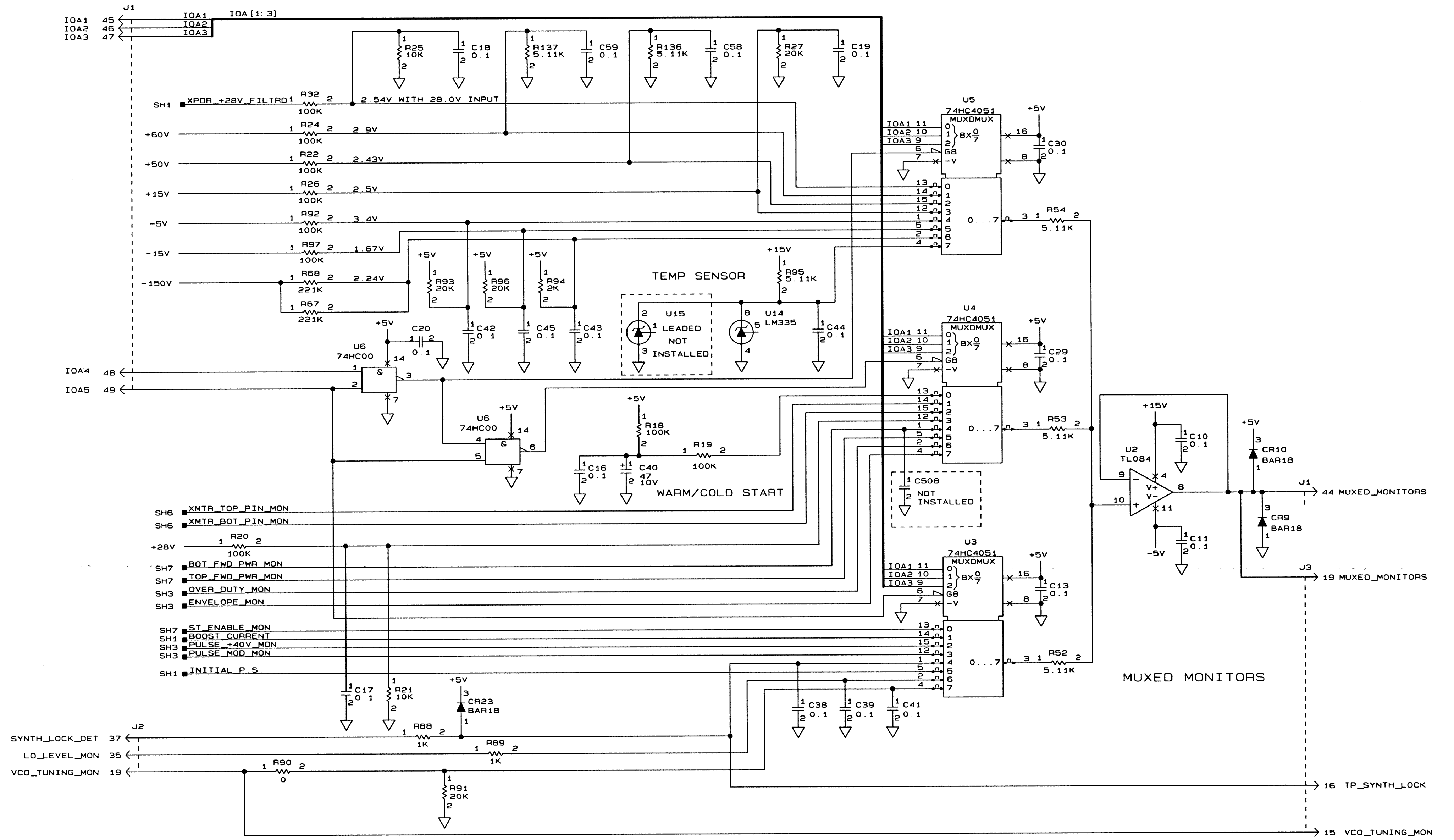
ZONE		REV	DESCRIPTION	REF DES	DATE	APPROVED
G3	A		UDTD SCHEM: REF DES	R149 AND R57 VALUES		
C5			WERE 1K	62699 (U1)		



PULSE MODULATOR / ENVELOPE MODULATOR /
OVERDUTY CYCLE SHUT DOWN
SCHEMATIC DIAGRAM SHEET NO. 3

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER, USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
		E	55939	7517470-903	A



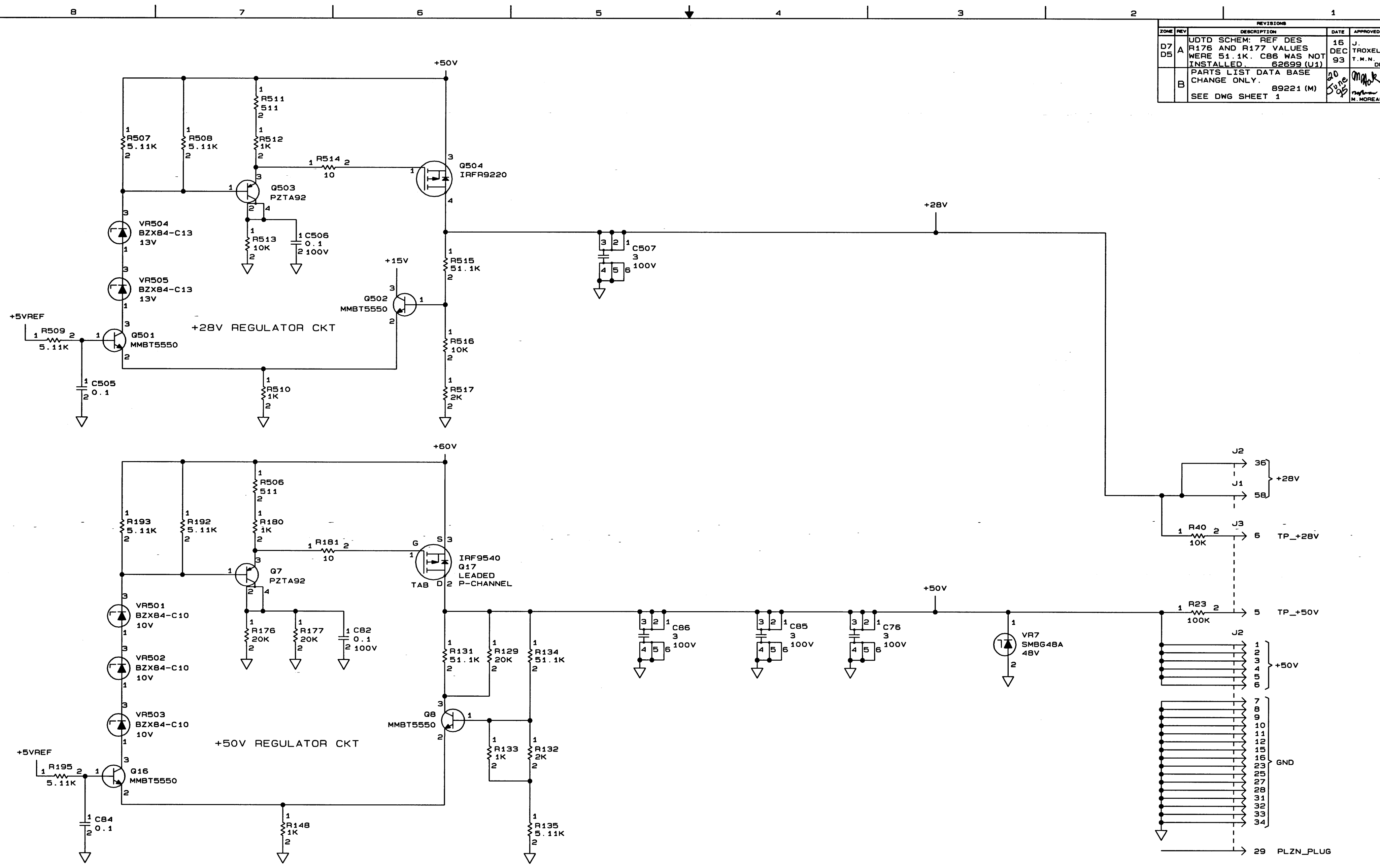
TEMP SENSOR / WARM-COLD START / MUXED MONITORS
SCHEMATIC DIAGRAM SHEET NO. 4

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE: E SCALE: —	CAGE CODE: 55939	DRAWING NO.: 7517470-903	REV: 6
	DEPT 5921 PAUL COBLENTZ		SHEET 6		

REV 7517470-903 6

REV		DESCRIPTION	DATE	APPROVED
D7	A	UDTD SCHEM: REF DES R176 AND R177 VALUES WERE 51.1K. C86 WAS NOT INSTALLED.	16 DEC 93	J. TROXEL T.M.N. DM
	B	PARTS LIST DATA BASE CHANGE ONLY. 89221 (M) SEE DWG SHEET 1		<i>Suno</i> <i>M. Moreau</i>



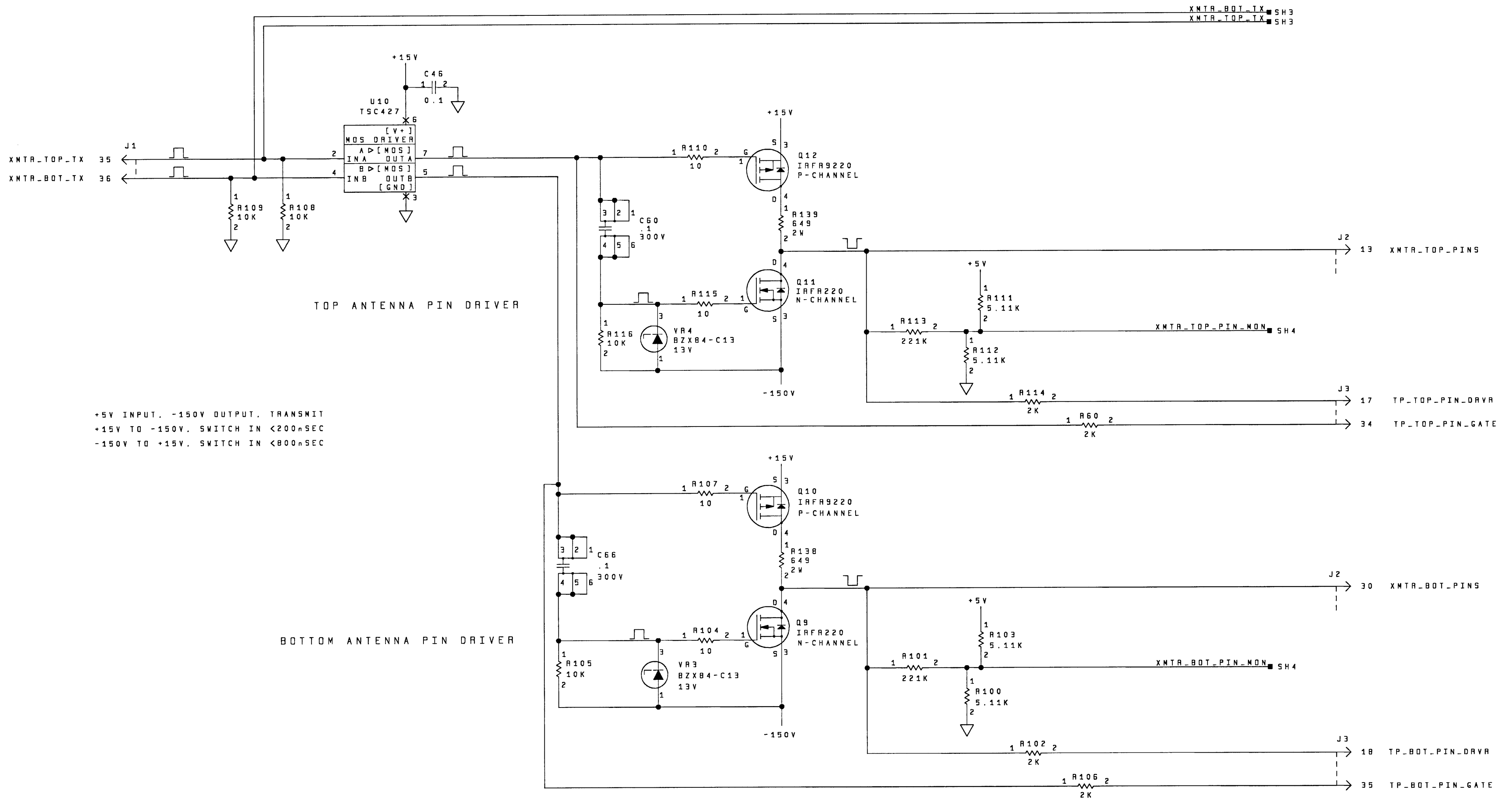
+50V REGULATOR / +28V REGULATOR
SCHEMATIC DIAGRAM SHEET NO. 5

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE CASE CODE E 55939	DRAWING NO. 7517470-903	REV B
	SCALE	SHEET 7		

REV 7517470-903 SHEET 7

REVISED		DATE	APPROVED
B	PARTS LIST DATA BASE CHANGE ONLY. 89221(M) SEE DWG SHEET 1	20 JUN 95	M. HOVK M. BROWN M. KOREAU
D	PARTS LIST DATA BASE CHANGE ONLY. 78292(U) SEE DWG SHEET 1		le [Signature] M. KOREAU



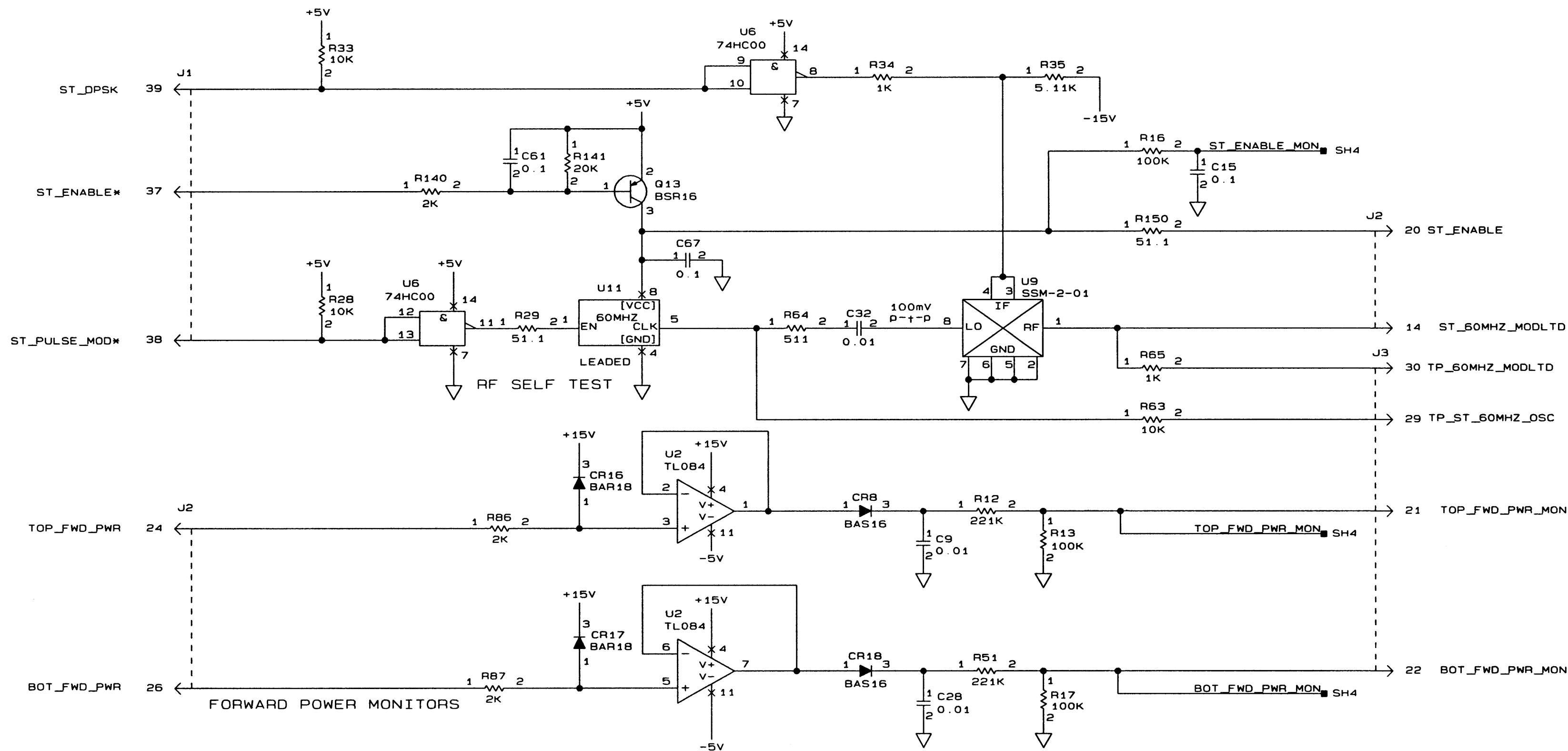
+5V INPUT. -150V OUTPUT. TRANSMIT
+15V TO -150V. SWITCH IN <200nSEC
-150V TO +15V. SWITCH IN <800nSEC

TOP AND BOTTOM PIN DIODE DRIVERS
SCHEMATIC DIAGRAM SHEET NO. 6

COMPUTER GENERATED

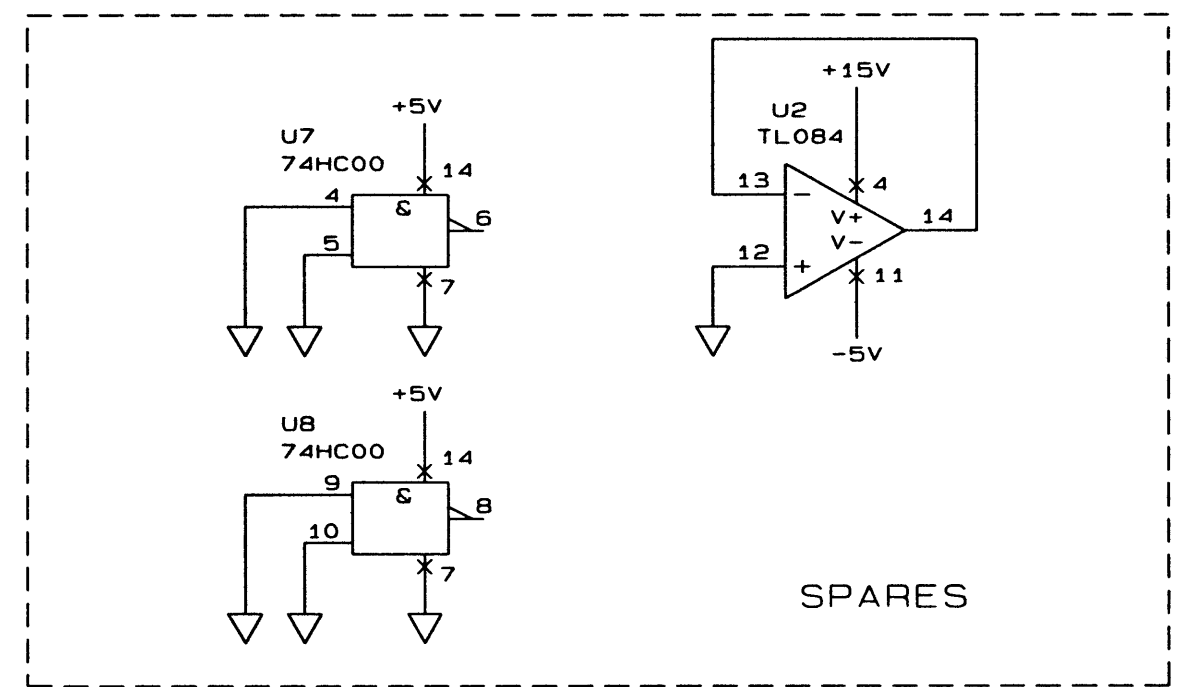
Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	E 55939	DRAWING NO.	7517470-903	REV	D
	SCALE						SHEET 8

REV 7517470-903



SELF TEST / TOP AND BOTTOM FORWARD PWR
SCHEMATIC DIAGRAM SHEET NO. 7

COMPUTER GENERATED



CONNECTOR BETWEEN PROC. & P.S./MOD. & RCVR.
 NOTE: " () " INDICATES NOT USED ON THIS P.S./MOD CCA

+5V		J1 CON50		+5V	
SH2	GND	1	(DIG TOP VIDEO)		
SH2	GND	3	(DIG BOT VIDEO)		
SH2	GND	5	(DPSK DATA)		
SH2	GND	7	(RRL VIDEO*)		
SH2	GND	9			
SH2	+15V	11	+15V	SH2	
SH2	-5V	13	-5V	SH2	
SH2	(RCVR BOT/TOP*)	15	-15V	SH2	
	(ANT TOP/BOT*)	17	(BOT MTL ADJ)	SH2	
	(TOP MTL ADJ)	19	(RRL CONTROL)		
	(RESERVE ADJ_1)	21	(SPARE_209)		
	(SPARE_210)	23	(BOT GAIN)		
	(SPARE_207)	25	(TOP GAIN)		
	(SPARE_208)	27	(SPARE_201)		
	(SPARE_202)	29	(SPARE_203)		
SH1	PLZN PLUG	31	(SPARE_204)		
	(SPARE_205)	33	(SPARE_206)		
SH6	XMTR TOP TX	35	XMTR BOT TX	SH6	
SH7	ST_ENABLE*	37	ST_PULSE_MOD*	SH7	
SH7	ST_DPSK	39	PS_INTERRUPT*	SH1	
SH3	XMTR_ENVELOPE*	41	XPDR_OFF<NO>	SH1	
SH1	100KHZ_PS_SYNC	43	MUXED_MONITORS	SH4	
SH4	IOA1	45	IOA2	SH4	
SH4	IOA3	47	IOA4	SH4	
SH4	IOA5	49	XMTR_PULSE_MOD	SH3	
SH1	GND	51	GND	SH1	
SH1	XPDR +28V_PWR	53	XPDR +28V_PWR	SH1	
SH1	XPDR +28V_PWR	55	XPDR +28V_PWR	SH1	
SH1	(SPARE_211)	57	+28V	SH5	
SH1	GND	59	GND	SH1	

CONNECTOR BETWEEN P.S./MOD. & XMTR

+5V		J2 CON40		+5V	
SH5	+50V	1	+50V	SH5	
SH5	+50V	3	+50V	SH5	
SH5	+50V	5	+50V	SH5	
SH5	GND	7	GND	SH5	
SH5	GND	9	GND	SH5	
SH5	GND	11	GND	SH5	
SH6	XMTR_TOP_PINS	13	ST_60MHZ_MODLTD	SH7	
SH5	GND	15	GND	SH5	
		17			
SH4	VCO_TUNING_MON	19	ST_ENABLE	SH7	
SH2	+15V	21	+15V	SH2	
SH5	GND	23	TOP_FWD_PWR	SH7	
SH5	GND	25	BOT_FWD_PWR	SH7	
SH5	GND	27	GND	SH5	
SH5	PLZN_PLUG	29	XMTR_BOT_PINS	SH6	
SH5	GND	31	GND	SH5	
SH5	GND	33	GND	SH5	
SH4	LO_LEVEL_MON	35	+28V	SH5	
SH4	SYNTH_LOCK_DET	37	XMTR +28V_PULSE	SH3	
SH3	XMTR +28V_ENV	39	XMTR +28V_PULSE	SH3	

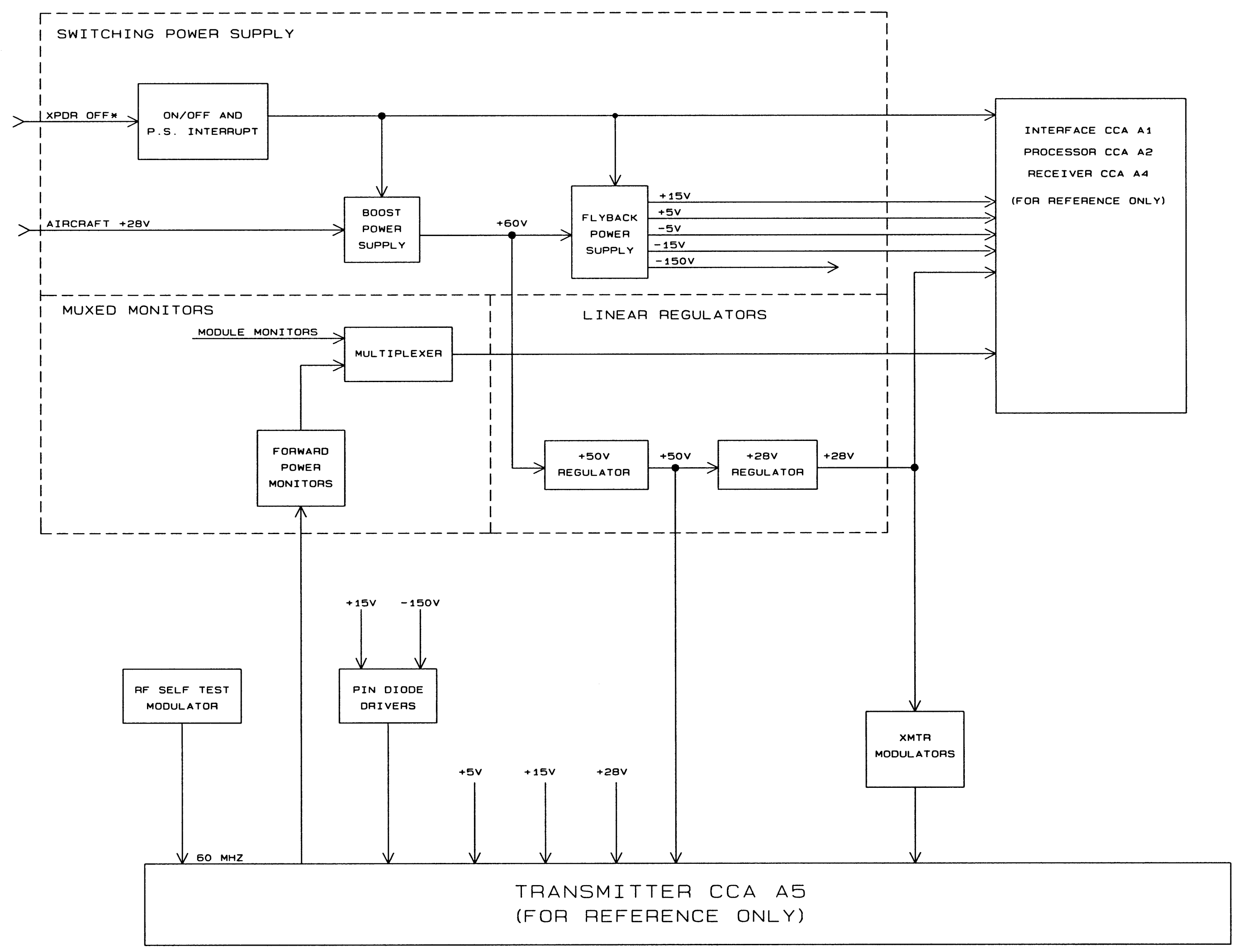
TEST CONNECTOR

+5V		J3 CON36		+5V	
SH1	TP +28V_FILTERED	1	TP PS_PWR	SH1	
SH1	TP STBY_PWR	3	TP +60V	SH1	
SH5	TP +50V	5	TP +28V	SH5	
SH2	TP +15V	7	TP +5V	SH2	
SH2	TP -5V	9	TP -15V	SH2	
SH2	TP -150V	11	TP PULSEMOD_GATE	SH3	
SH3	TP +28V_PULSEMOD	13	TP +28V_ENVELOPE	SH3	
SH4	VCO_TUNING_MON	15	TP SYNTH_LOCK	SH4	
SH6	TP TOP_PIN_DRV	17	TP BOT_PIN_DRV	SH6	
SH4	MUXED_MONITORS	19	TP PS_INTERRUPT*	SH1	
SH7	TOP_FWD_PWR_MON	21	BOT_FWD_PWR_MON	SH7	
SH2	TP XFMR_OUT +5V	23	TP BOOST_RESET*	SH1	
SH2	TP FLYBACK_RST*	25	TP BOOST_FET	SH1	
SH2	TP FLYBACK_FET	27	TP SNUBBER	SH2	
SH7	TP ST_60MHZ_OSC	29	TP 60MHZ_MODLTD	SH7	
SH2	PLZN_PLUG	31	TP PS_RAMP_SYNC	SH1	
SH1	TP PS_SYNC	33	TP TOP_PIN_GATE	SH6	
SH6	TP BOT_PIN_GATE	35	GND	SH2	

CONNECTOR PIN SUMMARY
 AND SPARE COMPONENTS
 CAD REFERENCE/REQUIREMENT
 SCHEMATIC DIAGRAM SHEET NO. 8

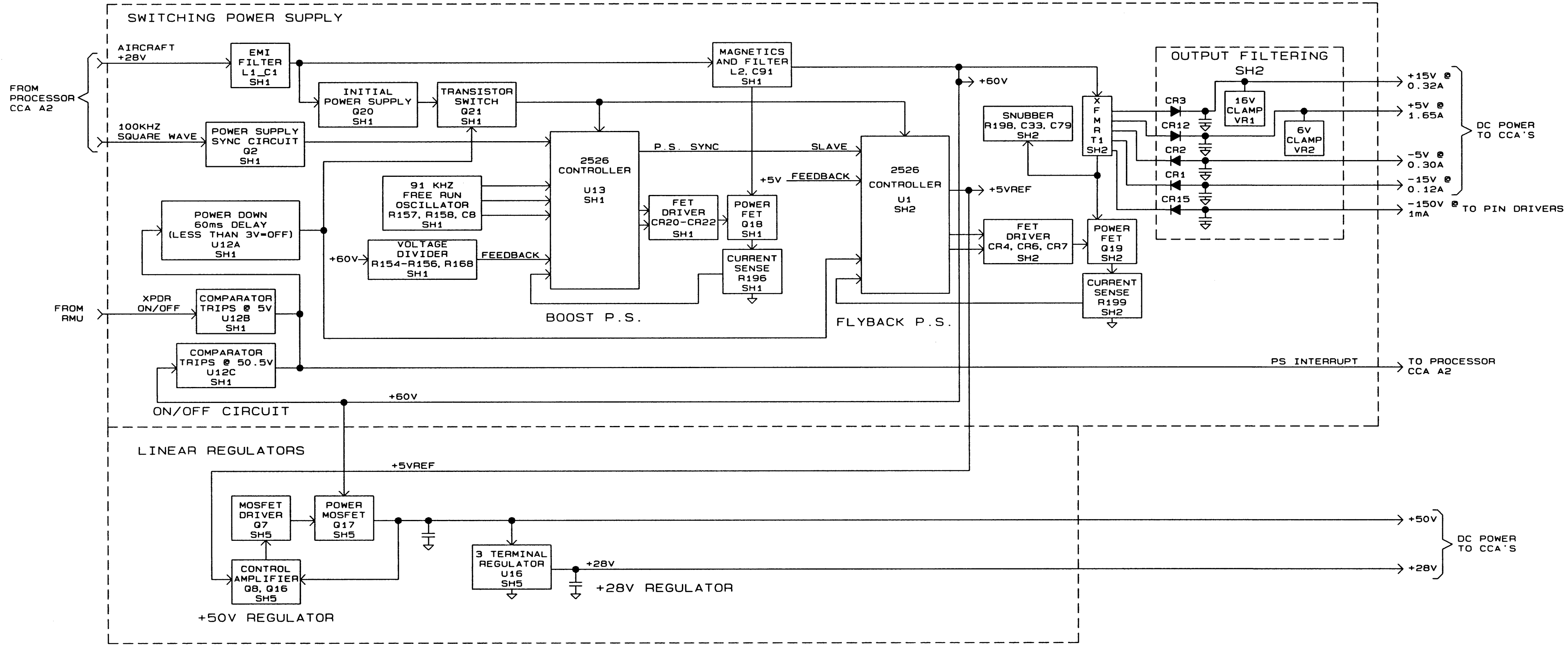
COMPUTER GENERATED

7517470-903 10
 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100



SIMPLIFIED BLOCK DIAGRAM
SCHEMATIC DIAGRAM SHEET NO. 9

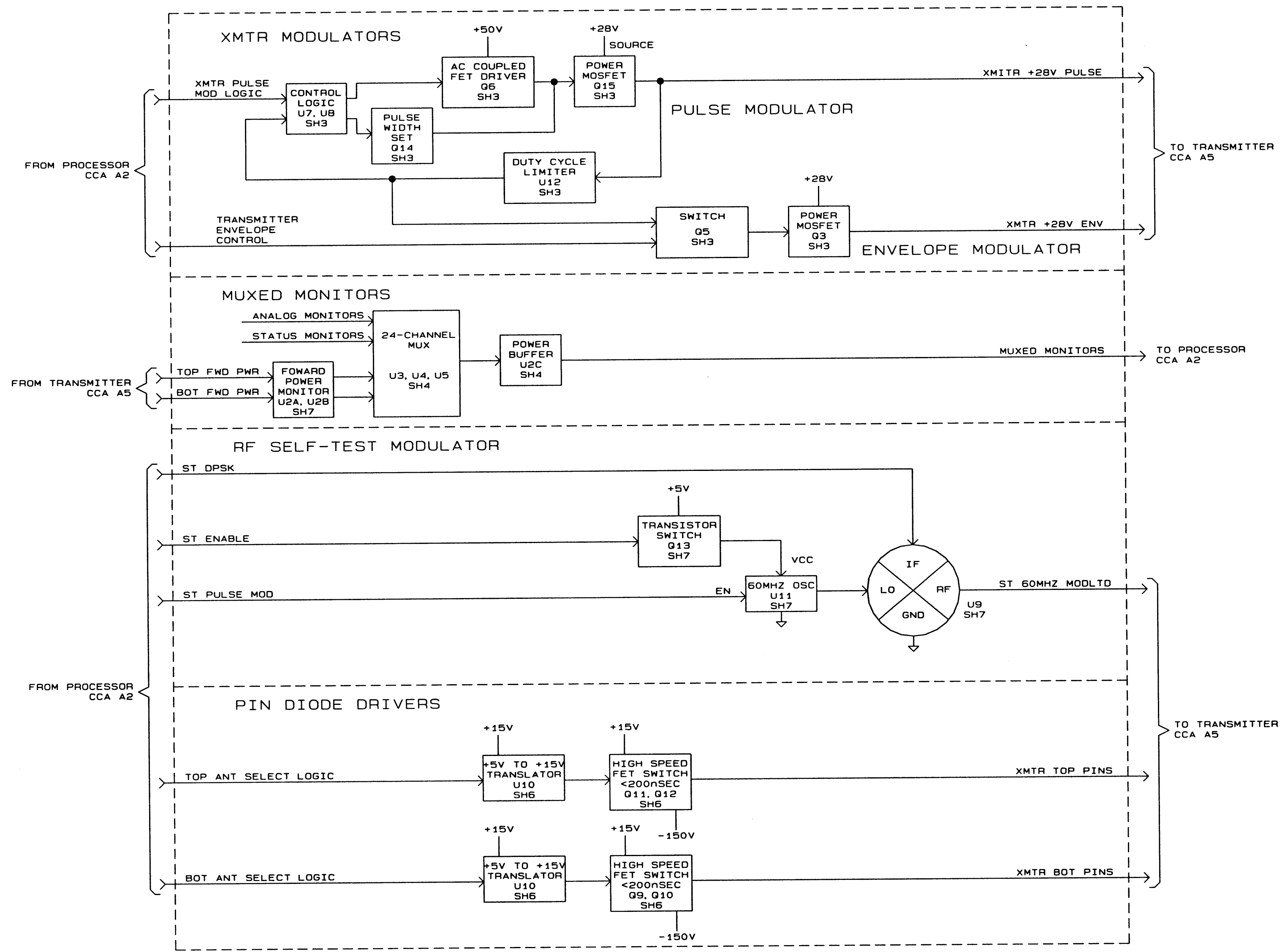
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DETAILED BLOCK DIAGRAM A
SCHEMATIC DIAGRAM SHEET NO. 10

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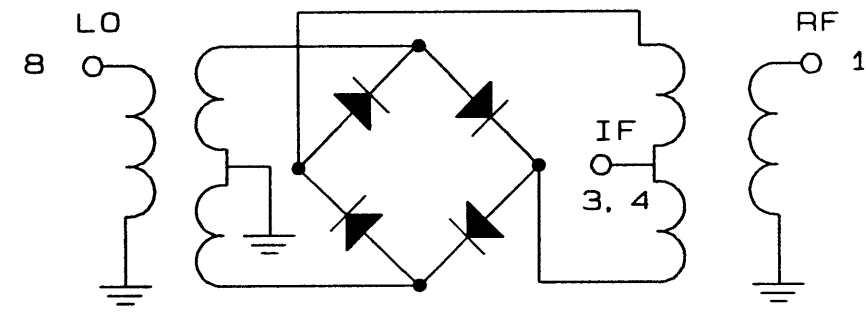
7517470-903 12



DETAILED BLOCK DIAGRAM B
SCHEMATIC DIAGRAM SHEET NO. 11

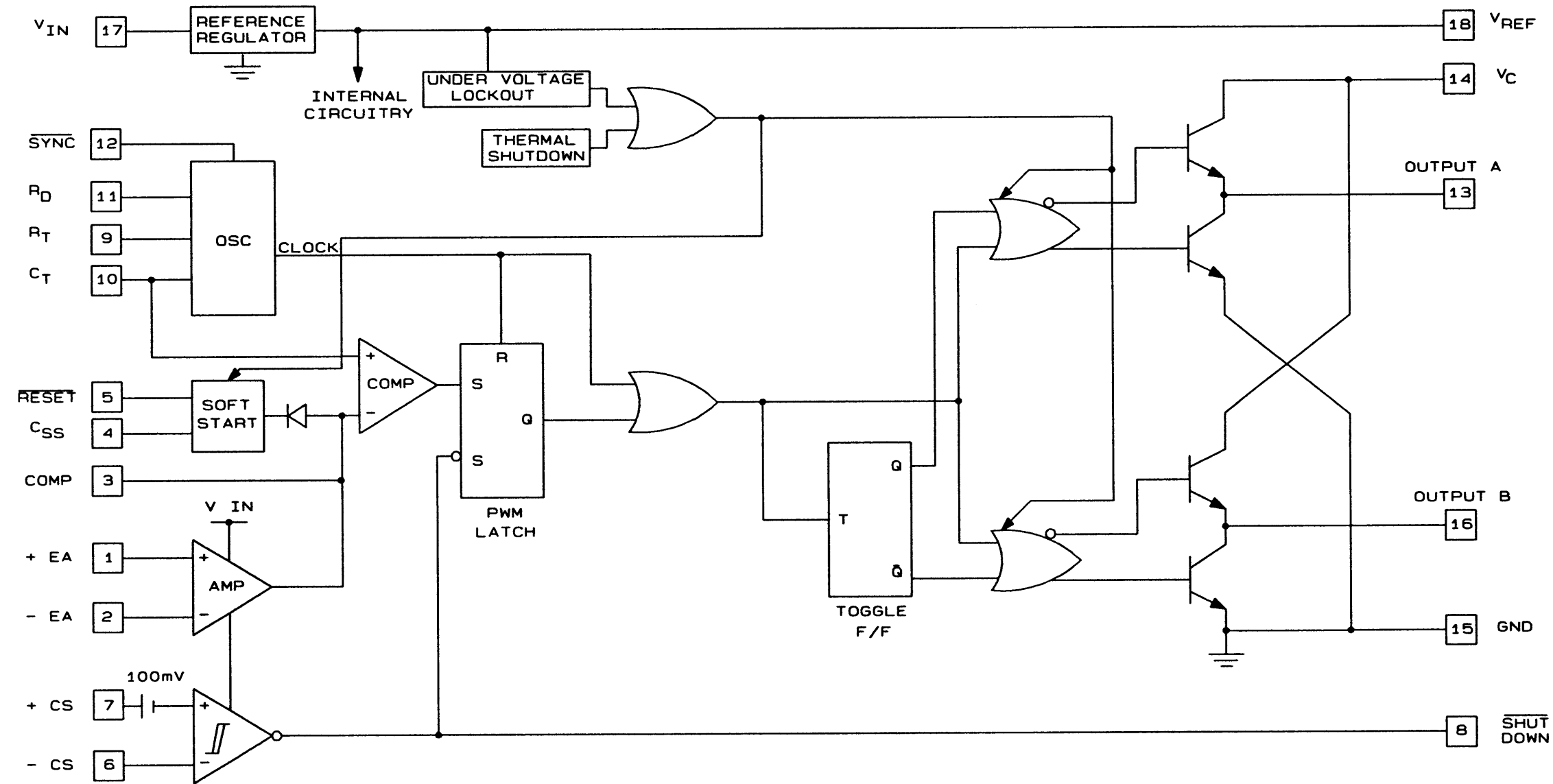
COMPUTER GENERATED

7517470-903 13



SSM-2-01 MIXER

PINS 2, 5, 6 AND 7 ARE GROUNDDED.



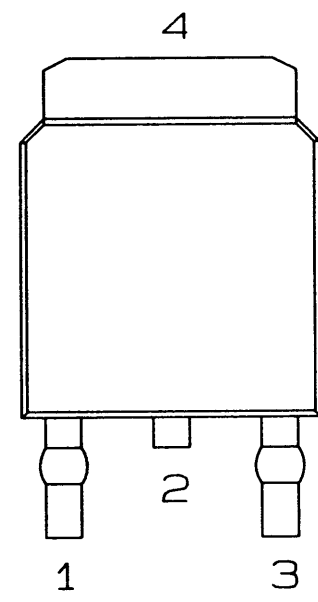
2526A PULSE WIDTH MODULATOR

FOR REFERENCE ONLY

COMPONENT BLOCK DIAGRAM
SCHEMATIC DIAGRAM SHEET NO. 12

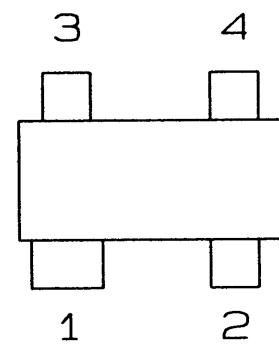
COMPUTER GENERATED

PINOOTS OF SMT DEVICES

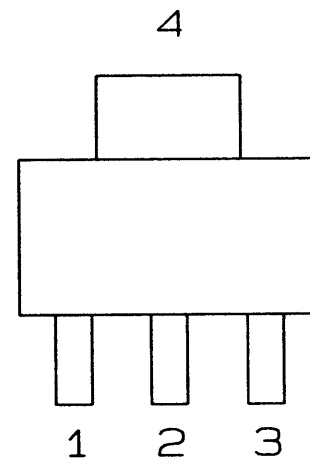


DPAK

NOTE: PIN 2 IS CONNECTED TO PIN 4 AND ALSO PIN 2 DOES NOT MOUNT TO PWB

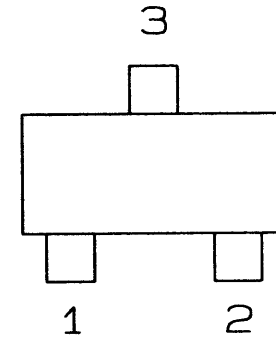


SOT143

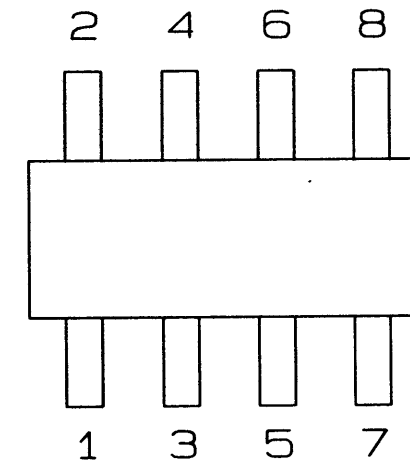


SOT223

NOTE: PIN 2 IS CONNECTED TO PIN 4



SOT23



SSM-2-01 MIXER

COMMON LABELS ON TOP OF SMT DEVICES

PART NAME	LABEL
BAS16	A6
BAR18	5F, D76
BZX84-C13	Y3
BSR14	U8
MMBT5550	1F
BSR16	T8
SMBG15A	LM
SMBG5A	KE
MURS160T3	U1J
MMBT5401	2L
BZX84-C10	6Z
SMBG48A	MX
PZTA92	ZTA92
CGY50	G2
MSA0686	A06
MSA0386	A03
HSMP3800	D0
NE02135	Bq
HSMP3820	F0

TEST POINT DESCRIPTIONS

TEST POINT	NAME	DESCRIPTION	VOLTAGE RANGE	NOMINAL VOLTAGE	SCHEMATIC LOCATION	WAVEFORM NUMBER
J3-1	TP_+28V_FILTERED	XPDR +28V AFTER EMI FILTER.	17-32 VDC			
J3-2	TP_PS_PWR	POWER CONTROLLER IC'S POWER SUPPLY.	13-15 VDC			
J3-3	TP_STBY_PWR	POWER SUPPLY USED FOR ON THE ON/OFF CIRCUIT.	13-15 VDC			
J3-4	TP_+60V	+60V DC POWER SUPPLY.	60 +/- 6 VDC			
J3-5	TP_+50V	+50V DC POWER SUPPLY.	50 +/- 5 VDC			
J3-6	TP_+28V	+28V DC POWER SUPPLY.	28 +/- 2.8 VDC			
J3-7	TP_+15V	+15V DC POWER SUPPLY.	15 +/- 1.5 VDC			
J3-8	TP_+5V	+5V DC POWER SUPPLY.	5 +/- .25 VDC			
J3-9	TP_-5V	-5V DC POWER SUPPLY.	-5 +/- .5 VDC			
J3-10	TP_-15V	-15V DC POWER SUPPLY.	-15 +/-1.5 VDC			
J3-11	TP_-150V	150V DC POWER SUPPLY.	-150 +/-15 VDC			
J3-12	TP_PULSEMOD_GATE	GATE OF POWER MOSFET FOR PULSE MOD. +40V WHEN PULSE MOD OUT.	0-40 VDC			
J3-13	TP_+28V_PULSEMOD	PULSE MOD TO TRANSMITTER. +28V WHEN TRANSMITTING.	0-28 VDC			
J3-14	TP_+28V_ENVELOPE	ENVELOPE SIGNAL TO TRANSMITTER. +28V WHEN TRANSMITTING.	0-28 VDC			
J3-15	TP_VCO_TUNING	VCO TUNING VOLTAGE FROM TRANSMITTER.	0-15 VDC			
J3-16	TP_SYNTH_LOCK	DIGITAL SIGNAL. SYNTHSIZER LOCK DETECT FROM TRANSMITTER.	0-5 VDC			
J3-17	TP_TOP_PIN_DRVVR	VOLTAGE ON TOP PINS ON XMTR. 1V=RX; -150V=TX.	15 TO -150 VDC			
J3-18	TP_BOT_PIN_DRVVR	VOLTAGE ON BOT PINS ON XMTR. 1V=RX; -150V=TX.	15 TO -150 VDC			
J3-19	MUXED_MONITORS	24 MUXED MONITORS TO PROCESSOR.	0-5 VDC			
J3-20	TP_PS_INTERRUPT*	DIGITAL SIGNAL. EARLY WARNING TO PROCESSOR WHEN LOSING POWER.	0-5 VDC			
J3-21	TOP_FWD_PWR_MON	TOP FORWARD POWER MONITOR. ONLY VALID 50uS AFTER XMIT.	0-15 VDC			
J3-22	BOT_FWD_PWR_MON	BOT FORWARD POWER MONITOR. ONLY VALID 50uS AFTER XMIT.	0-15 VDC			
J3-23	TP_XFMR_OUT_+5V	TRANSFORMER OUTPUT OF THE +5V LINE. 100KHZ.	-5 TO +5 VDC			
J3-24	TP_BOOST_RESET*	RESET OUT OF BOOST SUPPLY. GOES LOW DURING CURRENT LIMIT.	0-5 VDC			
J3-25	TP_FLYBACK_RST*	RESET OUT OF FLYBACK SUPPLY. GOES LOW DURING CURRENT LIMIT.	0-5 VDC			
J3-26	TP_BOOST_FET	DRAIN OF SWITCHING FET IN BOOST SUPPLY.	0-60 VDC			
J3-27	TP_FLYBACK_FET	DRAIN OF SWITCHING FET IN FLYBACK SUPPLY.	0-120 VDC			
J3-28	TP_SNUBBER	SNUBBER DC VOLTAGE IN FLYBACK SUPPLY.	111 VDC			
J3-29	TP_60MHZ_OSC	60MHZ OSCILLATOR ON ONLY DURING SELF TEST.	0-5 VDC			
J3-30	TP_60MHZ_MODLTD	60MHZ MODALTED PAM OR DPSK ONLY DURING SELF TEST.	50 mV p+p			
J3-31	PLZN_PLUG	POLARIZING PLUG.	NA			
J3-32	TP_PS_RAMP_SYNC	FREE RUNNING PS OSCILLATOR RAMP FROM BOOST SUPPLY.	0-5 VDC			
J3-33	TP_PS_SYNC	BOOST AND FLYBACK SYNC LINE FROM PROCESSOR.	0-5 VDC			
J3-34	TP_BOT_PIN_GATE	BOT PIN DRIVER GATE OF THE XMIT SIDE OF THE DRIVER.	-135 TO -150 VDC			
J3-35	TP_TOP_PIN_GATE	TOP PIN DRIVER GATE OF THE XMIT SIDE OF THE DRIVER.	-135 TO -150 VDC			
J3-36	GND	GND.	0 VDC			

FOR REFERENCE ONLY

SMT COMPONENT PINOUT / SMT COMPONENT MARKINGS / TEST POINT INFORMATION TABLE

SCHEMATIC DIAGRAM SHEET NO. 13

COMPUTER GENERATED

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