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ZONE	REV	DESCRIPTION	DATE	APPROVED
		INITIAL RELEASE	1DEC84	G. BEILER
A		SH IDX REV. PL AFFECTED. USE ALL PARTS IN-HOUSE, IN STOCK AND CURRENTLY ON ORDER (EFFECTIVITY IS NEXT BUY).	08460 (U) 13JAN90	M. HOWE R. RICHARDS
B		NOTES: NOTE 3 WAS MAXIMUM HEIGHT SIDE B --- .350. SH IDX REV. PL AFFECTED. USE ALL PARTS IN-HOUSE, IN STOCK AND CURRENTLY ON ORDER, REQUISITION OR IN PROCESS AT SUPPLIER. EFFECTIVITY IS NEXT BUY.	26892 (U) 0800T01	M. PORTS R. RICHARDS J. OLSON
C		SH IDX REV. PL NOT AFFECTED. USE ALL PARTS IN-HOUSE, IN STOCK AND CURRENTLY ON ORDER, REQUISITION OR IN PROCESS AT SUPPLIER. EFFECTIVITY IS NEXT BUY.	200200033A (U) 14JUL93	M. PORTS R. RICHARDS

NOTES:

- ASSEMBLE AND SOLDER PER ITEM 701.
- 
 THIS ASSEMBLY INCLUDES COMPONENTS WHICH ARE SUBJECT TO DAMAGE BY ELECTROSTATIC CHARGES; THEREFORE, ALL COMPONENTS SHALL BE HANDLED IN ACCORDANCE WITH GUIDELINES FOR ELECTROSTATIC DISCHARGE CONTROL.
- MAXIMUM COMPONENT HEIGHT, WITH THE EXCEPTION OF J1 AND J2, SIDE B SHALL BE:  
 CONDUCTIVE CASE .270  
 NONCONDUCTIVE OR GROUNDED CASE .320
- COMPONENT LEAD PROTRUSION SHALL BE .090 MAXIMUM; BARE COPPER PERMISSIBLE ON COMPONENT LEADS AT TRIMMED SURFACES.
- SQUARE PADS ON PRINTED WIRING BOARD INDICATE PIN 1.
- 
 CONFORMAL COAT CCA PER M7010975, EXCEPT P1 ON SIDE B, PINS OF J1, J2 AND J3 (.06 MAX FROM PLASTIC SURFACE WHICH PIN IS INSERTED, ON SIDE B) AND AREAS NOTED.
- 
 SHADED COMPONENTS NOT INSTALLED.

REV STATUS OF PARTS LIST SHALL BE SAME AS BASIC DRAWING NUMBER SEE SEPARATE PARTS LIST

45 44 43 42 41 40 39 38 37 36 35 34 33 32 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1		CONTRACT XS-952	OPERATIONAL NOTE
FOR HONEYWELL REFERENCE		DRAWN BY R. RICHARDS 21NOV94	SIMILAR ASSEMBLY
MFG REF NO.		DESIGNED BY	
DPA NO. A-15655		CHECKED BY E. THELSEN 21NOV94	<b>Honeywell</b> COMMERCIAL FLIGHT SYSTEMS GROUP PHOENIX, ARIZONA TITLE <b>CIRCUIT CARD ASSEMBLY-INTERFACE, AI</b>
PRODUCT LINE NO. 3834		APVD FOR MFG S. WATKIN 23NOV94	
AW/PS ANALYSIS EB 7510800		APVD FOR QE D. W. MULLEN 23NOV94	SIZE/CAGE CODE DRAWING NO. REV E 155939 7517410-9031 C SCALE NONE UNIT WT SHEET 1 OF 15
REF DWG REF SPEC		APVD FOR ENGRG R. PAULSEN 23NOV94	
7517400 7510700		APVD FOR ENGRG	
NEXT ASSY USED ON (SYS)		APVD FOR	
APPLICATION		FIRST USED ON 7510700	

REV 7517410-9031

REV 1

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	A	WTD VIEW: C62 WAS INSTALLED. SEE SH 1.	00460 (U) 13JUL99	M. HOWK R. RICHARDS L. WHITLER
	B	WTD VIEW. REF DES C75 WAS SHOWN INSTALLED. SEE SHEET 1.	26992 (U) 09OCT01	M. PORTS R. RICHARDS J. OLSON
	C	WTD VIEW. REF DES R500 WAS C75 AND NOT INSTALLED. SEE SHEET 1.	2002000334 (U) 14	M. PORTS R. RICHARDS J. OLSON

THIS SURFACE OF J1 AND J2 SHALL BE FLUSH TO WITHIN .015 TO SIDE B OF ITEM 1

90° ± 2°  
J1, J2 AND P1

ASSEMBLE J1 AND J2 WITH LONG PINS ON SIDE B OF ITEM 1

THIS SURFACE OF P1 MUST CONTACT SIDE B OF ITEM 1 AT BOTH LOCATING PINS

△ .120 MIN ANNULAR RING OF GROUND ON SIDE A

△ THIS AREA GROUND PLANE SIDE B

△ 8X R .230 MIN SIDE A AND SIDE B

IDENTIFICATION LABEL SIDE A

△ .120 MIN ANNULAR RING OF GROUND ON SIDE A

.485 MIN

.113 MIN

THIS AREA TO REMAIN FREE OF COMPONENTS

2X .075 MIN

.725 MIN

2X .175 MIN

2.695 MIN

2.097 MAX

.700 MIN

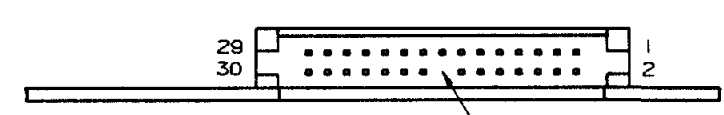
.300 MIN

VIEWING SIDE B

REMOVE PIN 19

REMOVE PIN 14

CUT PIN 16 .03 MAX FROM PLASTIC HOUSING



Honeywell

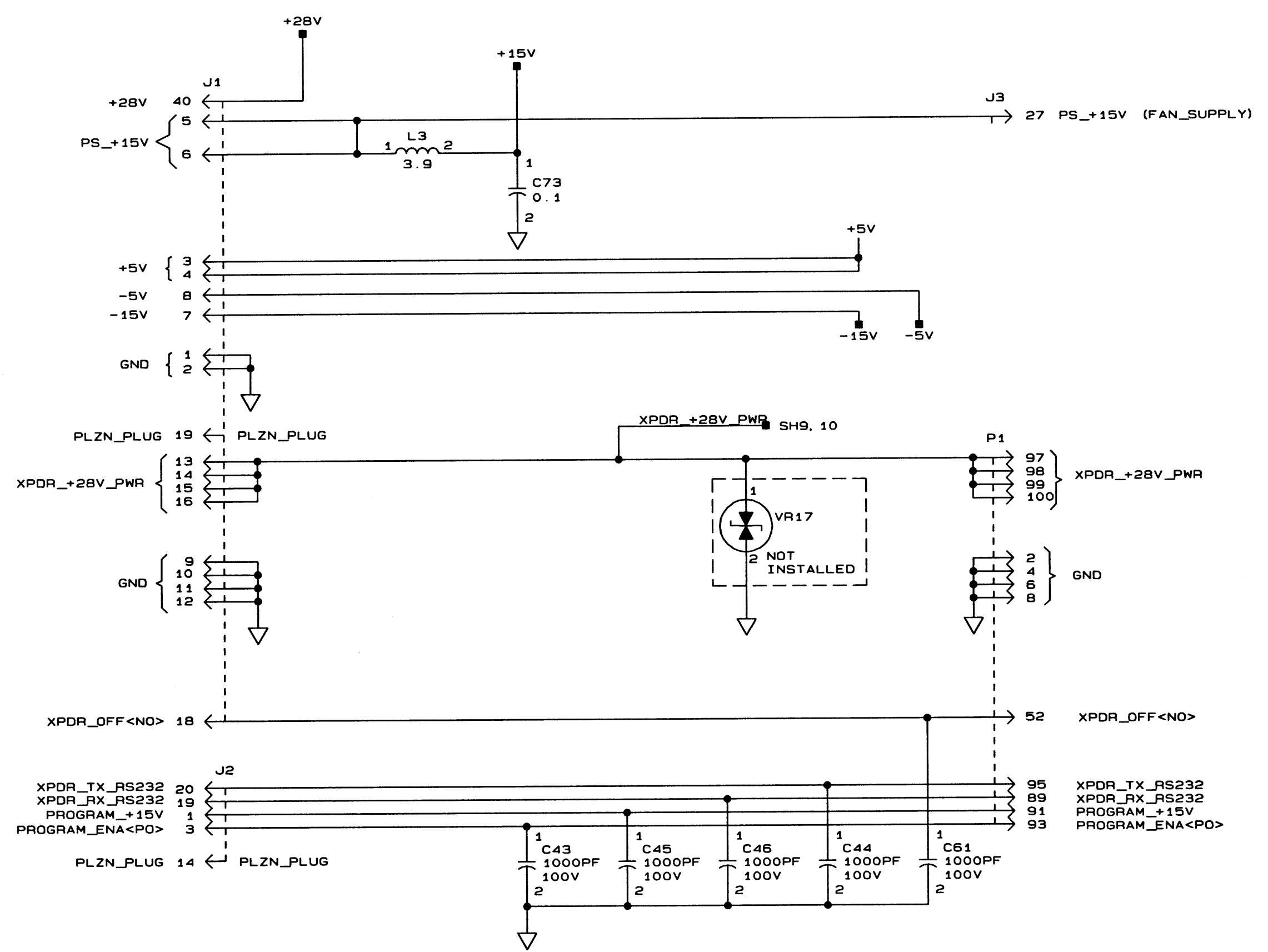
SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.

SIZE/CAGE CODE: E 55939

DRAWING NO.: 7517410-903

REV: C

SHEET 2



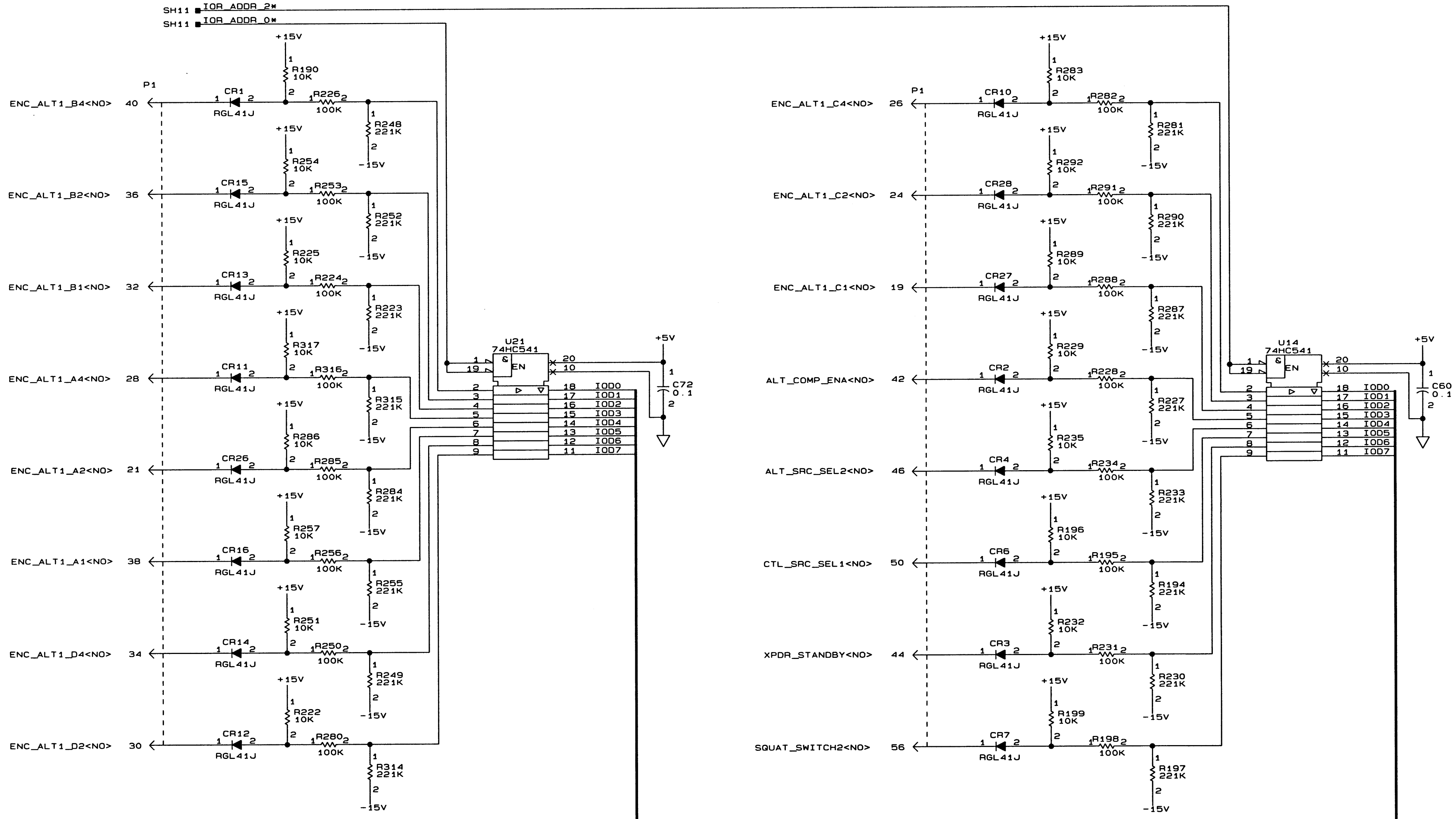
NOTES:  
 UNLESS OTHERWISE SPECIFIED:  
 1. ALL CAPACITANCE VALUES ARE IN MICROFARADS.  
 2. ALL RESISTANCE VALUES ARE IN OHMS.  
 3. ALL INDUCTANCE VALUES ARE IN MICROHENRIES.  
 4. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN;  
 FOR COMPLETE DESIGNATIONS PREFIX PART  
 DESIGNATION WITH SUBASSEMBLY DESIGNATION.  
 5. INTERPRET DRAWING PER ANSI Y32.2-1975 AND  
 ANSI/IEEE STD 991-1986. LOGIC SYMBOLS  
 CONFORM TO ANSI/IEEE STD 91-1984.

POWER LINE FILTERS  
 SCHEMATIC DIAGRAM SHEET NO. 1 OF 13

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE E 55939	DRAWING NO. 7517410-903	REV -
	SCALE -	SHEET 3		

REV 7517410-903 SHEET 3

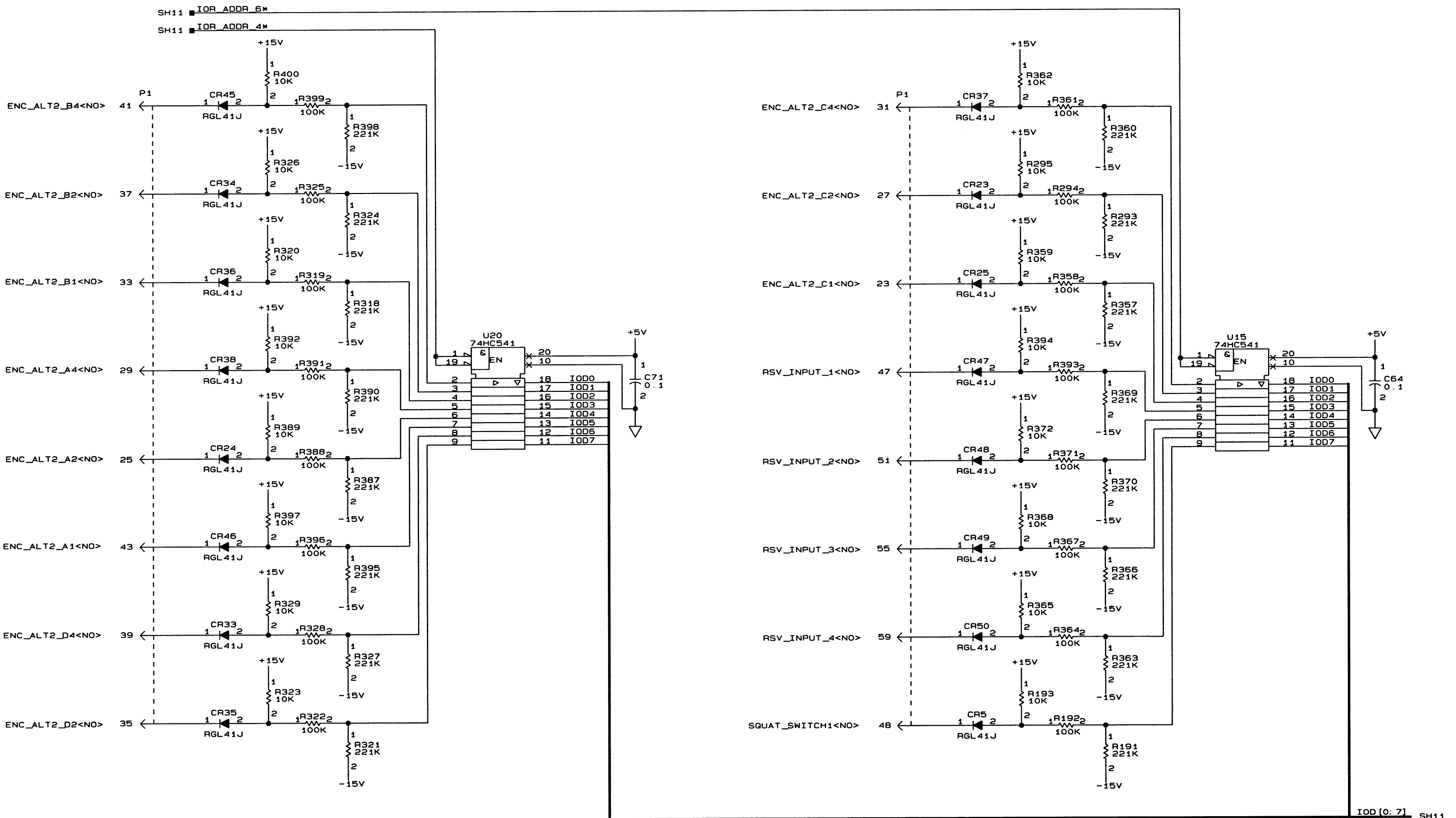


IOD [0: 7] SH11

DISCRETE INPUTS  
SCHEMATIC DIAGRAM SHEET NO. 2

**COMPUTER GENERATED**

REVISIONS			
ZONE	REV	DESCRIPTION	DATE



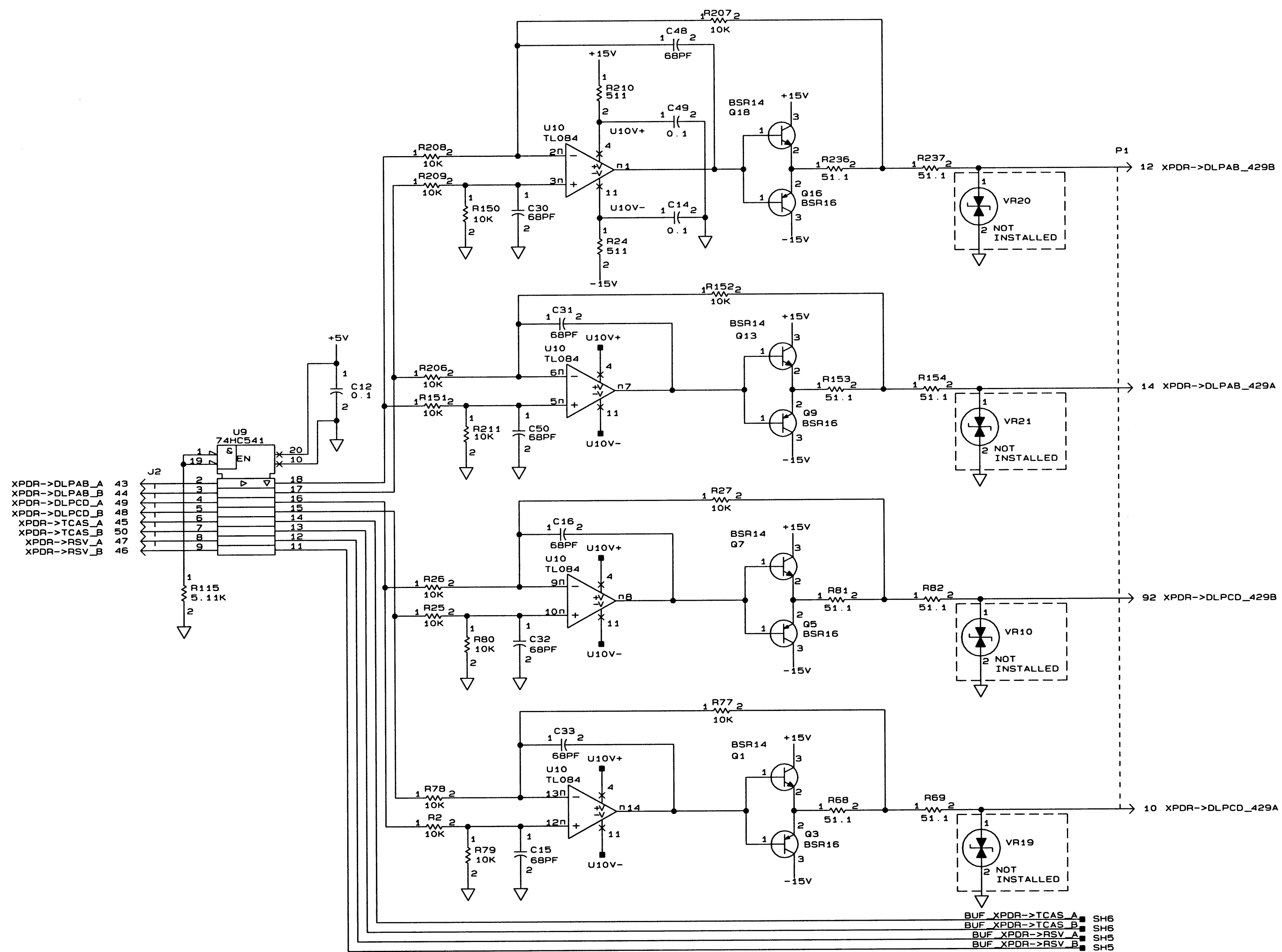
IOD [0: 7] SH11

DISCRETE INPUTS  
SCHEMATIC DIAGRAM SHEET NO. 3

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE CASE CODE	DRAWING NO.	REV
		E 55939	7517410-903	-
SCALE		SHEET 5		

REV 7517410-903



- XPDR->DLPAB\_A 43
- XPDR->DLPAB\_B 44
- XPDR->DLPCD\_A 49
- XPDR->DLPCD\_B 48
- XPDR->TCAS\_A 45
- XPDR->TCAS\_B 50
- XPDR->RSV\_A 47
- XPDR->RSV\_B 46

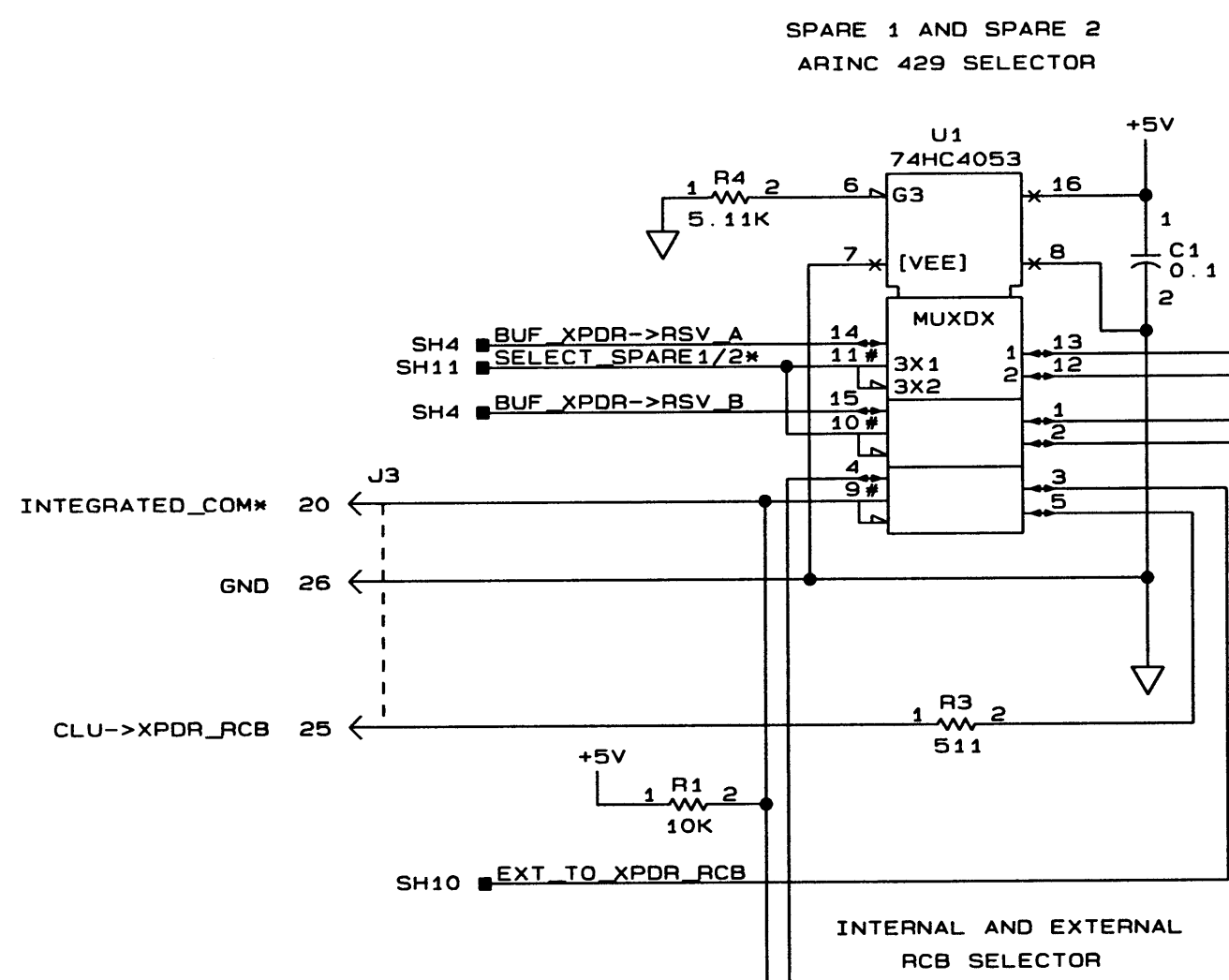
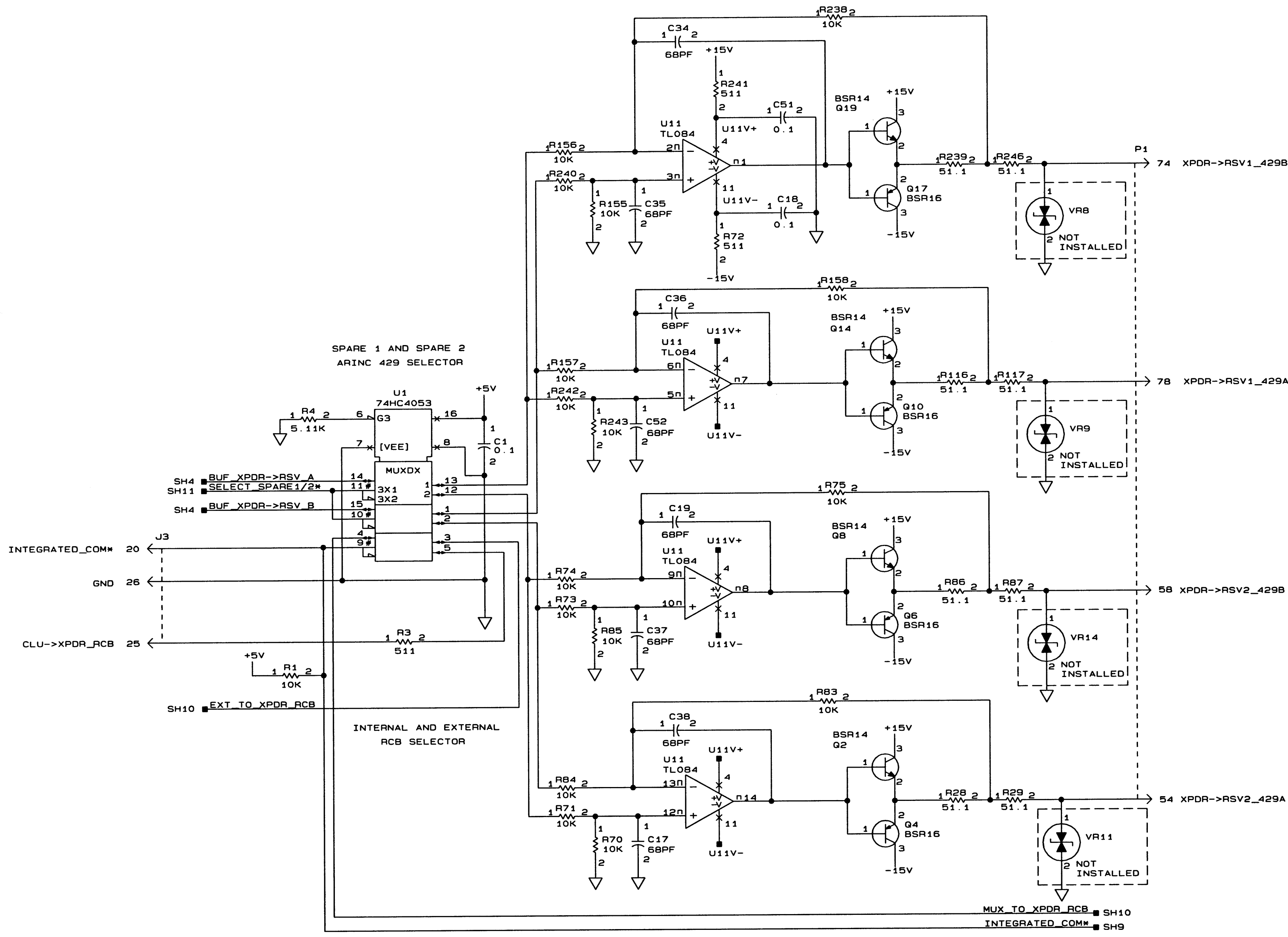
- BUF\_XPDR->TCAS\_A SH6
- BUF\_XPDR->TCAS\_B SH6
- BUF\_XPDR->RSV\_A SH5
- BUF\_XPDR->RSV\_B SH5

ARINC 429 OUTPUTS  
SCHEMATIC DIAGRAM SHEET NO. 4

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE CASE CODE E 55939	DRAWING NO. 7517410-903	REV -
	SCALE	SHEET 6	REV	1

REV 7517410-903 SHEET 6

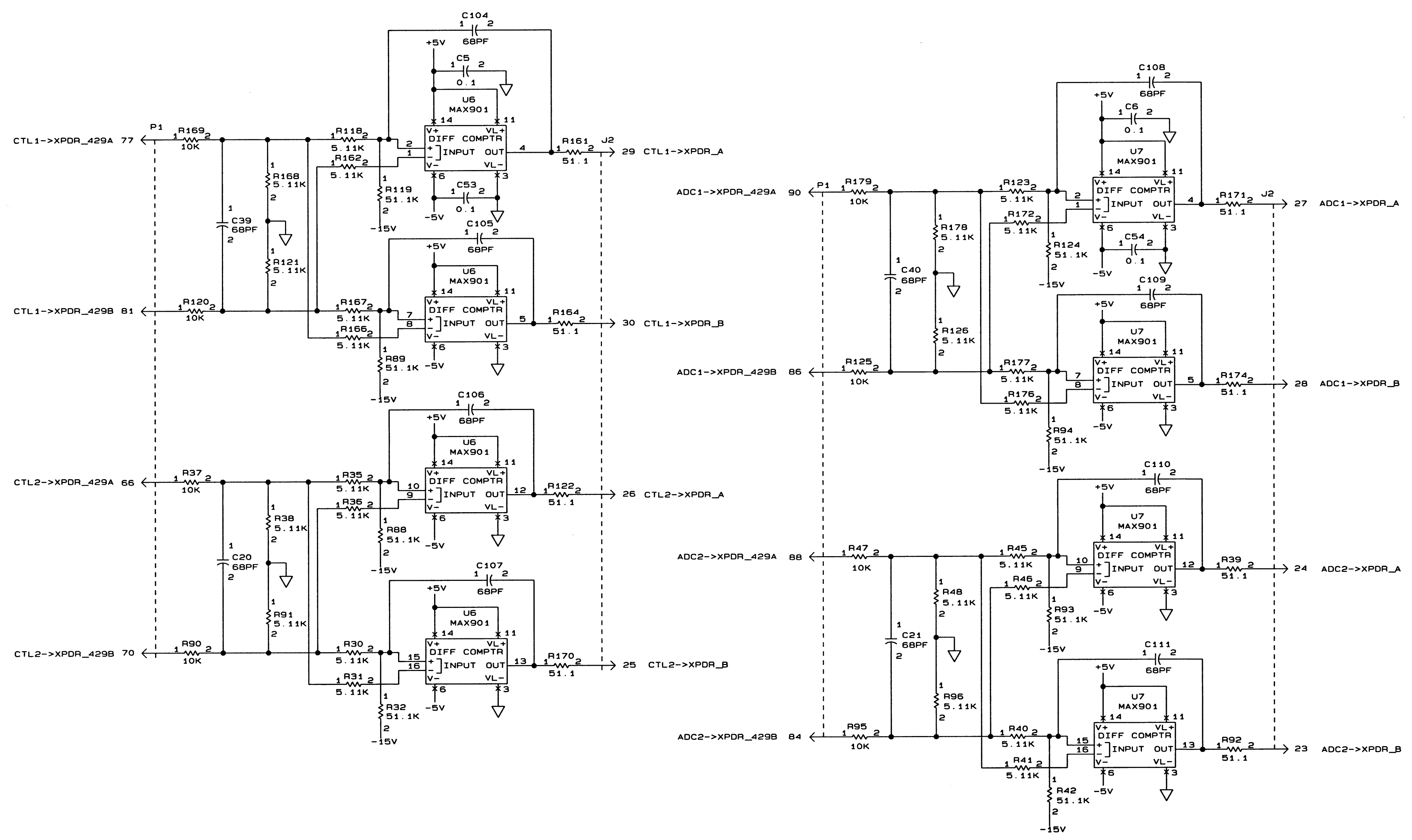


ARINC 429 OUTPUTS  
SCHEMATIC DIAGRAM SHEET NO. 5

COMPUTER GENERATED



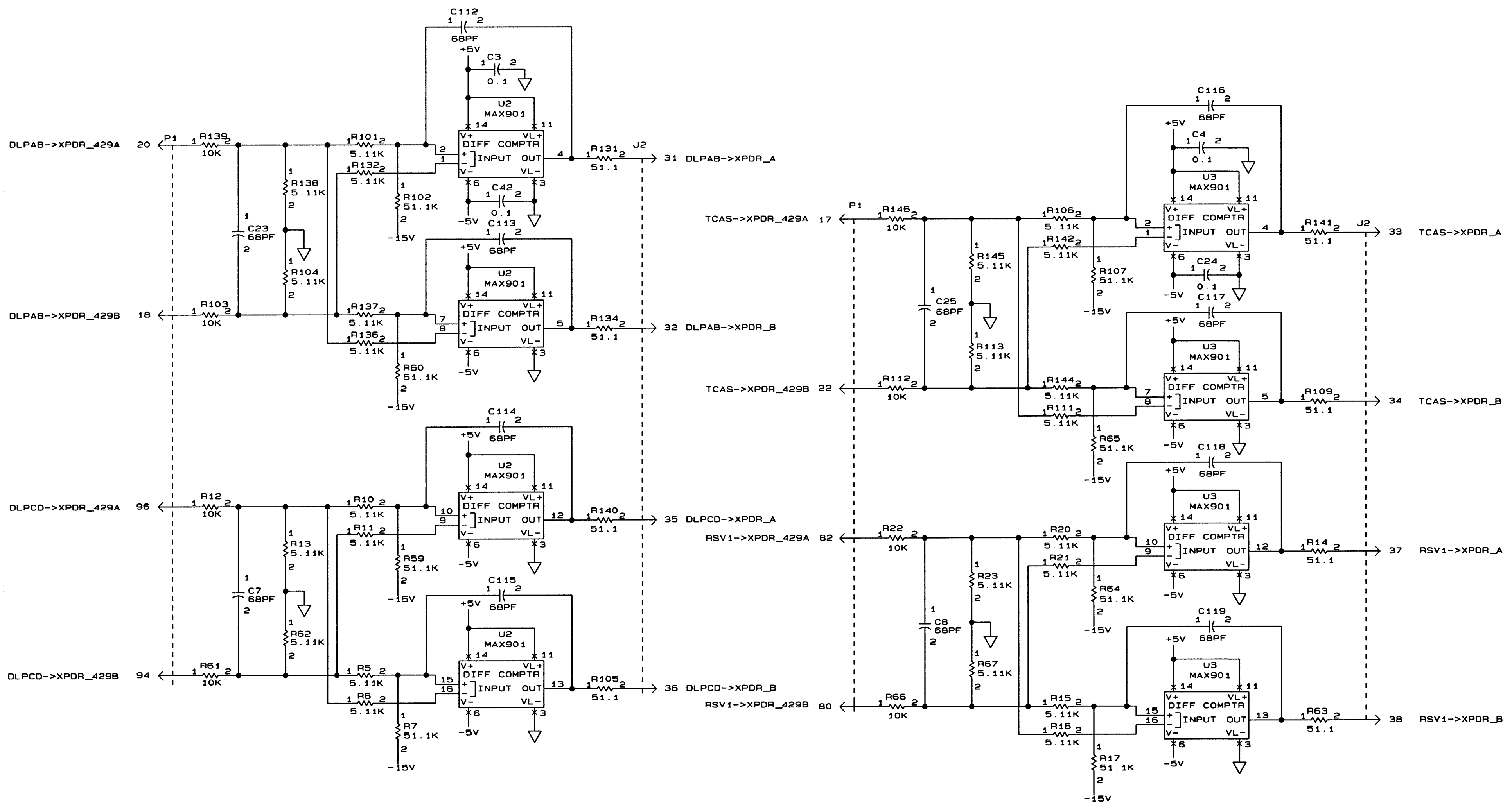




ARINC 429 INPUTS  
SCHEMATIC DIAGRAM SHEET NO. 7

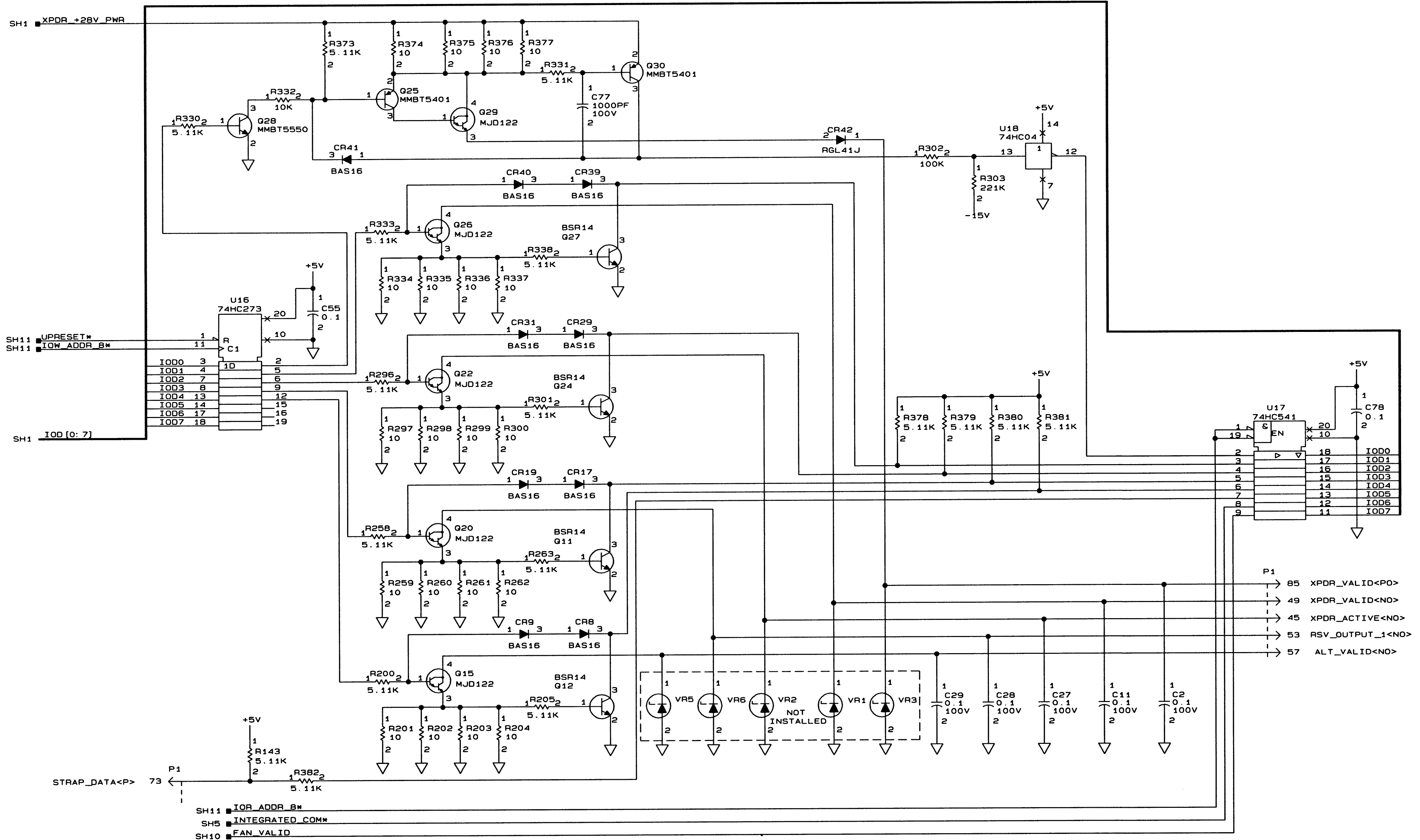
COMPUTER GENERATED

REV 7517410-903 SHEET 9



ARINC 429 INPUTS  
SCHEMATIC DIAGRAM SHEET NO. 8

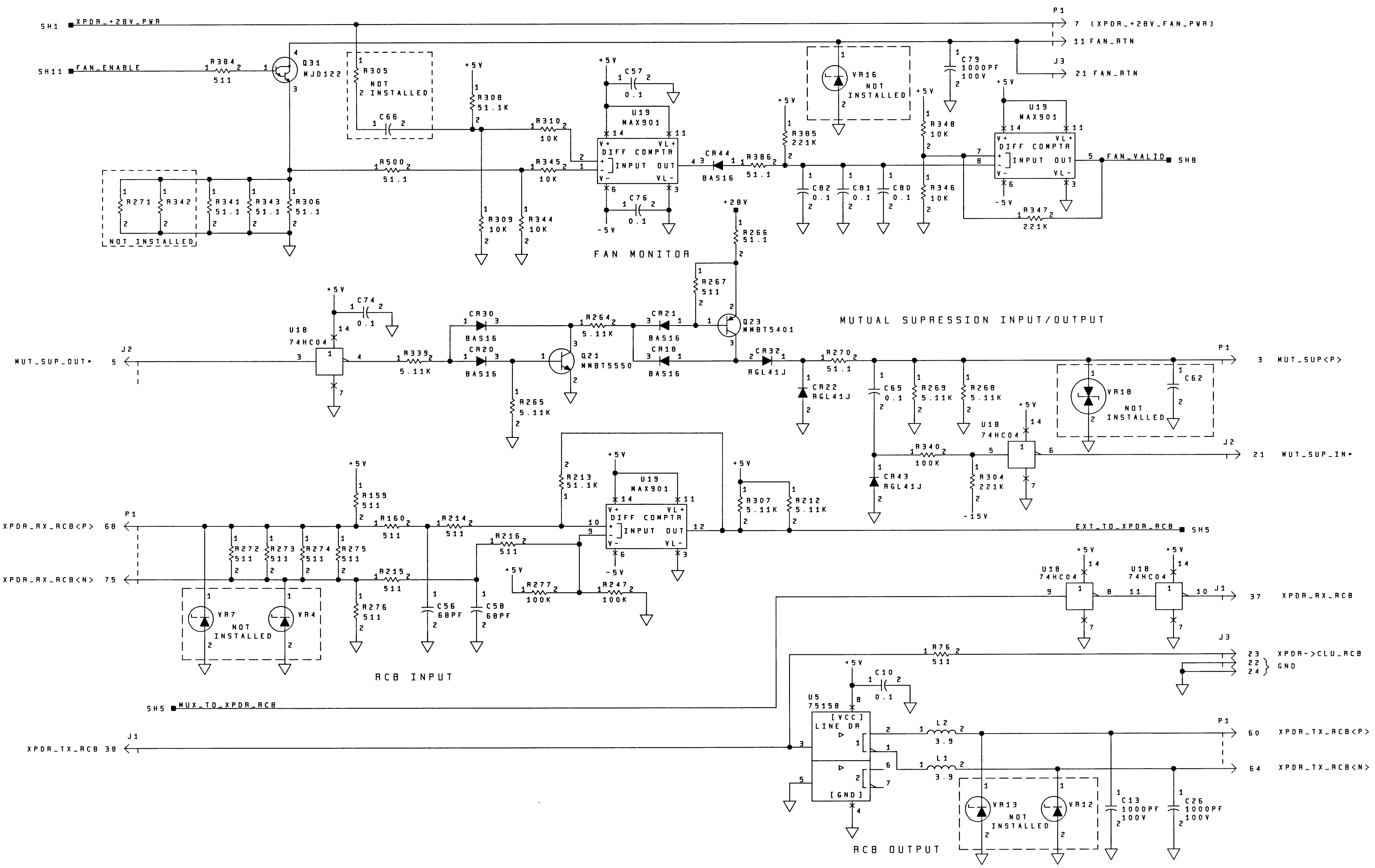
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DISCRETE OUTPUTS  
SCHEMATIC DIAGRAM SHEET NO. 9

COMPUTER GENERATED

ZONE	REV	DESCRIPTION	DATE	APPROVED
A	1	UDTD SCH. C62 WAS INSTALLED. SEE DWG SH 1.	99460(U)	<i>[Signature]</i>

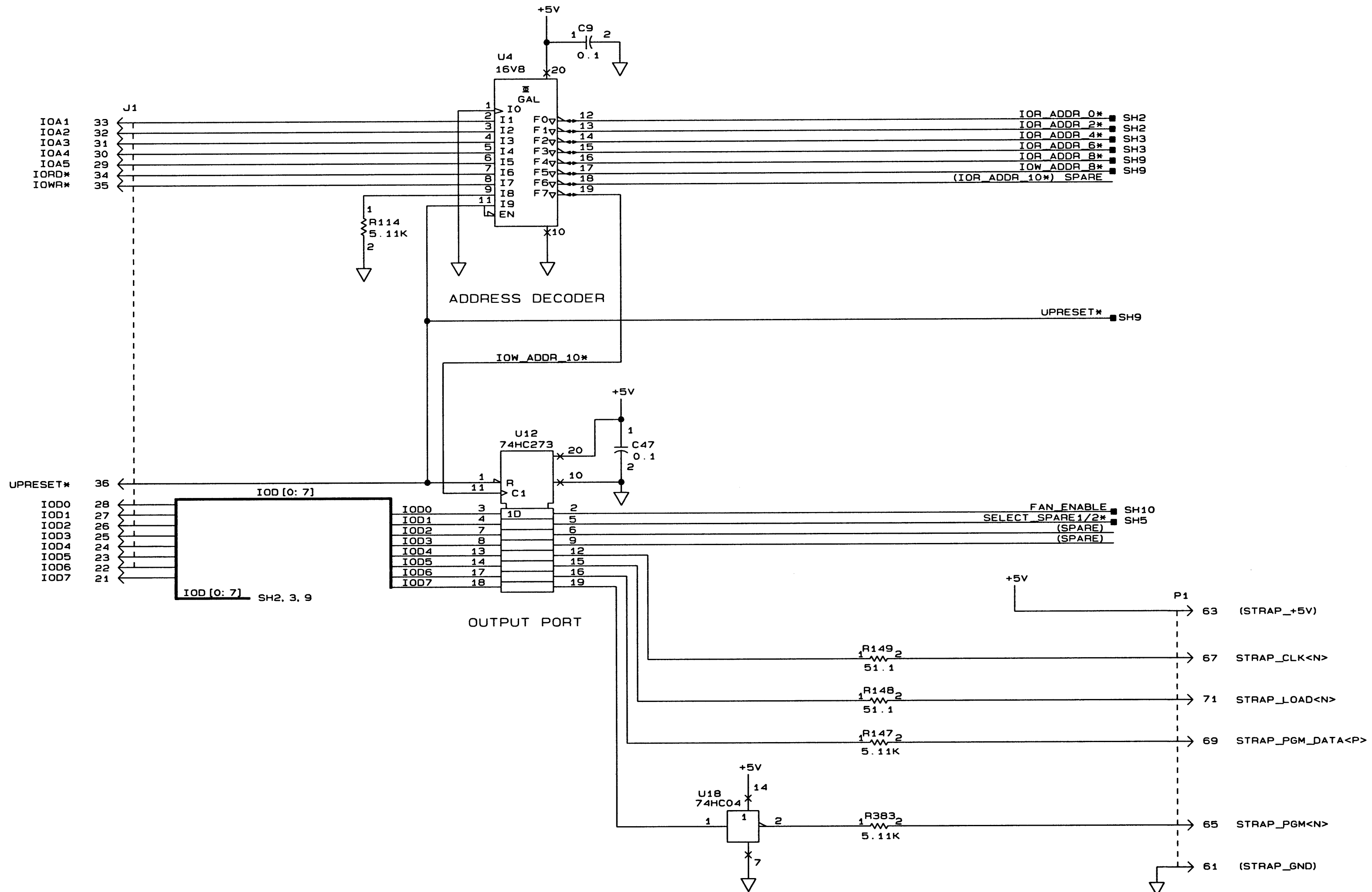


MUT SUP / RCB OUTPUT / FAN CKT  
SCHEMATIC DIAGRAM SHEET NO. 10

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	E 55939	DRAWING NO.	7517410-903	REV	A
	SCALE					SHEET	12

REV 7517410-903 12



STRAP CARD CIRCUIT  
SCHEMATIC DIAGRAM SHEET NO. 11

COMPUTER GENERATED

REV 13

INTF TO REAR CONNECTOR

		+5V			
SH10	XPDR_SPARE_4	1	2	GND	SH1
SH10	MUT_SUP<P>	3	4	GND	SH1
SH10	XPDR_SPARE_3	5	6	GND	SH1
SH10	XPDR +28V_PWR (XPDR +28V_FAN_PWR)	7	8	GND	SH1
SH10	(SPARE_302)	9	10	XPDR->DLPCD_429A	SH4
SH10	FAN RTN (SPARE_301)	11	12	XPDR->DLPAB_429B	SH4
SH6	XPDR->TCAS_429A	13	14	XPDR->DLPAB_429A	SH4
SH8	TCAS->XPDR_429A	15	16	XPDR->TCAS_429B	SH6
SH2	ENC_ALT1_C1<NO>	17	18	DLPAB->XPDR_429B	SH8
SH2	ENC_ALT1_A2<NO>	19	20	DLPAB->XPDR_429A	SH8
SH3	ENC_ALT2_C1<NO>	21	22	TCAS->XPDR_429B	SH8
SH3	ENC_ALT2_A2<NO>	23	24	ENC_ALT1_C2<NO>	SH2
SH3	ENC_ALT2_C2<NO>	25	26	ENC_ALT1_C4<NO>	SH2
SH3	ENC_ALT2_A4<NO>	27	28	ENC_ALT1_A4<NO>	SH2
SH3	ENC_ALT2_C4<NO>	29	30	ENC_ALT1_D2<NO>	SH2
SH3	ENC_ALT2_A4<NO>	31	32	ENC_ALT1_B1<NO>	SH2
SH3	ENC_ALT2_B1<NO>	33	34	ENC_ALT1_D4<NO>	SH2
SH3	ENC_ALT2_D2<NO>	35	36	ENC_ALT1_B2<NO>	SH2
SH3	ENC_ALT2_B2<NO>	37	38	ENC_ALT1_A1<NO>	SH2
SH3	ENC_ALT2_D4<NO>	39	40	ENC_ALT1_B4<NO>	SH2
SH3	ENC_ALT2_B4<NO>	41	42	ALT_COMP_ENA<NO>	SH2
SH3	ENC_ALT2_A1<NO>	43	44	XPDR_STANDBY<NO>	SH2
SH3	XPDR_ACTIVE<NO>	45	46	ALT_SRC_SEL2<NO>	SH2
SH9	RSV_INPUT_1<NO>	47	48	SQUAT_SWITCH1<NO>	SH3
SH9	XPDR_VALID<NO>	49	50	CTL_SRC_SEL1<NO>	SH2
SH9	RSV_INPUT_2<NO>	51	52	XPDR_OFF<NO>	SH1
SH9	RSV_OUTPUT_1<NO>	53	54	XPDR->RSV2_429A	SH5
SH9	RSV_INPUT_3<NO>	55	56	SQUAT_SWITCH2<NO>	SH2
SH3	ALT_VALID<NO>	57	58	XPDR->RSV2_429B	SH5
SH3	RSV_INPUT_4<NO>	59	60	XPDR_TX_RCB<P>	SH10
SH3	GND (STRAP_GND)	61	62	FMS->XPDR_429A	SH6
SH11	(STRAP +5V)	63	64	XPDR_TX_RCB<N>	SH10
SH11	STRAP_PGM<N>	65	66	CTL2->XPDR_429A	SH7
SH11	STRAP_CLK<N>	67	68	XPDR_RX_RCB<P>	SH10
SH11	STRAP_PGM_DATA<P>	69	70	CTL2->XPDR_429B	SH7
SH11	STRAP_LOAD<N>	71	72	FMS->XPDR_429B	SH6
SH9	STRAP_DATA<P>	73	74	XPDR->RSV1_429B	SH5
SH10	XPDR_RX_RCB<N>	75	76	RSV2->XPDR_429A	SH6
SH7	CTL1->XPDR_429A	77	78	XPDR->RSV1_429A	SH5
SH7	(SPARE_306)	79	80	RSV1->XPDR_429B	SH8
SH7	CTL1->XPDR_429B	81	82	RSV1->XPDR_429A	SH8
SH7	(SPARE_305)	83	84	ADC2->XPDR_429B	SH7
SH9	XPDR_VALID<PQ>	85	86	ADC1->XPDR_429B	SH7
SH6	RSV2->XPDR_429B	87	88	ADC2->XPDR_429A	SH7
SH1	XPDR_RX_RS232	89	90	ADC1->XPDR_429A	SH7
SH1	PROGRAM +15V	91	92	XPDR->DLPCD_429B	SH4
SH1	PROGRAM_ENA<PO>	93	94	DLPCD->XPDR_429B	SH8
SH1	XPDR_TX_RS232	95	96	DLPCD->XPDR_429A	SH8
SH1	XPDR +28V_PWR	97	98	XPDR +28V_PWR	SH1
SH1	XPDR +28V_PWR	99	100	XPDR +28V_PWR	SH1

INTF TO PROC AT END OF PWB

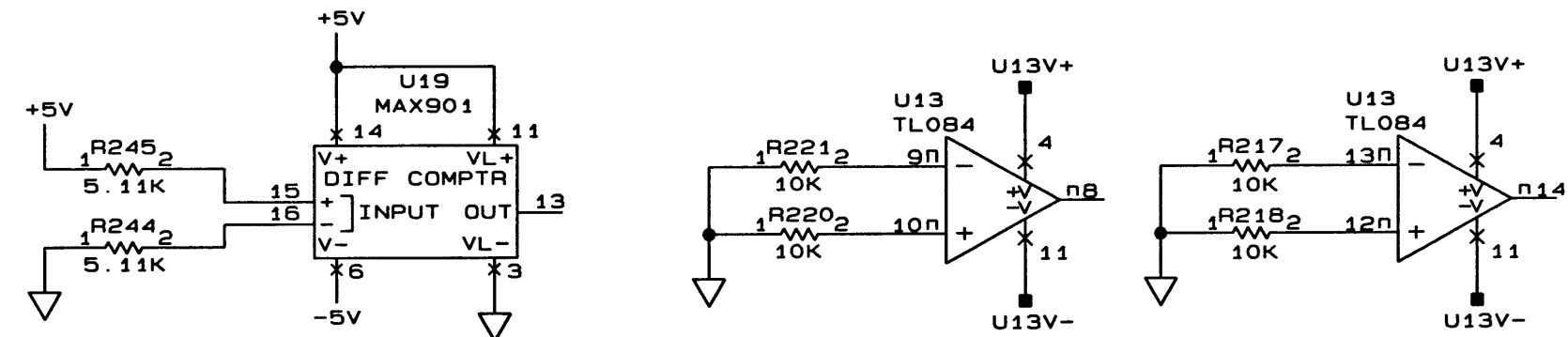
		+5V			
		J1 CONN40			
SH1	GND	1	2	GND	SH1
SH1	PS +15V	5	6	PS +15V	SH1
SH1	-15V	7	8	-5V	SH1
SH1	GND	9	10	GND	SH1
SH1	GND	11	12	GND	SH1
SH1	XPDR +28V_PWR	13	14	XPDR +28V_PWR	SH1
SH1	XPDR +28V_PWR	15	16	XPDR +28V_PWR	SH1
SH1	(SPARE_119)	17	18	XPDR_OFF<NO>	SH1
SH1	PLZN_PLUG	19	20	(SPARE_101)	SH1
SH11	IOD7	21	22	IOD6	SH11
SH11	IOD5	23	24	IOD4	SH11
SH11	IOD3	25	26	IOD2	SH11
SH11	IOD1	27	28	IOD0	SH11
SH11	IOA5	29	30	IOA4	SH11
SH11	IOA3	31	32	IOA2	SH11
SH11	IOA1	33	34	IORD*	SH11
SH11	IOWR*	35	36	UPRESET*	SH11
SH10	XPDR_RX_RCB (SPARE_104)	37	38	XPDR_TX_RCB +28V	SH10
		39	40		SH1

INTF TO PROC IN MIDDLE OF PWB

		J2 CONNECTOR			
SH1	PROGRAM +15V	1	2	(SPARE_102)	
SH1	PROGRAM_ENA<PO>	3	4	(SPARE_106)	
SH10	MUT_SUP_OUT*	5	6	(SPARE_108)	
SH10	(SPARE_107)	7	8	(SPARE_106)	
SH10	(SPARE_109)	9	10	(SPARE_110)	
SH10	(SPARE_111)	11	12	(SPARE_112)	
SH10	(SPARE_113)	13	14	(PLZN_PLUG)	
SH10	(SPARE_115)	15	16	(SPARE_116)	
SH10	(SPARE_117)	17	18	(SPARE_118)	
SH1	XPDR_RX_RS232	19	20	XPDR_TX_RS232	SH1
SH10	MUT_SUP_IN*	21	22	(SPARE_103)	
SH7	ADC2->XPDR_B	23	24	ADC2->XPDR_A	SH7
SH7	CTL2->XPDR_B	25	26	CTL2->XPDR_A	SH7
SH7	ADC1->XPDR_A	27	28	ADC1->XPDR_B	SH7
SH7	CTL1->XPDR_A	29	30	CTL1->XPDR_B	SH7
SH8	DLPAB->XPDR_A	31	32	DLPAB->XPDR_B	SH8
SH8	TCAS->XPDR_A	33	34	TCAS->XPDR_B	SH8
SH8	DLPCD->XPDR_A	35	36	DLPCD->XPDR_B	SH8
SH8	RSV1->XPDR_A	37	38	RSV1->XPDR_B	SH8
SH6	RSV2->XPDR_A	39	40	RSV2->XPDR_B	SH6
SH6	FMS->XPDR_A	41	42	FMS->XPDR_B	SH6
SH4	XPDR->DLPAB_A	43	44	XPDR->DLPAB_B	SH4
SH4	XPDR->TCAS_A	45	46	XPDR->RSV_B	SH4
SH4	XPDR->RSV_A	47	48	XPDR->DLPCD_B	SH4
SH4	XPDR->DLPCD_A	49	50	XPDR->TCAS_B	SH4

XPDR TO CLUSTER

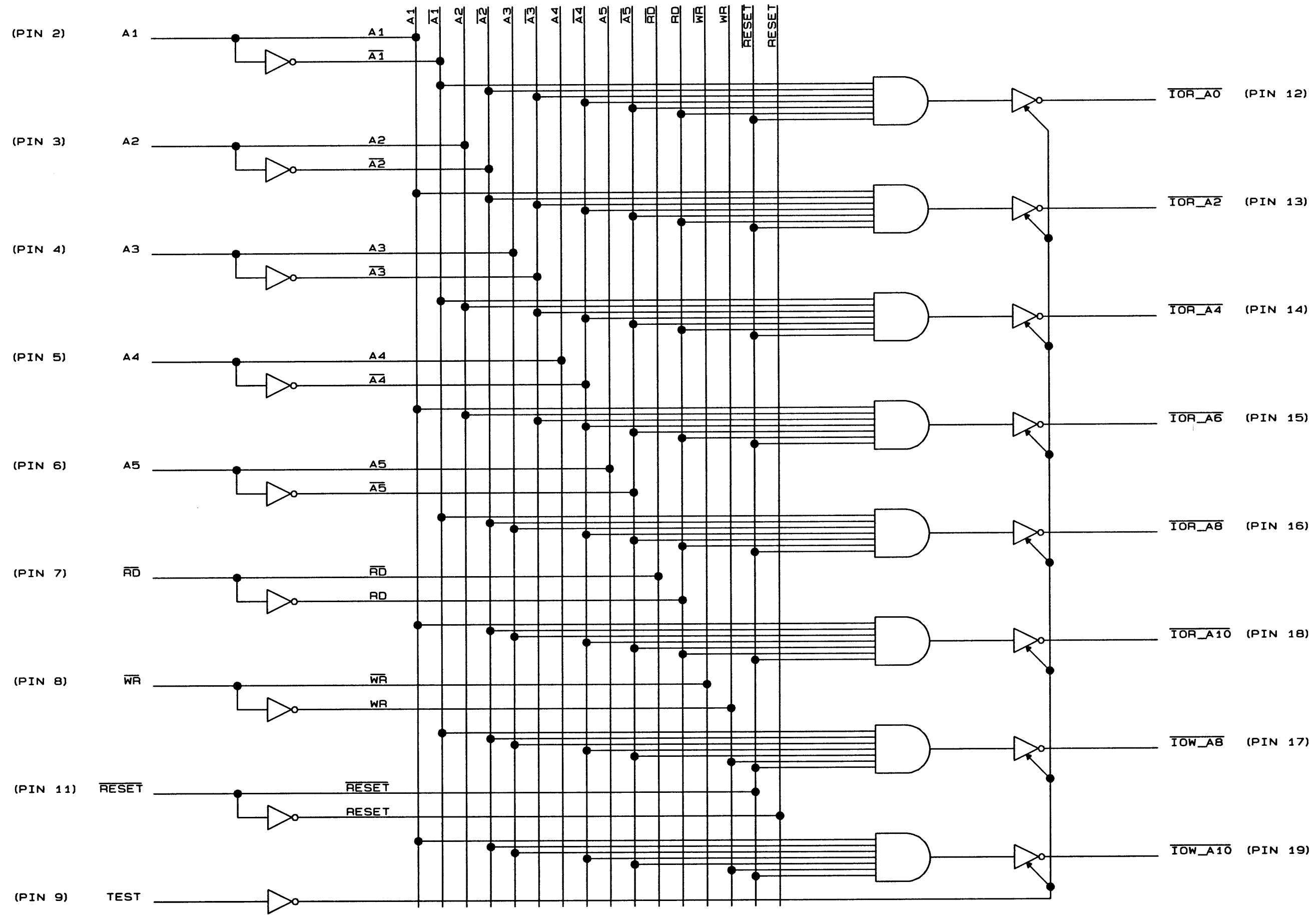
		J3 CONNECTOR			
		30			
1		16			(PLZN PLUG)
2		17			
3		18			
4		19			
5		20			INTEGRATED_COM* SH10
6		21			FAN RTN SH10
7		22			GND SH10
8		23			XPDR->CLU_RCB SH10
9		24			GND SH10
10		25			CLU->XPDR_RCB SH5
11		26			GND SH5
12		27			PS +15V (FAN_SUPPLY) SH1
13		28			
14		29			
15		30			



SPARES

CONNECTOR PIN SUMMARY  
 AND SPARE COMPONENTS.  
 CAD REQUIREMENT/REFERENCE.  
 SCHEMATIC DIAGRAM SHEET NO. 12

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PAL LOGIC DIAGRAM  
SCHEMATIC DIAGRAM SHEET NO. 13

COMPUTER GENERATED

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	SCALE	SHEET 15		

REV 7517410-903 15