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TITLE: INTEGRATED TEST SPECIFICATION FOR MODE S TRANSPONDER, PART NO. 4061400-9XX

1. SCOPE

This Integrated Test Specification (IT) establishes the manufacturing and operational requirements that the Mode S Transponder, Part No. 4061400-9XX, must meet to ensure that the unit is in proper operating condition.

2. REFERENCE DOCUMENTS

These reference documents are not required for performance of the test procedure. The purpose of listing these documents is to provide an aid for troubleshooting should any discrepancies occur during the performance of the test procedure.

4061400-9XX	End Item Drawing - Mode S Transponder
4061401	Outline and Installation Drawing
64980-90912	HP 64000 System Overview Reference
64980-90928	HP 64000 System Software Reference
1002-6801-000	IFR 1403 Operation/Maintenance Manual
C35-3641-05	System Control Panel User's Manual

3. GENERAL INFORMATION

3.1 All tests shall be performed under the following conditions:

Temperature = 25 ± 5 C Relative humidity = 95% maximum Pressure = between 20 and 32 inHg

3.2 Tests 1 thru 10 shall be performed with the HP 64000/UDE or PC/CPI emulation system attached to the top connector of the unit under test (UUT) A1 CCA (refer to figure 1). All subsequent tests shall be performed in a stand-alone configuration with the emulation system removed or deactivated. Power to the UUT shall be removed before attaching or removing any interconnecting systems.

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	3.3	Notes on operating th	e HP 64000 emulation system ar	e as follows:						
	3.3.1	All tests requiring the SDP-185 monitor so the 'HW' command f	All tests requiring the HP 64000 emulation system shall be performed while executing the SDP-185 monitor software. In the engineering environment, the monitor is invoked by executing the 'HW' command file from within the MODESP user ID of the HP 64000 system.							
	3.3.2	All address and data indicates the data is	a information should be entered in in hexadecimal). See example i	n Hex with a leading 0 and trailing H n 3.3.3.	(the H					
	3.3.3	Most emulation com CRT. Only hexaded the HP 64000. For e (Hex), the following	immands are invoked by pressing HP 64000 softkeys, found directly below the ecimal addresses and data are typed using the standard typewriter keypad of or example, to change the contents of location C000 (Hex) to the value AAAA g HP 64000 command would be entered:							
		modify memory 0C	000H to 0AAAAH							
		In this example, the softkeys.	e words (modify, memory, and to	 are entered with single-keystroke 						
	3.4	Notes on operation of	the PC emulation system are as	follows:						
	3.4.1	All tests requiring the PC emulation system shall be performed while executing the SDP-185 monitor software. In the engineering environment, the monitor is invoked by executing the 'SCP MDS' command file.								
	3.4.2	All address and data and a leading 1# for	address and data information should be entered in Hex with a leading 0# for CACHE locations a leading 1# for local memory. See example in 3.4.3.							
	3.4.3	Most emulation com 'MDS' are typed usir location C000 (Hex)	mands are invoked by pressing t ng the standard typewriter keys. to the value AAAA (Hex), the foll	he softkeys. Only addresses, data, a For example, to change the contents lowing PC command would be entere	and of ed:					
		modify memory MD	S word 0#C000 to AAAA							
		In this example, the softkeys.	e words (modify, memory, word,	and to) are entered with single-key	vstroke					
	3.5	When testing diversity conversion is consisted be correlated to the ad be connected to eithe	v, the two antenna signals are ref ent with that used by the IFR trans ctual top and bottom antenna sig r ANT A port or ANT B port of the	erred to as ANT A and ANT B. This sponder test equipment and does no nals. In other words, the TOP ANT c IFR equipment.	t need to able can					
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	3.6	Standard rate and pov	ver for ATCRBS and Mode S	interro	ogatio	ns are defined as follows:	
		ATCRBS: 450 Hz, Mode S: 45 Hz,	-50 dBm -50 dBm				
	3.7	Input discrete signal s	tandards are as follows:				
		GND - <3.5 V at 1 OPEN - >100k ohm +5V - 5 ± 1 V dc +28V - >18 V dc	00 mA s at 20 mA				
	3.8	Output discrete signal	standards are as follows:				
		GND - <3.5 V dc OPEN - >100k ohm +28V - >18 V dc	S				
	3.9	Notes on entering dat	a into the IFR S-1403 are list	ed belo	ow:		
	3.9.1 Data is entered by use of the cursor keys, slew knob, and numeric keypad. Use the cursor keys to move between input fields (the cursor will not move to an output/display field). Once in the desired field, use the slew knob and/or numeric keypad to alter the data.						
	3.9.2	9.2 Some fields require the data to toggle through one or more states (for example, the SPR: ON/OFF field). Use the ON/CAL button to toggle such fields.					
	3.9.3	Some input fields ca field). These fields r slew knob and nume	n be moved through a range nust first be toggled out of th eric keypad can then be used	of nun e CAL to pro	neric state vide t	values (for example, the D and into the data entry mo he desired value.	v=CAL de. The
	3.9.4	Some fields require example, the UF= fields	the ENTER key to be presse eld).	d after	the re	equired data has been sele	cted (for
	3.10	Notes and a list of HP	64000 command files (Hone	ywell F	Part N	lo. MT4061400-902) are a	s follows:
	3.10.1 The total execution time of this Integrated Test Procedure may be greatly reduced by using the HP 64000 command files listed in paragraph 3.10.3. These command files automatically execute many keystrokes found in certain tests and match statement-for-statement (excluding 'wait' statements) the steps found in the IT work steps.						sing the HP ‹ecute vait'
	3.10.2	After executing the ' filename (using all c allow the operator to	HW' batch file (see paragrapl aps) into the HP 64000 and p record data. Press any key	h 3.3), pressin to rest	execu g RE ⁻ ume c	ute the command file by ty FURN. Most command file command file execution.	bing the s pause to
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	3.10.3	IT TEST NO.	HP 64000 Command Flien	anne					
		1	IT10 IT20						
		3	IT30 (See note 1.)						
		4	IT40						
		6	IT60						
		7	IT70 (Shall be executed twi	ce to comp	lete test 7.)				
		9	IT81						
		10	IT90						
			11 100 (See note 2.)						
	NOTE 1: Before running IT30, test 3 must be performed. IT30 must also be performed at the completion of the IT to clear E ² .								
	NOTE 2: Before running IT100, end out of emulation mode by entering "END" into the HP 64000.								
	3.11	11 Notes and a list of PC command files (Honeywell Part No. MT4061400-101, Revision A) are as follows:							
	3.11.1	The total execution command files listed keystrokes found in statements) the ster	time of this Integrated Test P d in paragraph 3.11.3. These certain tests and match state os found in the IT work steps	rocedure n command ement-for-s	nay be greatly reduced by us files automatically execute r tatement (excluding 'pause'	ing the PC nany			
	3.11.2	After executing the	SCP MDS' batch file (see pa	ragraph 3.4	 execute the command file 	e by typing			
		the filename (using allow the operator to	all caps) into the PC and presoned and presoned data. Press any key	ssing ENTE to resume	ER. Most command files pau command file execution.	use to			
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	3.11.3	<u>IT Test No.</u>	PC Command Filename					
		1	IT10					
		2 3	IT30 (See note 1.)					
		4	IT40					
		5 6	IT50 IT60					
		7	IT70 (Shall be executed twi	ce to co	omple	te test 7.)		
		8 9 1						
		9	IT90					
		10	IT100 (See note 2.)					
		NOTE 1: Before r the com	Before running IT30, test A3 must be performed. IT30 must also be performed at the completion of the IT to clear E2.					
	NOTE 2: Before running IT100, end out of emulation mode by entering "END" into the PC. (Resume testing at test 10.13.)						the PC.	
	3.12 This IT conforms to Specification ATLAS ATP SA4061400-905, Revision A or later.							
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REV LTR							
	Та	ble 1. Initial IFR ATC	-1400A C	Control S	ettings		
	Switch		Initial Se	etting			
	RF LEVEL control		-70 dBm	1			
	CW/NORM/OFF switch		OFF				
	SUPPRESSOR ON/OFF switch		OFF				
	SLS/ECHO ON/OFF switch		OFF				
	XPDR PULSE WIDTH VAR/CA	L switch	CAL				
	FREQ STEP RATE control		OFF				
	MAN/AUTO/MAN STEP switch		MAN				
	XPDR DEV P3/CAL switch		CAL				
	1.0µs/1.45µs switch		1.45 µs				
	XPDR DEV P2/CAL switch		CAL				
	TO/TAC/TD switch		TD				
	PRF/SQTR ON/OFF switch		ON				
	F2/P2 F1/P1 switch		F2/P2				
	XPDR MODE control		А				
	DISPLAY SELECT control		FREQ MHz				
	PRF/SQTR thumbwheels		45 Hz				
	DBL INTERR/INTERF PULSE to	047.9 µs	S OFF				
	XPDR P2/P3 DEV thumbwheels	0.20 µs					
	FREQ/FUNCTION SELECT thu	mbwheels	1030 MHz XPDR				
	DELTA F thumbwheels		0.00 MHz OFF				
	XPDR PULSE WIDTH thumbwh	neels	0.00 µs				
	SLS/ECHO thumbwheels		+0 dB				
	DECODER (at rear of unit)		NARRO	W			
	NOTE: If unit is cold, provide a until the FREQ DISPLA	a 5-minute warmup per AY indicates 1030.00 N	riod. Adju MHz.	ust the FF	REQ and DELTA F thumbv	vheels	
		Initial IFR S-1403	B Control	Settings	;		
	Menu	Sub-Menu/Field			Initial Settings		
	Sequence	S01			UF = 5		
					ADD = 25252525		
		S02 thru S16:			OFF		
	Control Menu 1	Func:			1 ATC (ATCRBS)		
		RF LvI:			+0.0		
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Table 2. Initial Mode S Transponder (UUT) Configuration

SECURITY NOTATION

(Mode S Interface Panel)

Signal Name	Switch Position	Signal Sense	UUT Rear Connector Pin No.
MODE S ADDRESS A1	0	OPEN	J1B-1A
A2	1	GND	J1B-1B
A3	0	OPEN	J1B-1C
A4	1	GND	J1B-1D
A5	0	OPEN	J1B-1E
A6	1	GND	J1B-1F
A7	0	OPEN	J1B-1G
A8	1	GND	J1B-1H
A9	0	OPEN	J1B-1J
A10	1	GND	J1B-1K
A11	0	OPEN	J1B-2A
A12	1	GND	J1B-2B
A13	0	OPEN	J1B-2C
A14	1	GND	J1B-2D
A15	0	OPEN	J1B-2E
A16	1	GND	J1B-2F
A17	0	OPEN	J1B-2G
A18	1	GND	J1B-2H
A19	0	OPEN	J1B-2J
A20	1	GND	J1B-2K
A21	0	OPEN	J1B-3A
A22	1	GND	J1B-3B
A23	0	OPEN	J1B-3C
A24	1	GND	J1B-3D

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RE\ LTF

		(Mode S	Interface Pane	el)	
Signal Name		Switch Position	Signal Sense	UUT Rear Conne Pin No.	ctor
ANT PGM		2	OPEN	J1A-6K	
ANT CABLE DLY	T/B	вот	GND	J1A-3C	
	В	1	OPEN	J1A-3D	
	А	0	GND	J1A-3E	
GILHAM ALT 1	A1	1	GND	J1B-4A	
	A2	0	OPEN	J1B-4B	
	A4	1	GND	J1B-4C	
	B1	0	OPEN	J1B-4D	
	B2	1	GND	J1B-4E	
	B4	0	OPEN	J1B-4F	
	C1	1	GND	J1B-4G	
	C2	0	OPEN	J1B-4H	
	C4	1	GND	J1B-4J	
	D2	0	OPEN	J1B-4K	
	D1	1	GND	J1B-5K	
GILHAM ALT 2	A1	0	OPEN	J1A-1A	
	A2	1	GND	J1A-1B	
	A4	0	OPEN	J1A-1C	
	B1	1	GND	J1A-1D	
	B2	0	OPEN	J1A-1E	
	B4	1	GND	J1A-1F	
	C1	0	OPEN	J1A-1G	
	C2	1	GND	J1A-1H	
	C4	0	OPEN	J1A-1J	
	D2	1	GND	J1A-1K	
	D4	0	OPEN	J1A-2K	
ANT BITE		ON	GND	J1B-5J	
		AW/CRITICAL N			

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Table 2(Cont). Initial Mode S Transponder (UUT) Configuration

SECURITY NOTATION

(Mode S Interface Panel)						
Signal Name		Switch Position	Signal Sense	UUT Rear Connector Pin No.		
FUNC TEST		ON	GND	J1B-3H		
STBY/ON		ON	OPEN	J1A-7G		
SYNC ALT FLAG N	o. 1	INV	OPEN	J1A-4J		
SYNC ALT FLAG N	o. 2	VLD	+18 V	J1B-7J		
ALT COMP		ON	GND	J1B-5G		
ALT SRC		1	OPEN	J1B-6E		
ALT TYP SEL	В	1	OPEN	J1B-6F		
	А	0	GND	J1B-6G		
MAX AIRSPEED	15	1	OPEN	J1A-5A		
	16	0	GND	J1A-5B		
	17	1	OPEN	J1A-5C		
SDI	В	1	OPEN	J1A-3G		
	А	0	GND	J1A-3H		
DATA LINK		DLP	GND	J1B-5H		
CNTL PNL		В	OPEN	J1A-7D		
AIR/GND No. 1		AIR	OPEN	J1A-5K		
AIR/GND No. 2		AIR	OPEN	J1A-5J		

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Signal Name MODE S ADDRESS A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A10 A11 A12 A13 A10 A11 A12 A13 A14 A15 A16 A17 A18 A18	Switch Position 1 0 1 1	Signal Sense GND OPEN GND OPEN GND OPEN GND OPEN GND OPEN GND OPEN GND
MODE S ADDRESS A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18	1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0	GND OPEN GND OPEN GND OPEN GND OPEN GND OPEN GND OPEN GND
A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18	0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1	OPEN GND OPEN GND OPEN GND OPEN GND OPEN GND OPEN GND
A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18	1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1	GND OPEN GND OPEN GND OPEN GND OPEN GND OPEN GND
A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18	0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1	OPEN GND OPEN GND OPEN GND OPEN GND OPEN GND OPEN GND
A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18	1 0 1 0 1 0 1 0 1 0 1 0 1	GND OPEN GND OPEN GND OPEN GND OPEN GND OPEN GND
A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18	0 1 0 1 0 1 0 1 0 1 0 1	OPEN GND OPEN GND OPEN GND OPEN GND
A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18	1 0 1 0 1 0 1 0 1	GND OPEN GND OPEN GND OPEN GND
A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18	0 1 0 1 0 1 0 1	OPEN GND OPEN GND OPEN GND GND
A9 A10 A11 A12 A13 A14 A15 A16 A17 A18	1 0 1 0 1 0 1	GND OPEN GND OPEN GND GND
A10 A11 A12 A13 A14 A15 A16 A17 A18	0 1 0 1 0 1	OPEN GND OPEN GND OPEN GND
A11 A12 A13 A14 A15 A16 A17 A18	1 0 1 0 1	GND OPEN GND OPEN GND
A12 A13 A14 A15 A16 A17 A18	0 1 0 1	OPEN GND OPEN GND
A13 A14 A15 A16 A17 A18	1 0 1	GND OPEN GND
A14 A15 A16 A17 A18	0 1	OPEN
A15 A16 A17 A18	1	GND
A16 A17 A18		ONE
A17 A18	0	OPEN
A18	1	GND
	0	OPEN
A19	1	GND
A20	0	OPEN
A21	1	GND
A22	0	OPEN
A23	1	GND
A24	0	OPEN
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(Mode S Interface Panel)						
Signal Name		Switch Position	Signal Sense			
ANT PGM		1	GND			
ANT CABLE DLY	T/B	TOP	OPEN			
	В	0	GND			
	А	1	OPEN			
GILHAM ALT 1	A1	0	OPEN			
	A2	1	GND			
	A4	0	OPEN			
	B1	1	GND			
	B2	0	OPEN			
	B4	1	GND			
	C1	0	OPEN			
	C2	1	GND			
	C4	0	OPEN			
	D2	1	GND			
	D4	0	OPEN			
GILHAM ALT 2	A1	1	GND			
	A2	0	OPEN			
	A4	1	GND			
	B1	0	OPEN			
	B2	1	GND			
	B4	0	OPEN			
	C1	1	GND			
	C2	0	OPEN			
	C4	1	GND			
	D2	0	OPEN			
	D4	1	GND			

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	Table 3(Cont).	Comple	mented Mode S T	ransponde	r (UUT) Configuration	
			(Mode S Interface	Panel)		
	Signal Name		Switch Positi	ion	Signal Sense	9
	ANT BITE		OFF		OPEN	
	FUNC TEST		OFF		OPEN	
	STBY/ON		STBY		GND	
	SYNC ALT FLAG No. 1		VLD		+18 V	
	SYNC ALT FLAG No. 2		INV		OPEN	
	ALT COMP		OFF		OPEN	
	ALT SRC		2		GND	
	ALT TYP SEL	В	0		GND	
		А	1		OPEN	
	MAX AIRSPEED	15	0		GND	
		16	1		OPEN	
		17	0		GND	
	SDI	В	0		GND	
		А	1		OPEN	
	DATA LINK		DL		OPEN	

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А

GROUND

GROUND

GND

GND

GND

CNTL PNL

AIR/GND No. 1

AIR/GND No. 2

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Address	Modify to
C000H thru C006H	0000H
C007H thru C00DH	5555H
COOEH	D457H
C00FH	AAAAH
C01FH	FFFFH
C020H	AAAAH
C021H	AAAAH
C022H	5555H
C023H	5555H
C024H	000DH
C025H	009EH
C026H	00BFH
C028H	002AH
C02AH	5500H
C02BH	FFAAH
C02FH	0007H
TE 1: Read 1DW4 (location 0D604)	

NOTE 3: Copy bits 4 thru 7 of 1DW4 plus 01DH into bits 0 thru 5 of RF 0DW17(location 0C029).

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Table 5. Initial Settings For Mode S And ATCRBS (Mode S SW Running) (Mode S Interface Panel) Signal Name Switch Position Signal Sense MODE S ADDRESS A1 0 OPEN A2 1 GND A3 0 OPEN A4 1 GND A5 0 OPEN A6 GND 1 A7 0 OPEN A8 1 GND A9 0 OPEN A10 GND 1 OPEN A11 0 A12 GND 1 A13 0 OPEN A14 1 GND A15 OPEN 0 A16 GND 1 A17 0 OPEN A18 GND 1 A19 0 OPEN A20 GND 1 OPEN A21 0 A22 1 GND A23 0 OPEN A24 1 GND ANT PGM 2 OPEN T/B OPEN ANT CABLE DELAY BOT В 1 OPEN А 1 OPEN ANT BITE ON GND FUNC TEST OFF OPEN STBY/ON ON OPEN **AW/CRITICAL NOTATION** Honeywell 14 SUPPLEMENTS

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	(Mode S Interface Par	nel)	
Signal Name		Switch Position	Signal Sense	
ALT COMP		OFF	OPEN	
ALT SRC SEL		2	GND	
ALT TYPE SEL	В	1	OPEN	
	А	1	OPEN	
CNTL PNL		В	OPEN	
AIR/GND	1	GROUND	GND	
AIR/GND	2	AIR	OPEN	
capability):	<u>SO</u>	URCE 1	SOURCE 2	
Transmit speed: Transmit parity: Transmit rate: 429 Label: 429 Data: Connect to: (On MDS interface panel)	LOW ODD 168 m 016 (c 1FFE4 CNTL	is octal) 48 (Hex) DATA B	LOW ODD 50 ms 203 (octal) 627740 (Hex) 429 ADC 1 and 429 ADC 2	
NOTE 3: Run Mode S softwa reenergizing the uni	re by remov t.	ing or disabling the	UDE or CPI interface from the UUT	and
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		Table 6. Initial 429 Setup	(Inte	ernal V	Vrap)			
	Config Reg	Address			Modify To			
	0	0D218H			08888H			
	1	0D21AH			08888H			
	2	0D21CH			00000H			
	3	0D400H			0FFE0H			
	N/A	0D220H thru 0D22EH			0FFAAH			
	Та	ble 6.1. Walking 1S 429 Se	tup	(Intern	nal Wrap)			
	Config Reg	Address			Modify To			
	0	0D218H			08888H			
	1	0D21AH			08888H			
	2	0D21CH			00000H			
	3	0D400H			0FFE0H			
	N/A	0D220H thru 0D22EH			00000H			
		Table 7. Initial 429 Setup	(Ext	ernal \	Wrap)			
	Config Reg	Address			Modify To			
	0	0D218H			08888H			
	1	0D21AH			088AAH			
	2	0D21CH			00090H			
	3	0D400H			0FF76H			
	N/A	0D220H thru 0D22EH			0FF00H			
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	From			То	
Signal Name		UUT Rear Connector Pin No.	Signal Name		UUT Rear Connector Pin No.
DATA LINK OUT	А	J1B-5E	DATA LINK IN	А	J1A-2A
	В	J1B-5F		В	J1A-2B
XT COORD	A	J1A-5G	TX COORD	A	J1A-5E
	В	J1A-5H		В	J1A-5F
MAINT DATA IN	A	J1B-6A	FLT ID	А	J1A-6A
	В	J1B-6B		В	J1A-6B
MAINT DATA OUT	А	J1B-6C	MAINT DATA IN	А	J1A-6A
	В	J1B-6D		В	J1A-6B
429 ADC #1	А	J1A-7H	CNTL DATA A	А	J1A-7A
	В	J1A-7J		В	J1A-7B
429 ADC #2	A	J1B-5A	CNTL DATA B	A	J1A-7E
	В	J1B-5B		В	J1A-7F
FLT ID	A	J1A-6A	CNTL DATA A	A	J1A-7A
	В	J1A-6B		В	J1A-7B
CNTL DATA A	А	J1A-7A	CNTL DATA B	А	J1A-7E
	В	J1A-7B		В	J1A-7F

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4. **POWER REQUIREMENTS**

115 ± 5 V ac, 400 ± 10 Hz

115 ± 5 V ac or 230 ± 10 V ac, 48 to 66 Hz

TEST EQUIPMENT 5.

5.1 **Honeywell Test Equipment**

4066423-902	CPI (modified with connector board, Part No. 4067220)
4067839	Mode S Interface Panel
4067841-901	RF Cable
4067841-902	RF Cable
4067842	M-S LRU Cooling Tray
4067846	SDP-185 UDE
MT4061400-902	HP 64000/UDE Test Software
MT4061400-101	PC/CPI Test Software

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5.2 **Commercial Test Equipment**

IEEE-488 Cable, 10833A
IFR ATC-1400A Transponder Tester
IFR S-1403 Mode S Tester
HP 64000 System, or equivalent
JCAir 429E ARINC 429 Analyzer, or equivalent
Tektronix 2432A Digital Oscilloscope, or equivalent
Digital Voltmeter, Fluke 77, or equivalent
Omni Spectra, 2021-1314-02, or equivalent zero ohm terminator
Omni Spectra, 3082-2240-00, Type N-SMA or equivalent connector
IBM PC, or equivalent, with a Ziatech IEEE-488 interface card, ZT1444A

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6. TEST SETUP

- **6.1** Turn on the commercial test equipment and allow it to warm up. Verify that the test equipment used is calibrated and working properly.
- **6.2** Connect the UUT to the IFR test equipment, Mode S interface panel, and HP 64000 system. (See figure 1.)

7. TEST REQUIREMENTS

7.1 The IFR equipment requires a 5-minute warmup period. No other warmup is required before testing the Mode S Transponder.

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	7.2	The test steps shall be must be performed ag	e performed in the order listed. In gain from the beginning.	n the event of a failure and repair, the	e test						
	7.3	All control settings that are altered during the course of a specific test should be returned to their initial settings before starting a new procedure.									
	7.4	This procedure is intended to be performed by a knowledgeable technician or engineer. It is assumed that the equipment will be energized and deenergized as appropriate when changing connections and setups.									
	7.5	Individual column definitions are as follows:									
		<u>Column</u>		Description							
		Rev Ltr	Revision letters are used to id	entify revised material.							
		Test No.	Tests are numbered in seque	nce.							
		Opr Limits	Unit under test (UUT) shall me the manufacturing facility. Wh corresponding test is not requ	eet these limits when tested at other nen an item is marked OPTIONAL, th ired except as an aid in troubleshoot	than ie ing.						
		Test Description	ers to which the UUT was designed a ifying the input and output signal ired are not repeated for each test, a ious tests also apply.	nd nd							
		Switch Pos	Positions to which switches must be set are listed in required order and are grouped to correspond to applicable work steps.								
		Work Steps	This column defines the opera achieve a result. Set switches performing corresponding wor	ations necessary to perform a test an s to designated positions before k step.	d						
		Mfg Limits	UUT shall meet these limits at final buyoff before customer delivery.								
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8. TEST PROCEDURE

- 8.1 Perform tests 1 thru 28 if using the HP 64000 emulation system.
- 8.2 Perform tests A1 thru A10 in Appendix A if using the PC/CPI emulation system.
- **8.3** When alternative tests A1 thru A10 are completed, perform tests 11 thru 28 to complete the test procedures.

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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	1			SOFTWA While rur SDP-185 (refer to p modify th SDP-185 shown: M CACHE t Modify re EPROM_	ARE LOAD monitor program paragraph 3.3.1), e following registers as Modify register o 1. egister EN to 1.			SOFTWARE LOAD Run the SDP-185 monitor program (refer to paragraph 3.3.1). Modify the following SDP-185 registers as shown: Enter "modify register CACHE to 1" "modify register EPROM_EN to 1"	
	1.1	7FDE:580D 7FDF:EEE0		Observe in progra locations 7FDFH). be as spe	the CRC words m ROM (memory 7FDEH and The values shall ecified for a -902.			Enter "display memory 07FDEH thru 07FDFH" The values shall be as specified for a -902.	7FDE:580D 7FDF:EEE0
	1.2	7FDE:718F 7FDF:3150	Observe in progra locations 7FDFH). be as spo		the CRC words m ROM (memory 7FDEH and The values shall ecified for a -903.			Enter "display memory 07FDEH thru 07FDFH" The values shall be as specified for a -903.	7FDE:718F 7FDF:3150
	1.3	7FDE:0E2D 7FDF:F202	Observe in progra locations 7FDFH). be as spe		the CRC words m ROM (memory 7FDEH and The values shall ecified for a -904.			Enter "display memory 07FDEH thru 07FDFH" The values shall be a specified for a -904.	7FDE:0E2D 7FDF:F202
	1.4	7FDE:4353 7FDF:F5DF		Observe in progra locations 7FDFH). be as spe not MOD	the CRC words m ROM (memory 7FDEH and The values shall ecified for a -905 E.			Enter "display memory 07FDEH thru 07FDFH" The values shall be as specified for a -905 not MOD E.	7FDE:4353 7FDF:F5DF
	1.5	7FDE:6C8B 7FDF:BA1D	Observe t in program locations 7FDFH). be as spe MOD E.		the CRC words m ROM (memory 7FDEH and The values shall ecified for a -905			Enter "display memory 07FDEH thru 07FDFH" The values shall be as specified for a -905 MOD E.	7FDE:6C8B 7FDF:BA1D
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REV	TEST		s	PECIFICATIO	N	PROCEDURE			SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	2			<u>CACHE I</u> Modify re 0.	RAM egister CACHE to			CACHE RAM Modify register CACHE to 0.	
				If this IT performe automate complete	is being d using an ed test facility, e test 2.1.			If this IT is being performed using an automated test facility, complete test 2.1.	
			If this IT i performe manual te complete		is being d using the est fixture, e test 2.2.			If this IT is being performed using the manual test fixture, complete test 2.2.	
	2.1	N/A		Automate	ed Procedure			Automated Procedure	N/A
				Write the address (3FFFH) t cache RA 23FFFH)	cache RAM (0000H thru o each location in AM (20000H thru			Write the cache RAM address (0000H thru 3FFFH) to each location in cache RAM (20000H thru 23FFFH).	
	2.1.1	0000H thru 3FFFH	Verify tha RAM loca correct da		at each cache ation contains the ata.			Verify that each cache RAM location contains the correct data.	0000H thru 3FFFH
				Write the each cac (FFFFH t each loca RAM (20 23FFFH)	complement of he RAM address hru C000H) to ation in cache 000H thru			Write the complement of each cache RAM address (FFFFH thru C000H) to each location in cache RAM (20000H thru 23FFFH).	
	2.1.2	FFFFH thru C000H		Verify tha RAM loca correct d	at each cache ation contains the ata.			Verify that each cache RAM location contains the correct data.	FFFFH thru C000H
	2.2	N/A		Manual T	est Procedure			Manual Test Procedure	N/A
				Enter "me 20000H t 0AAAAH	odify memory hru 23FFFH to "			Enter "modify memory 20000H thru 23FFFH to 0AAAAH"	
	2.2.1	ААААН		Enter "dis 20000H t	splay memory hru 2007FH"			Enter "display memory 20000H thru 2007FH"	ААААН
				Enter "modify memory 20000H thru 23FFFH to 05555H"				Enter "modify memory 20000H thru 23FFFH to 05555H"	
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	2.2.2	5555H		Enter "dis 20000H t	splay memory hru 2007FH"			Enter "display memory 20000H thru 2007FH"	5555H
	2.2.3	5555H		Enter "dis 23E80H	splay memory thru 23FFFH"			Enter "display memory 23E80H thru 23FFFH"	5555H
	3	N/A		EEPRON	1			<u>EEPROM</u>	N/A
				Enter "m	odify register ΓΟ 0"			Enter "modify register CACHE to 0"	
				Write LR numbers locations 9FCAH r stands fo and S/N number.	U dash and serial to the EEPROM 9FC0H to espectively. D/N or dash number stands for serial			Enter "modify memory 20000H thru 2000AH to (1st digit of D/N). (2nd digit of D/N). (1st digit of S/N). (2nd digit of S/N). (2nd digit of S/N). (3rd digit of S/N). (3rd digit of S/N). (5th digit of S/N). (6th digit of S/N). (7th digit of S/N)." D/N stands for dash number and S/N for serial number. Enter "END" Switch HBDIS* on UDE (LED shall be illuminated).	
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REV TES	Т	SPECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR NO.	. OPR LIMITS	C TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
LTR NO. 3.1	. OPR LIMITS	cTESTNOTE:E8 bits widIf 16 bitsspecifiedsignificarThus, FEas 34H.Write theaddress ofeach loca(8000H tl)Verify eaWrite theeach EEI(FFH thrulocation i(8000H tl)Verify eaWrite theeach loca(8000H tl)Verify eaWrite theeach loca(8000H tl)Verify eaWrite theserial nunformat, tolocations9FCAH.	EEPROM is only de, instead of 16. of data are , only the least at 8 bits are used. 34H is the same EEPROM (00H thru FFH) to ation in EEPROM hru 9FFFH). ch location. complement of PROM address u 00H) to each n EEPROM hru 9FFFH). ch location. value FFH to ation in EEPROM hru 9FFFH). ch location. value FFH to ation in EEPROM hru 9FFFH). ch location. LRU dash and mbers, in ISO-5 o the EEPROM 9FC0H to	SWITCH POS	C	WORK STEPS Enter "SW IT30P" Enter "run from startup" Program will take several minutes to complete. Front panel LEDs shall flash in a repetitive pattern while the program is executing. When the program is complete all six LEDs shall be illuminated.	N/A N/A
3.2	PASS					Enter "display memory 20010H thru 20011H" Look in the ASCII decode display for the Pass/Fail indicator. Status shall be as indicated. Enter "modify register CACHE to 1"	PASS
			AW/CRITICAL I				
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	NO.

SPECIFICATION					CA			CAGE 58960			REV LTR
			SEE THE TITLE PAGE FOR			ROF	PRIETARY AND DATA RIGHTS NOTA	TION	S.		
REV	EV TEST SPECIFICATIO				N				PROCEDURE	SP	ECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH	POS	С	WORK STEPS	N	IFG LIMITS
	3.3	PASS		Verify that each 8-bit word in locations 9FC0H thru 9FCAH contains the proper dash and serial numbers.					Enter "display memory 9FC0H thru 9FCAH" Look in the ASCII decode display and verify that each 8-bit word contains the proper dash and serial numbers.	PA	SS
									Enter "END"		
									Switch HBDIS* on VDE (LED shall not illuminate).		
									Enter "HW"		
	4			VIDEO R	AM				VIDEO RAM		
				Write the video RAM address (C000H thru C00FH) to each location in video RAM (C000H thru C00FH).				Enter "modify register CACHE to 1"			
			in CC					Enter "modify memory 0C000H thru 0C003H to 0C000H, 0C001H, 0C002H, 0C003H"			
									Enter "modify memory 0C004H thru 0C007H to 0C004H, 0C005H, 0C006H, 0C007H"		
									Enter "modify memory 0C008H thru 0C00BH to 0C008H, 0C009H, 0C00AH, 0C00BH"		
								Enter "modify memory 0C00CH thru 0C00FH to 0C00CH, 0C00DH, 0C00EH, 0C00FH"			
									Enter "display memory 0C000H thru 0C00FH"		
		<u> </u>		<u> </u>					<u> </u>	<u> </u>	
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REV TEST SPECIFICATIO			PECIFICATIO	N	PROCEDURE			SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
REV	TEST NO. 4.1	OPR LIMITS C000H thru C00FH	S	PECIFICATIO TEST Verify tha RAM loca correct da Write the each vide (3FFFH t each loca RAM (C0 C00FH).	N DESCRIPTION at each video ation contains the ata. complement of eo RAM address hru 3FF0H) to ation in video 00H thru	SWITCH POS	C	WORK STEPS WORK STEPS Verify that each video RAM location contains the correct data. Enter "modify memory 0C000H thru 0C003H to 3FFFH, 3FFEH, 3FFDH, 3FFCH" Enter "modify memory 0C004H thru 0C007H to 3FFBH, 3FFAH, 3FF9H, 3FF8H" Enter "modify memory 0C008H thru 0C00BH to 3FF7H, 3FF6H, 3FF5H, 3FF4H"	SPECIFICATION MFG LIMITS C000H thru C00FH
	4.2	3FFFH thru 3FF0H		Verify tha RAM loca correct da	at each video ation contains the ata.	NOTATION		Enter "modify memory 0C00CH thru 0C00FH to 3FF3H, 3FF2H, 3FF1H, 3FF0H" Enter "display memory 0C000H thru 0C00FH" Verify that each video RAM location contains the correct data.	3FFFH thru 3FF0H
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REV	TEST	SPECIFICATIO			N			PROCEDURE	SPECIFICATION		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS		
	5			INPUT DI Perform t accordan Read the memory I	ISCRETES est setup in ce with table 2. following ocations:			INPUT DISCRETES Perform test setup for the transponder interface panel in accordance with table 2. Enter "modify register CACHE to 1" Enter "display memory 0D600H thru 0D605H repetitively" The memory locations shall indicate as follows: (X = don't care)			
	5.1	X355H or XB55H X4AAH or XCAAH 5555H 55XXH 6AXXH XABFH, bit 12 = 0		0D600H 0D601H 0D602H 0D603H 0D604H 0D605H Complem discrete i the UUT. Read the memory I	ent all of the nput stimuli to following ocations:	Mode S Interface <u>Panel</u> Table 3		0D600H 0D601H 0D602H 0D603H 0D603H 0D605H Complement all of the discrete input stimuli to the UUT by performing test setup in accordance with table 3. The memory locations shall indicate as follows: (X = don't care)	X355H or XB55H X4AAH or XCAAH 5555H 55XXH 6AXXH XABFH, bit 12 = 0		
	5.2	X4AAH or XCAAH X355H or XB55H AAAAH AAXXH 95XXH X744H, bit 12 = 1		0D600H 0D601H 0D602H 0D603H 0D604H 0D605H				0D600H 0D601H 0D602H 0D603H 0D604H 0D605H	X4AAH or XCAAH X355H or XB55H AAAAH AAXXH 95XXH X744H, bit 12 = 1		
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LTR NO. OPR LIMITS C TES						PROCEDURE WORK STERS		
	5.3	X5XXH	While (To Tes front pa read m D605H be as i	pressing the Push t button on the anel of the UUT, emory location . The value shall ndicated.	Switch POS		Work sters While pressing the Push To Test button on the front panel of the UUT, enter "display memory 0D605H". The value shall be as indicated.	X5XXH
	6		(X = do <u>OUTPI</u> Perforr accord Write t 0 and 0 Registe	n't care) <u>JT DISCRETES</u> n test setup in ance with table 2. O Output Data Word Configuration er 3 as shown:	1		(X = don't care) <u>OUTPUT DISCRETES</u> Perform test setup in accordance with table 2. Write to Output Data Word 0 and Configuration Register 3 as shown: (Enter "modify memory <address> to <data>")</data></address>	
	6.1	on off off on off on	Addres 0D607 0D400 Verify t followin LEDs: <u>Signal</u> XPDRF XPDRF CNTLF TOPAN BOTAN ALTSIC Verify t signals shall b	s Data H 0012H H 2A00H he state of the og UUT front panel Name PASS PASS FAIL NL IT IT FASS AIL NL IT E Ass Fail NL FASS Fail Sail NL Fail IT Fail Ass Sail NL Fail Fail Fail Ass Sail Ass Sail			AddressData0D607H0012H0D400H2A00HVerify the state of the following UUT front panel LEDs:Signal NameXPDRPASS XPDRFAIL CNTLPNL TOPANT BOTANT ALTSIGVerify the following signals. The indicator lamps on the Mode S interface panel (or the signals themselves) shall be as specified.	on off off on off on
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REV	TEST		S	PECIFICATIO	N		1-	PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS
		<u>Sig/Lamp</u>		Sig/Lamp	<u>)</u>			<u>Sig/Lamp</u>	<u>Sig/Lamp</u>
	6.2	OPN/ON (Green)		ALT FAIL	_ 1			ALT FAIL 1	OPN/ON (Green)
		GND/OFF			_ 2 \\\ 1				GND/OFF
		GND/ON						XPDR FAIL 1	GND/ON
		(Green)							(Green)
				Write to	Output Data Word			Write to Output Data Word	1
Registe				0 and Co Register	as shown:			0 and Configuration Register 3 as shown:	
								(Enter "modify memory <address> to <data>")</data></address>	
Address				Address	Data			Address Data	
0D607H 0D400H				0D607H 0D400H	0001H 1500H			OD607H O001H OD400H 1500H	
	Verify th following LEDs:				e state of the UUT front panel			Verify the state of the following UUT front panel LEDs:	
				Signal Na	ame			<u>Signal Name</u>	
	6.3	off		XPDRPA	SS			XPDRPASS	off
	on CNTLP off TOPAN							on	
			TOPANT	-			TOPANT	off	
		on	BOTANT					BOTANT	on
		off		ALISIG				ALISIG	off
				Verify the	fy the following			Verify the following	
				signals.	The signal sense			signals. The indicator	
					as specified.			interface panel (or the	
								signals themselves) shall	
								be as specified.	
	<u> </u>	1	1	·	AW/CRITICAL N	NOTATION			1
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REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	с	WORK STEPS	MFG LIMITS
	6.4	Sig/Lamp GND/OFF OPN/ON (Red) +5V/ON (Red) OPN/OFF		Signal Na ALT FAIL ALT FAIL XPDR FA	ame . 1 . 2 AIL 1 AIL 2			<u>Signal Name</u> ALT FAIL 1 ALT FAIL 2 XPDR FAIL 1 XPDR FAIL 2	Sig/Lamp GND/OFF OPN/ON (Red) +5V/ON (Red) OPN/OFF
	7			SYNCHR Perform f accordan Stimulate SYNCHR with a 26 sinusoida Stimulate synchro a with a se sinusoid synchron reference Apply this the follow inputs wit side of th connecte SYNC AL SYNC AL SYNC AL SYNC AL SYNC AL SYNC AL SYNC AL	C ALTITUDE test setup in ce with table 2. the UUT C REF H and C REF C signals V ac, 400 Hz al waveform. the UUT altitude inputs cond 400 Hz that is ous with the e signal (above). s signal across ving UUT synchro th the negative e signal d to the Z input: T 1 FINE X-Z T 1 CRSE X-Z T 1 CRSE Y-Z T 2 FINE Y-Z T 2 CRSE X-Z T 2 CRSE Y-Z T 2 CRSE Y-Z	Mode S Interface <u>Panel</u> 26 VAC POL = +		SYNCHRO ALTITUDE Perform test setup in accordance with table 2. On the Mode S interface panel, connect the 26 V ac REF + and - signals to the Sync Alt 1/2 RH and RC signals. Connect the TEST VAC + signal to the FX, FY, CX, and CY signals. Connect the TEST VAC - signal to the FZ and CZ signals. Adjust the amplitude of the TEST VAC signal to 9 V rms.	
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REV	TEST		SPECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	C TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
			1. Adjust 1. Adjust this sig Read the each syn using the procedur 1. Write f data (s table) locatic 2. Wait a 3. Read I 4. Record The data the range Limits.	the amplitude of gnal to 9 V rms. e digital value for chro source, e following e: the appropriate see following to memory on D607H. t least 75 µs. location D606H. d the value. should fall within e shown in the			Read the digital value for each synchro source, using the following procedure: 1. Write the appropriate data (see following table) to memory location D607H. 2. Enter "modify memory 0D607H to <value from<br="">table>" 3. Enter "display memory 0D606H thru 0D606H" 4. Record the value. The data should fall within the range shown in the Limits.</value>	
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS
	7.1	CD5 ± 6FH OH CD5 ± 6FH 2H CD5 ± 6FH 3H CD5 ± 6FH 4H CD5 ± 6FH 6H CD5 ± 6FH 7H		Write To D607H 0100H (uppe (lower 0120H (uppe (lower 0140H (uppe (lower 0160H (uppe (lower 0180H (uppe (lower 01A0H (uppe (lower 01A0H (uppe (lower 01C0H (uppe (lower 01E0H (uppe (lower 01E0H (uppe (lower 01E0H (uppe (lower 01E0H (uppe (lower 01E0H (uppe (lower 01E0H (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower 01E0H) (uppe (lower) 01E0H) (uppe (lower) 01E0H) (uppe (lower) 01E0H) (uppe (lower) 01E0H) (uppe (lower) 01E0H) (uppe (lower) 01E0H) (uppe (lower) 01E0H) (uppe (lower) 01E0H) (uppe (lower) 01E0H) (uppe (lower) 01E0H) (uppe (lower) 01E0H)(lower) 01E0H)(lower) 01E0H)(lower) 01E0H)(Source Being <u>Tested</u> ALT 1 FIN X-Z r 12 bits) r 4 bits) ALT 1 FIN Y-Z r 12 bits) r 4 bits) ALT 1 CRS X-Z r 12 bits) r 4 bits) ALT 1 CRS Y-Z r 12 bits) r 4 bits) ALT 2 FIN X-Z r 12 bits) r 4 bits) ALT 2 FIN Y-Z r 12 bits) r 4 bits) ALT 2 CRS X-Z r 12 bits) r 4 bits) ALT 2 CRS Y-Z r 12 bits) r 4 bits) ALT 2 CRS Y-Z r 12 bits) r 4 bits) the phase 180 on the RO REFERENCE he UUT.	Mode S Interface Panel 26 VAC POL = -		WriteSourceToBeingD607HTested0100HALT 1 FIN X-Z(upper 12 bits)(lower 4 bits)0120HALT 1 FIN Y-Z(upper 12 bits)(lower 4 bits)0140HALT 1 CRS X-Z(upper 12 bits)(lower 4 bits)0160HALT 1 CRS Y-Z(upper 12 bits)(lower 4 bits)0180HALT 2 FIN X-Z(upper 12 bits)(lower 4 bits)01A0HALT 2 FIN Y-Z(upper 12 bits)(lower 4 bits)01C0HALT 2 CRS X-Z(upper 12 bits)(lower 4 bits)01E0HALT 2 CRS Y-Z(upper 12 bits)(lower 4 bits)01E0HALT 2 CRS Y-Z(upper 12 bits)(lower 4 bits)01E0HALT 2 CRS Y-Z(upper 12 bits)(lower 4 bits)Reverse the phase 180degrees on theSYNCHRO REFERENCEinput to the UUT.Read the digital value foreach synchro source,using the followingprocedure:	CD5 ± 6FH 0H CD5 ± 6FH 1H CD5 ± 6FH 2H CD5 ± 6FH 3H CD5 ± 6FH 6H CD5 ± 6FH 6H CD5 ± 6FH 7H
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REV	TEST		SP	ECIFICATIO	N		Noi	PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
REV	TEST NO.	OPR LIMITS 329 ± 6FH 0H 329 ± 6FH 1H 329 ± 6FH 2H 329 ± 6FH 2H 329 ± 6FH 329 ± 6FH 329 ± 6FH 329 ± 6FH	SPP	The data table) f locatio 2. Wait a 3. Read I 4. Record The data the range Limits. Write To D607H 0100H (uppe (lower 0120H (uppe (lower 0140H (uppe (lower 0140H (uppe (lower 0140H (uppe (lower 0140H (uppe (lower 0140H (uppe (lower 0140H (uppe (lower 0140H (uppe (lower 0140H (uppe (lower 0140H (uppe (lower 0140H (uppe (lower 0140H (uppe (lower 0140H	N DESCRIPTION the appropriate see following to memory in D607H. t least 75 µs. ocation D606H. d the value. should fall within e shown in the Source Being <u>Tested</u> ALT 1 FIN X-Z r 12 bits) ALT 1 FIN Y-Z r 12 bits) ALT 1 CRS X-Z r 12 bits) ALT 1 CRS X-Z r 12 bits) ALT 1 CRS Y-Z r 12 bits) ALT 2 FIN X-Z r 12 bits)	SWITCH POS		RETART AND DATA RIGHTS NOTA PROCEDURE WORK STEPS 1. Write the appropriate data (see following table) to memory location D607H. 2. Enter "modify memory 0D607H to <value from="" table="">" 3. Enter "display memory D606H" 4. Record the value. The data should fall within the range shown in the Limits. Write Source To Being D607H D607H Tested 0100H ALT 1 FIN X-Z (upper 12 bits) (lower 4 bits) 0120H ALT 1 FIN Y-Z (upper 12 bits) (lower 4 bits) 0140H ALT 1 CRS X-Z (upper 12 bits) (lower 4 bits) 0160H ALT 1 CRS Y-Z (upper 12 bits) (lower 4 bits) 0160H ALT 1 CRS Y-Z (upper 12 bits) (lower 4 bits) 0160H ALT 1 CRS Y-Z (upper 12 bits) (lower 4 bits) 0180H ALT 2 FIN X-Z (upper 12 bits) (lower 4 bits)</value>	SPECIFICATION MFG LIMITS 329 ± 6FH 0H 329 ± 6FH 1H 329 ± 6FH 1H 329 ± 6FH 329 ± 6FH
		4H 329 ± 6FH 5H 329 ± 6FH 6H 329 ± 6FH 7H		(lower 01A0H (uppe (lower 01C0H (uppe (lower 01E0H (uppe (lower	ALT 2 FIN Y-Z ALT 2 FIN Y-Z r 12 bits) ALT 2 CRS X-Z r 12 bits) ALT 2 CRS Y-Z r 12 bits) ALT 2 CRS Y-Z r 12 bits) ALT 3 bits)			(lower 4 bits) 01A0H ALT 2 FIN Y-Z (upper 12 bits) (lower 4 bits) 01C0H ALT 2 CRS X-Z (upper 12 bits) (lower 4 bits) 01E0H ALT 2 CRS Y-Z (upper 12 bits) (lower 4 bits)	4H 329 ± 6FH 5H 329 ± 6FH 6H 329 ± 6FH 7H
	Н	onevw	el		AW/CRITICAL N				
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REV	TEST		s	PECIFICATIO	N	PROCEDURE			SPECIFICATION		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS		
	8			429 REC <u>(INTERN</u> Internal v 0 thru 7.	EIVER TESTS <u>AL)</u> vrap on receiver			429 RECEIVER TESTS (INTERNAL)			
				Perform t accordan	est setup in ce with table 2.			Perform test setup in accordance with table 2.			
				(Accept A masking)	AH label with no			(Accept AAH label with no masking)			
	o. I IN/A Perform accorda			Perform i accordan	nitial setup in ce with table 6.			Perform initial setup in accordance with table 6.	N/A		
	Modify r <u>Address</u> 0D210F 0D211F				emory: <u>Data</u> AAAAH AAAAH			Enter "modify memory 0D210H thru 0D211H to AAAAH"			
	Read the of memo D20FH.				following block ry: D200H thru			Enter "display memory 0D200H thru 0D20FH"			
	8.2	AAAAH 2AAAH		Even Loc Odd Loca	ations ations			Even Locations Odd Locations	AAAAH 2AAAH		
				(Reject e no label o	ven parity data, qualification)			(Reject even parity data, no label qualification)			
	8.3	N/A		Perform i table 6 ex <u>Address</u> 0D21CH 0D220H 0D22EH	nitial setup per kcept: <u>Data</u> 0249H - 0044H			Perform initial setup per table 6 except: <u>Address Data</u> 0D21CH 0249H 0D220H - 0D22EH 0044H	N/A		
				Modify m <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 5555H 5555H			Enter "modify memory 0D210H thru 0D211H to 5555H"			
				Read the of memor 0D20FH.	following block ry: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"			
-		<u> </u>	<u> </u>		AW/CRITICAL	NOTATION	<u> </u>	<u> </u>			
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REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS	
	8.4	AAAAH 2AAAH (no change from test 8.2)		Even Loc Odd Loca (Reject n labels)	ations ations onmatching			Even Locations Odd Locations (Reject nonmatching labels)	AAAAH 2AAAH (no change from test 8.2)	
	8.5	N/A		Perform i accordan except: <u>Address</u> 0D220H 0D22EH Modify m <u>Address</u> 0D210H 0D211H Read the of memor D20FH.	nitial setup in ce with table 6 <u>Data</u> EF44H emory: <u>Data</u> 5555H 5555H following block y: D200H thru			Perform initial setup in accordance with table 6 except: <u>Address Data</u> 0D220H - 0D22EH EF44H Enter "modify memory 0D210H thru 0D211H to 5555H" Enter "display memory 0D200H thru 0D20FH"	N/A	
	8.6	AAAAH Even Lo 2AAAH Odd Loo (no change from test 8.2) (Reject feature)			ations ations ue to disable			Even Locations Odd Locations (Reject due to disable feature)	AAAAH 2AAAH (no change from test 8.2)	
	8.7	N/A		Perform i accordan except: <u>Address</u> 0D218H 0D21AH 0D220H 0D22EH Modify m <u>Address</u> 0D210H 0D211H Read the of memor 0D20FH.	nitial setup in ce with table 6 Data 0000H 0000H EE44H emory: Data 5555H 5555H following block ry: 0D200H thru			Perform initial setup in accordance with table 6 except: <u>Address</u> <u>Data</u> 0D218H 0000H 0D21AH 0000H 0D220H - 0D22EH EE44H Enter "modify memory 0D210H thru 0D211H to 5555H" Enter "display memory 0D200H thru 0D20FH"	N/A	
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS			
	8.8	AAAAH 2AAAH (no change)		Even Loc Odd Loca (Accept v	ations ations valid data and			Even Locations Odd Locations (Accept valid data and	AAAAH 2AAAH (no change)			
				label)				label)				
	8.9	N/A		Perform i accordan except: <u>Address</u> 0D220H - 0D22EH	nitial setup in ce with table 6 <u>Data</u> - EE44H			Perform initial setup in accordance with table 6 except: <u>Address Data</u> 0D220H - 0D22EH EE44H	N/A			
				Modify m <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 5555H 5555H			Enter "modify memory 0D210H thru 0D211H to 5555H"				
				Read the of memor 3D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"				
	8.10	5555H D555H		Even Loc Odd Loca	ations ations			Even Locations Odd Locations	5555H D555H			
	8.11	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A			
				Modify M Address 0D210H 0D211H Read the	emory: <u>Data</u> 8000H 0000H following block			Enter "modify memory 0D210H to 8000H" Enter "modify memory 0D211H to 0000H" Enter "display memory				
				of memor 0D20FH.	ry: 0D200H thru			0D200H thru 0D20FH"				
	8.12	8000H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	8000H 0000H			
	·		<u> </u>		AW/CRITICAL	OTATION		-				
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REV	TEST		SF	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	8.13	N/A		Perform i accordan Modify Ma <u>Address</u> 0D210H 0D211H Read the of memor 0D20FH.	nitial setup in ce with table 6.1. emory: <u>Data</u> 4000H 0000H following block ry: 0D200H thru			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 4000H" Enter "modify memory 0D211H to 0000H" Enter "display memory 0D200H thru 0D20FH"	N/A	
	8.14	4000H 0000H		Even loca Odd locat	ations tions			Even locations Odd locations	4000H 0000H	
	8.15	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify Mo <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 2000H 0000H			Enter "modify memory 0D210H to 2000H" Enter "modify memory 0D211H to 0000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.16	2000H 0000H		Even loca Odd locat	ations tions			Even locations Odd locations	2000H 0000H	
	8.17	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify Mo	emory: <u>Data</u> 1000H 0000H			Enter "modify memory 0D210H to 1000H" Enter "modify memory 0D211H to 0000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.18	1000H 0000H		Even loca Odd locat	ations tions			Even locations Odd locations	1000H 0000H	
					AW/CRITICAL N	NOTATION				
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REV	TEST		SF	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	8.19	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the of memor 0D20FH.	nitial setup in ice with table 6.1. emory: <u>Data</u> 0800H 0000H following block ry: 0D200H thru			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 0800H" Enter "modify memory 0D211H to 0000H" Enter "display memory 0D200H thru 0D20FH"	N/A	
	8.20	0800H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0800H 0000H	
	8.21	N/A		Perform i accordan	nitial setup in ice with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0400H 0000H			Enter "modify memory 0D210H to 0400H" Enter "modify memory 0D211H to 0000H"		
				of memor 0D20FH.	ry: 0D200H thru			OD200H thru 0D20FH"		
	8.22	0400H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0400H 0000H	
	8.23	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the of memor 0D20FH.	nitial setup in ice with table 6.1. emory: <u>Data</u> 0200H 0000H following block ry: 0D200H thru			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 0200H" Enter "modify memory 0D211H to 0000H" Enter "display memory 0D200H thru 0D20FH"	N/A	
	8.24	0200H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0200H 0000H	
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REV	TEST		SF	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	8.25	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the of memor 0D20FH.	nitial setup in ce with table 6.1. emory: <u>Data</u> 0100H 0000H following block ry: 0D200H thru			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 0100H" Enter "modify memory 0D211H to 0000H" Enter "display memory 0D200H thru 0D20FH"	N/A	
	8.26	0100H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0100H 0000H	
	8.27	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0080H 0000H			Enter "modify memory 0D210H to 0080H" Enter "modify memory 0D211H to 0000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.28	0080H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0080H 0000H	
	8.29	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M Address 0D210H 0D211H	emory: <u>Data</u> 0040H 0000H			Enter "modify memory 0D210H to 0040H" Enter "modify memory 0D211H to 0000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.30	0040H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0040H 0000H	
					AW/CRITICAL					
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REV	TEST		S	PECIFICATIO	N		,	PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS	
	8.31	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the	nitial setup in ce with table 6.1. emory: <u>Data</u> 0020H 0000H following block			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 0020H" Enter "modify memory 0D211H to 0000H" Enter "display memory 0D200H thru 0D20EH"	N/A	
	8.32	0020H 0000H		0D20FH. Even loca Odd loca	ations			Even locations Odd locations	0020H 0000H	
	8.33	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M Address 0D210H 0D211H	emory: <u>Data</u> 0010H 0000H			Enter "modify memory 0D210H to 0010H" Enter "modify memory 0D211H to 0000H"		
				Read the of memor 0D20FH.	following block ry: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.34	0010H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0010H 0000H	
	8.35	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the of memor 0D20FH.	nitial setup in ce with table 6.1. emory: <u>Data</u> 0008H 0000H following block ry: 0D200H thru			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 0008H" Enter "modify memory 0D211H to 0000H" Enter "display memory 0D200H thru 0D20FH"	N/A	
	8.36	0008H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0008H 0000H	
					AW/CRITICAL	NOTATION				
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REV	TEST		S	PECIFICATIO	N		,	PROCEDURE	SPECIFICATION		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS		
	8.37	N/A		Perform i accordan Modify M <u>Address</u> 0D210H	nitial setup in ce with table 6.1. emory: <u>Data</u> 0004H			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 0004H" Enter "modify memory	N/A		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"			
	8.38	0004H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0004H 0000H		
	8.39	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A		
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0002H 0000H			Enter "modify memory 0D210H to 0002H" Enter "modify memory 0D211H to 0000H"			
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"			
	8.40	0002H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0002H 0000H		
	8.41	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A		
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0001H 0000H			Enter "modify memory 0D210H to 0001H" Enter "modify memory 0D211H to 0000H"			
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"			
	8.42	0001H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0001H 0000H		
					AW/CRITICAL	NOTATION	1				
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS	
	8.43	N/A		Perform i accordan Modifv M	nitial setup in ce with table 6.1. emory:			Perform initial setup in accordance with table 6.1. Enter "modify memory	N/A	
				<u>Address</u> 0D210H 0D211H	<u>Data</u> 0000H 8000H			0D210H to 0000H" Enter "modify memory 0D211H to 8000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.44	0000H 8000H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 8000H	
	8.45	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 4000H			Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 4000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.46	0000H 4000H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 4000H	
	8.47	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 2000H			Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 2000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.48	0000H 2000H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 2000H	
		1	<u> </u>		AW/CRITICAL N	ΝΟΤΑΤΙΟΝ	L	1		
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS
	8.49	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the	nitial setup in ce with table 6.1. emory: <u>Data</u> 0000H 1000H			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 1000H"	N/A
	8.50	0000H		of memoi 0D20FH. Even loca	ry: 0D200H thru			0D200H thru 0D20FH" Even locations	0000H
		1000H		Odd loca	tions			Odd locations	1000H
	8.51	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0800H			Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0800H"	
				Read the of memor 0D20FH.	following block ry: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"	
	8.52	0000H 0800H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0800H
	8.53	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0400H			Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0400H"	
				Read the of memore 0D20FH.	following block ry: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"	
	8.54	0000H 0400H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0400H
		<u>. </u>	1		AW/CRITICAL N	OTATION		1	1
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REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS	
	8.55	N/A		Perform i accordan	nitial setup in ice with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Address 0D210H 0D211H	<u>Data</u> 0000H 0200H			0D210H to 0000H" Enter "modify memory 0D211H to 0200H"		
				Read the of memor 0D20FH.	following block ry: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.56	0000H 0200H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0200H	
	8.57	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0100H			Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0100H"		
				Read the of memor 0D20FH.	following block ry: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.58	0000H 0100H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0100H	
	8.59	N/A		Perform i accordan	nitial setup in ice with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0080H			Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0080H"		
				Read the of memor 0D20FH.	following block ry: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.60	0000H 0080H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0080H	
	<u> </u>		<u> </u>	·	AW/CRITICAL N	NOTATION				
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REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	8.61	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H	nitial setup in ce with table 6.1. emory: <u>Data</u> 0000H 0040H			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0040H"	N/A	
	8.62	0000H		of memor 0D20FH. Even loca	ry: 0D200H thru			OD200H thru OD20FH"	0000Н	
		0040H		Odd loca	tions			Odd locations	0040H	
	8.63	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0020H			Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0020H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.64	0000H 0020H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0020H	
	8.65	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0010H			Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0010H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"		
	8.66	0000H 0010H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0010H	
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS		
	8.67	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the of memor 0D20FH.	nitial setup in ice with table 6.1. emory: <u>Data</u> 0000H 0008H following block ry: 0D200H thru			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0008H" Enter "display memory 0D200H thru 0D20FH"	N/A		
	8.68	0000H 0008H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0008H		
	8.69	N/A		Perform i accordan	nitial setup in ice with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A		
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0004H			Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0004H"			
				Read the of memore 0D20FH.	following block ry: 0D200H thru			Enter "display memory 0D200H thru 0D20FH"			
	8.70	0000H 0004H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0004H		
	8.71	N/A		Perform i accordan	nitial setup in ice with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A		
				Address 0D210H 0D211H Read the of memory	emory: <u>Data</u> 0000H 0002H following block ry: 0D200H thru			Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0002H" Enter "display memory 0D200H thru 0D20FH"			
	8.72	0000H 0002H		0D20FH. Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0002H		
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REV	TEST		SI	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	8.73	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the	initial setup in nce with table 6.1. lemory: <u>Data</u> 0000H 0001H e following block			Perform initial setup in accordance with table 6.1. Enter "modify memory 0D210H to 0000H" Enter "modify memory 0D211H to 0001H" Enter "display memory	N/A
	8.74	0000H 0001H		of memore 0D20FH. Even loca	ry: 0D200H thru ations tions			DD200H thru 0D20FH" Even locations Odd locations	0000H 0001H
	9	9 429 REC TRANSI		429 REC TRANSM (EXTERN	EIVER/ IITTER TESTS <u>NAL)</u>			429 RECEIVER/ TRANSMITTER TESTS (EXTERNAL)	
				Perform faccordan and 8.	test setup in ace with tables 2			Perform test setup in accordance with tables 2 and 8.	
1			_		AW/CRITICAL I	NOTATION	1		1
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REV	TEST		SI	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
LTR	<u>NO.</u> 9.1	OPR LIMITS	C	<u>Xmtr 0/Re</u> (MAINT E DATA 1) Perform i accordan	DESCRIPTION <u>cvr 0</u> DATA OUT/AIR nitial setup in ce with table 7.	SWITCH POS	c	work steps <u>Xmtr 0/Rcvr 0</u> (MAINT DATA OUT/AIR DATA 1) Connect Xmtr 0 (Maint Data Out) to Rcvr 0 (429 Air Data 1). Perform initial setup in accordance with table 7.	MFG LIMITS
				Modify re Address 0D220H Modify tra Address 0D210H 0D211H	ceiver label: <u>Data</u> FFAAH ansmitter data: <u>Data</u> AAAAH AAAAH			Enter "modify memory 0D220H to FFAAH" Enter "modify memory 0D210H thru 0D211H to AAAAH"	
				Data sho follows:	uld read as			Enter "display memory 0D200 thru 0D201H" Data should read as follows:	
		<u> </u>	<u> </u>		AW/CRITICAL	NOTATION	<u> </u>	<u> </u>	<u> </u>
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REV TEST SPECIFIC LTR NO. OPR LIMITS C 9.2 AAAAH 0D20 2AAAH 0D20 On M panel conne #1 an ADC	ICATION TEST DESCRIPTION SWITCH POS ress 00H 01H 01H Mode S interface el, remove	PROCEDURE c WORK STEPS Address 0D200H 0D201H 0D201H On Mode S interface	SPECIFICATION MFG LIMITS AAAAH 2AAAH
LTR NO. OPR LIMITS C 9.2 AAAAH 0D20 2AAAH 0D20 On M panel conne #1 an	TEST DESCRIPTION SWITCH POS ress 00H 01H Mode S interface el, remove	C WORK STEPS Address 0D200H 0D201H On Mode S interface	AAAAH 2AAAH
9.2 AAAAH 2AAAH 2AAAH 0D20 0D20 0n M panel conne #1 an ADC	<u>ress</u> 00H 01H Mode S interface el, remove	Address 0D200H 0D201H On Mode S interface	ААААН 2АААН
9.3 5555H D555H 9.3 5555H D555H	nections on 429 ADC and reconnect on 575 C #1. lify receiver label: <u>ress Data</u> 20H FF55H lify transmitter data: <u>ress Data</u> 10H 5555H 11H 5555H a should read as ws: <u>ress</u> 00H 01H	 panel, remove connections on 429 ADC #1 and reconnect on 575 ADC #1. Enter "modify memory 0D220H to FF55H" Enter "modify memory 0D210H thru 0D211H to 5555H" Enter "display memory 0D200H thru 0D201H" Data should read as follows: Address 0D200H 0D200H 0D201H 	5555H D555H

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REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS
	9.4	N/A	c	Xmtr 0/R (MAINT I DATA 2) Perform i accordan Modify re Address	DESCRIPTION <u>cvr 1</u> DATA OUT/AIR initial setup in ice with table 7. eceiver label: Data	SWITCH POS	C	<u>Xmtr 0/Rcvr 1</u> (MAINT DATA OUT/AIR DATA 2) Perform initial setup in accordance with table 7. Enter "modify memory 0D222H to FFAAH"	N/A
				0D222H Modify tra Address 0D210H 0D211H	FFAAH ansmitter data: <u>Data</u> AAAAH AAAAH			Enter "modify memory 0D210H thru 0D211H to AAAAH"	
				Data sho follows:	uld read as			Data should read as follows:	
				Address				Address	
	9.5	ААААН 2АААН		0D202H 0D203H				0D202H 0D203H	ААААН 2АААН
				On Mode panel, re connectio #2 and re ADC #2.	S interface move ons on 429 ADC econnect on 575			On Mode S interface panel, remove connections on 429 ADC #2 and reconnect on 575 ADC #2.	
				Modify re <u>Address</u> 0D222H	ceiver label: <u>Data</u> FF55H			Enter "modify memory 0D222H to FF55H"	
				Modify tra <u>Address</u> 0D210H 0D211H	ansmitter data: <u>Data</u> 5555H 5555H			Enter "modify memory 0D210H thru 0D211H to 5555H"	
								Enter "display memory 0D202H thru 0D203H"	
				Data sho follows:	uld read as			Data should read as follows:	
╞	<u>I</u>	<u> </u>		<u>I</u>				<u> </u>	<u> </u>
			~			NUTATION	1		[
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SECURITY NOTATION	SPEC NO.	IT4061400-907	SEE PAGE INDEX FOR THIS SHEET REV LETTER

REVTESTSPECIFICATIONPROCEDURELTRNO.OPR LIMITSCTEST DESCRIPTIONSWITCH POSCWORK STEPS9.65555HOD202HOD203HOD203HOD203HOD203HXmtr 0/Rcvr 29.7N/A(MAINT DATA OUT/CNTL DATA B)(MAINT DATA OUT/CNTL DATA B)(MAINT DATA OUT/CNTL DATA B)Perform initial setup in accordance with table 7.Perform initial setup in accordance with table 7.Perform initial setup in accordance with table 7.Modify receiver label: AddressModify transmitter data: AddressData OD210HEnter "modify memory OD210HOD210H AAAAH	SPECIFICATION
LTR NO. OPR LIMITS C TEST DESCRIPTION SWITCH POS C WORK STEPS 9.6 5555H 0D202H 0D203H Address 0D203H 0D203H 9.7 N/A (MAINT DATA OUT/CNTL DATA OUT/CNTL DATA B) Xmtr 0/Rcvr 2 (MAINT DATA OUT/CNTL DATA B) Perform initial setup in accordance with table 7. Perform initial setup in accordance with table 7. Perform initial setup in accordance with table 7. Modify receiver label: Address Data Data D224H Enter "modify memory 0D224H to FFAAH" Modify transmitter data: Address Data 0D210H AAAAH AAAAH	STEERINGATION
9.65555H D555HAddress OD202H OD203HAddress OD203H9.7N/A(MAINT DATA OUT/CNTL DATA B)(MAINT DATA OUT/CNTL DATA B)(MAINT DATA OUT/C DATA B)9.7N/A(MAINT DATA OUT/CNTL DATA B)(MAINT DATA OUT/CNTL DATA B)Perform initial setup in accordance with table 7. Modify receiver label: Address DD224HPerform initial setup in accordance with table 7.Modify receiver label: Address OD224HModify transmitter data: Address Data OD210HEnter "modify memory OD210H thru 0D211H AAAAH	MFG LIMITS
9.7 N/A Xmtr 0/Rcvr 2 (MAINT DATA OUT/CNTL DATA B) Xmtr 0/Rcvr 2 (MAINT DATA OUT/C DATA B) Perform initial setup in accordance with table 7. Perform initial setup in accordance with table 7. Perform initial setup in accordance with table 7. Modify receiver label: Address Data 0D224H Data FFAAH DD224H to FFAAH" Modify transmitter data: Address Data 0D210H Data AAAAH Enter "modify memory 0D210H thru 0D211H	5555H D555H
9.7 N/A (MAINT DATA OUT/CNTL DATA B) (MAINT DATA OUT/C DATA B) Perform initial setup in accordance with table 7. Perform initial setup in accordance with table 7. Perform initial setup in accordance with table 7. Modify receiver label: Address Data 0D224H Data FFAAH Enter "modify memory 0D224H to FFAAH" Modify transmitter data: Address Data 0D210H OD210H Enter "modify memory 0D210H	200011
Perform initial setup in accordance with table 7. Perform initial setup i accordance with table 7. Modify receiver label: Modify receiver label: Address Data 0D224H FFAAH Modify transmitter data: Enter "modify memory Address Data 0D210H AAAAH	NTL N/A
Modify receiver label: Enter "modify memory Address Data 0D224H FFAAH Modify transmitter data: Enter "modify memory Address Data 0D210H AAAAH	¹ 7.
Modify transmitter data: Enter "modify memory Address Data 0D210H AAAAH	,
0D211H AAAAH	to
Enter "display memor 0D204H thru 0D205H	/
Data should read as follows: Data should read as	
Address Address	
9.8AAAAH 2AAAH0D204H 0D205H0D204H 0D205H	ААААН 2АААН
Modify receiver label: Address 0D224HEnter "modify memory 0D224H to FF55HData 0D224H0D224H to FF55H	,
Modify transmitter data: AddressEnter "modify memory 0D210H thru 0D211H 5555H0D210H5555H0D211H5555H	to
Enter "display memor 0D204H thru 0D205H	/
Data should read as follows: Data should read as follows:	
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS		
				Address				Address			
	9.9	5555H D555H		0D204H 0D205H				0D204H 0D205H	5555H D555H		
				Xmtr 0/R	<u>cvr 3</u>			Xmtr 0/Rcvr 3			
	9.10	N/A		(MAINT E OUT/MA	DATA INT DATA IN)			(MAINT DATA OUT/MAINT DATA IN)	N/A		
				Perform i accordan	nitial setup in ce with table 7.			Perform initial setup in accordance with table 7.			
Modify r <u>Address</u> 0D226H				Modify re <u>Address</u> 0D226H	ceiver label: <u>Data</u> FFAAH			Enter "modify memory 0D226H to FFAAH"			
Modify t <u>Address</u> 0D210H 0D211H				Modify tra <u>Address</u> 0D210H 0D211H	ansmitter data: <u>Data</u> AAAAH AAAAH			Enter "modify memory 0D210H thru 0D211H to AAAAH"			
								Enter "display memory 0D206H thru 0D207H"			
				Data sho follows:	uld read as			Data should read as follows:			
				<u>Address</u>				Address			
	9.11	AAAAH 2AAAH		0D206H 0D207H				0D206H 0D207H	AAAAH 2AAAH		
Modify ru <u>Address</u> 0D226H Modify tr <u>Address</u> 0D210H 0D211H			Modify re <u>Address</u> 0D226H	ceiver label: <u>Data</u> FF55H			Enter "modify memory 0D226H to FF55H"				
			ansmitter data: <u>Data</u> 5555H 5555H			Enter "modify memory 0D210H thru 0D211H to 5555H"					
								Enter "display memory 0D206H thru 0D207H"			
				Data sho follows:	uld read as			Data should read as follows:			
					AW/CRITICAL N						
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	9.12	5555H		<u>Address</u> 0D206H				<u>Address</u> 0D206H	5555H	
		D555H		0D207H Xmtr 0/R	cvr 6			0D207H Xmtr 0/Rcvr 6	D555H	
	9.13	N/A		(MAINT E OUT/FLIC	DATA GHT ID)			(MAINT DATA OUT/FLIGHT ID)	N/A	
				Perform s accordan	setup in ce with table 7.			Perform setup in accordance with table 7.		
				Modify re <u>Address</u> 0D22CH	ceiver label: <u>Data</u> FFAAH			Enter "modify memory 0D22CH to FFAAH"		
	Modify t <u>Address</u> 0D210H 0D211H				ansmitter data: <u>Data</u> AAAAH AAAAH			Enter "modify memory 0D210H thru 0D211H to AAAAH"		
								Enter "display memory 0D20CH thru 0D20DH"		
				Data sho follows:	uld read as			Data should read as follows:		
				<u>Address</u>				<u>Address</u>		
	9.14	AAAAH 2AAAH		0D20CH 0D20DH				0D20CH 0D20DH	AAAAH 2AAAH	
				Modify re <u>Address</u> 0D22CH	ceiver label: <u>Data</u> FF55H			Enter "modify memory 0D22CH to FF55H"		
				Modify tra <u>Address</u> 0D210H 0D211H	ansmitter data: <u>Data</u> 555H 5555H			Enter "modify memory 0D210H thru 0D211H to 5555H"		
								Enter "display memory 0D20CH thru 0D20DH"		
				Data sho follows:	uld read as			Data should read as follows:		
	1	<u>I</u>	1	L	AW/CRITICAL N	NOTATION	1	1	<u>I</u>	
	₽₽	onevwa	ב							
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REV	TEST		s	PECIFICATION	N		_	PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	9.15	5555H D555H		<u>Address</u> 0D20CH 0D20DH				<u>Address</u> 0D20CH 0D20DH	5555H D555H	
	9.16	N/A		<u>Xmtr 0/Rc</u> (MAINT D DATA A) Perform ir	ovr 7 OATA OUT/CNTL			<u>Xmtr 0/Rcvr 7</u> (MAINT DATA OUT/CNTL DATA A) Perform initial setup in	N/A	
	Address 0D22EH Modify tr Address 0D210H 0D211H				ce with table 7. ceiver label: <u>Data</u> FFAAH			accordance with table 7. Enter "modify memory 0D22EH to FFAAH"		
					<u>Data</u> AAAAH AAAAH AAAAH			Enter "display memory		
				Data shou follows:	uld read as			0D20EH thru 0D20FH" Data should read as follows:		
	9.17	AAAAH 2AAAH		0D20EH 0D20FH				0D20EH 0D20FH	ААААН 2АААН	
				Modify red <u>Address</u> 0D22EH	ceiver label: <u>Data</u> FF55H			Enter "modify memory 0D22EH to FF55H"		
				Modify tra <u>Address</u> 0D210H 0D211H	insmitter data: <u>Data</u> 5555H 5555H			Enter "modify memory 0D210H thru 0D211H to 5555H"		
							Enter "display memory 0D20EH thru 0D20FH"			
				Data shou follows:	uld read as			Data should read as follows:		
	1		<u> </u>	·	AW/CRITICAL N					
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REV	TEST		SPEC	IFICATION	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	9.18	5555H D555H	<u>Ad</u> 0D 0D	ddress D20EH D20FH				<u>Address</u> 0D20EH 0D20FH	5555H D555H	
			<u>Xn</u>	mtr 1/Ro	<u>evr 4</u>			<u>Xmtr 1/Rcvr 4</u>		
	9.19	N/A	(X		RD/TX COORD)			(XT COORD/TX COORD)	N/A	
			Pe ac	erform in cordan	nitial setup in ce with table 7.			Perform initial setup in accordance with table 7.		
			Mo <u>Ad</u> 0D	odify ree <u>ddress</u> D228H	ceiver label: <u>Data</u> FFAAH			Enter "modify memory 0D228H to FFAAH"		
			Mc <u>Ad</u> 0D 0D	odify tra <u>ddress</u> D212H D213H	nsmitter data: <u>Data</u> AAAAH AAAAH			Enter "modify memory 0D212H thru 0D213H to AAAAH"		
								Enter "display memory 0D208H thru 0D209H"		
			Da fol	ata shou Ilows:	uld read as			Data should read as follows:		
			Ad	<u>ddress</u>				<u>Address</u>		
	9.20	AAAAH 2AAAH	0D 0D	D208H D209H				0D208H 0D209H	AAAAH 2AAAH	
			Mo <u>Ad</u> 0D	odify red <u>ddress</u> D228H	ceiver label: <u>Data</u> FF55H			Enter "modify memory 0D228H to FF55H"		
	Modify t <u>Address</u> 0D212H 0D213H			odify tra <u>ddress</u> D212H D213H	nsmitter data: <u>Data</u> 5555H 5555H			Enter "modify memory 0D212H thru 0D213H to 5555H"		
								Enter "display memory 0D208H thru 0D209H"		
			Da fol	ata shou Ilows:	uld read as			Data should read as follows:		
					AW/CRITICAL N	NOTATION				
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REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS
	9.21	5555H D555H		<u>Address</u> 0D208H 0D209H				<u>Address</u> 0D208H 0D209H	5555H D555H
				Xmtr 2/R	<u>cvr 5</u>			Xmtr 2/Rcvr 5	
	9.22	N/A		(DATA LI LINK IN)	INK OUT/DATA			(DATA LINK OUT/DATA LINK IN)	N/A
				Perform i accordan	initial setup in ice with table 7.			Perform initial setup in accordance with table 7.	
				Modify re <u>Address</u> 0D22AH	ceiver label: <u>Data</u> FFAAH			Enter "modify memory 0D22AH to FFAAH"	
	Modify t <u>Address</u> 0D214H 0D215H				ansmitter data: <u>Data</u> AAAAH AAAAH			Enter "modify memory 0D214H thru 0D215H to AAAAH"	
	Data she follows:							Enter "display memory 0D20AH thru 0D20BH"	
				Data sho follows:	uld read as			Data should read as follows:	
				Address				<u>Address</u>	
	9.23	AAAAH 2AAAH		0D20AH 0D20BH				0D20AH 0D20BH	AAAAH 2AAAH
				Modify re <u>Address</u> 0D22AH	ceiver label: <u>Data</u> FF55H			Enter "modify memory 0D22AH to FF55H"	
	Modify tr <u>Address</u> 0D214H 0D215H			Modify tra <u>Address</u> 0D214H 0D215H	ansmitter data: <u>Data</u> 5555H 5555H			Enter "modify memory 0D214H thru 0D215H to 5555H"	
								Enter "display memory 0D20AH thru 0D20BH"	
				Data sho follows:	uld read as			Data should read as follows:	
-									
	₽₽	OHOVA			AW/GRITIGAL NOTATION				
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
	NO.	OPR LIMITS	C	IESI	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFGLIMITS
	9.24	5555H D555H		Address 0D20AH 0D20BH				Address 0D20AH 0D20BH	5555H D555H
	10			INTERNA <u>SELF-TE</u>	NL RF <u>ST</u>	Mada O		INTERNAL RF <u>SELF-TEST</u>	
				Perform t accordan	est setup in ce with table 4.	Mode S Interface		Perform test setup in accordance with table 4.	
				Set up vie according	deo ASIC g to table 4.	<u>Panel</u> STBY/ON: ON		Enter "END"	
								Disable heartbeat by switching HBDIS* switch on the UDE (LED should be illuminated).	
								Enter "SWIT100P"	
								Enter "trace about address FINISHED break_on measurement_complete"	
								Enter "run from STARTUP". Program takes only a fraction of a second to run. When all six front panel LEDs are illuminated, program is complete.	
								Enter "modify register CACHE to 1"	
				Short Mo <u>Bottom A</u>	de S <u>ntenna</u>			Short Mode S <u>Bottom Antenna</u>	
	10.1	N/A		Initiate th writing to	e self-test by memory as			Enter "modify memory 0C02EH to 0031H"	N/A
	Address 0C02EH		<u>Data</u> 0031H			Enter "display memory 0C000H thru 0C003H"			
				The receindata shal	ved interrogation I be as shown.			Data shall indicate as follows:	
	<u> </u>	1	<u> </u>	<u> </u>	AW/CRITICAL	NOTATION	<u>I</u>	1	1
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REV	TEST		SF	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
				<u>Address</u>				Address	
	10.2	5555H		0C000H	thru 0C003H			0C000H thru 0C003H	5555H
				Long Moo Top Ante	de S <u>nna</u>			Long Mode S <u>Top Antenna</u>	
	10.3	N/A		Initiate th writing to shown. <u>Address</u> 0C007H	e self-test by memory as <u>Data</u> thru			Enter "modify memory 0C007H thru 0C00DH to AAAAH"	N/A
				0C00DH 0C02EH	AAAAH 0037H			0C02EH to 0037H"	
						н		Enter "display memory 0C000H thru 0C007H"	
				The rece data shal	ived interrogation I be as shown.	n		Data shall indicate as follows:	
				<u>Address</u>				<u>Address</u>	
	10.4	ААААН		0C000H	thru 0C006H			0C000H thru 0C006H	ААААН
				Mode S L <u>Top Ante</u>	₋ong Squitter <u>nna Pwr Vld</u>			Mode S Long Squitter <u>Top Antenna Pwr Vld</u>	
	10.5	N/A	Set up re writing to		ply data by memory as	Mode S Interface		Enter "modify memory 0C007H to F000H"	N/A
	shown. pulses o <u>Address</u> 0C007H 0C008H 0C00DH		shown. (pulses or <u>Address</u> 0C007H 0C008H 0C00DH	Send preamble hly.) <u>Data</u> F000H thru 0000H	<u>Panel</u> STBY/ON: ON		Enter "modify memory 0C008H thru 0C00DH to 0000H"		
			Initiate the writing to shown. <u>Address</u> 0C02EH		e self-test by memory as <u>Data</u> 003FH			Enter "modify memory 0C02EH to 003FH"	
	<u>. </u>	1	<u>1 </u>		AW/CRITICAL	NOTATION	1		1
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REV LTR	TEST NO.		S	PECIFICATIO	N	1		PROCEDURE	0050151015101
LTR	NO.						1		SPECIFICATION
1		OPRLIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	10.6	XXX8H bits 4 and 5 = 1		Observe power val reading th word, add This word specified.	the top antenna lid signal by ne self-test status Iress 0C011H. I shall be as			Enter "display memory 0C011H thru 0C011H" Location shall be as specified.	XXX8H bits 4 and 5 = 1
				(X = don'	t care)			(X = don't care)	
				Mode S L Bot Anter	ong Squitter Ina Pwr Vld			Mode S Long Squitter <u>Bot Antenna Pwr Vld</u>	
	10.7	N/A		Initiate th writing to shown. <u>Address</u> 0C02EH	e self-test by memory as <u>Data</u> 003DH	Mode S Interface <u>Panel</u> STBY/ON: ON		Enter "modify memory 0C02EH to 003DH"	N/A
	10.8	XXXX4H bits 4 and 5 = 1		Observe antenna p signal by test statu 0C011H. be as spe	the bottom bower valid reading the self- s word, address This word shall ecified.			Enter "display memory 0C011H thru 0C011H" This location shall be as specified.	XXXX4H bits 4 and 5 = 1
				(X = don'	t care)			(X = don't care)	
				ATCRBS Top Ante	Mode A <u>nna</u>			ATCRBS Mode A <u>Top Antenna</u>	
	10.9	N/A		Set up interrogation data for Mode A.AddressData0C007HE000H0C008H0000H0C009HE000H0C00AH0000H0C00CH0000H0C00DH0000H0C00DH0000H0C00DH0000H0C00DH0000H0C00DH0000H0C00DH0000H0C00DH0000H0C00DH0000H0C00DH0000H0C00DH0000H0C02EH0036H				Modify memory according to the following table: (Enter "modify memory <address> to <data>") <u>Address</u> <u>Data</u> 0C007H E000H 0C008H 0000H 0C009H E000H 0C000H 0000H 0C00CH 0000H 0C00CH 0000H 0C00DH 0000H Enter "modify memory 0C02EH to 0036H"</data></address>	N/A
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REV	TEST		SF	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	c	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	10.10	ХХХ2Н		Observe signal by test statu 0C011H. as specif	the Mode A reading the self- is word, address Word shall be ied.			Enter "display memory 0C011H thru 0C011H" This location shall be as specified.	ХХХ2Н	
				(X = don'	t care)			(X = don't care)		
				ATCRBS Bot Anter	Mode C nna			ATCRBS Mode C Bot Antenna		
	10.11	N/A		Set up in for Mode Address 0C007H 0C008H 0C009H 0C00BH 0C00CH 0C00CH	terrogation data C. <u>Data</u> E000H 0000H 0000H 0000H 0000H 0E00H 0000H			Modify memory according to the following table: (Enter "modify memory <address> to <data>") <u>Address</u> <u>Data</u> 0C007H E000H 0C008H 0000H 0C009H 0000H 0C00BH 0000H 0C00BH 0000H 0C00CH 0E00H 0C00CH 0E00H</data></address>	N/A	
				Initiate th writing to shown. <u>Address</u> 0C02EH	e self-test by memory as <u>Data</u> 0030H			Enter "modify memory 0C02EH to 0030H"		
	10.12	XXX1H		Observe the Mode A signal by reading the self-test status word, address 0C011H. Word shall be as specified. (X = don't care)				Enter "display memory 0C011H thru 0C011H" This location shall be as specified. (X = don't care)	XXX1H	
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	10.13	N/A	Perform t accordan STBY/ON discrete (hardwar Interroga an ATCR interroga standard		test setup in ice with table 1. N e path) te the UUT with BS Mode A tion at the rate and power.	S-1403 FUNC 1 ATC-1400A XPDR MODE A PRF: 0450 RF LvI: -50 CW/NORM/ OFF: NORM Mode S Interface Panel STBY/ON: ON		Perform test setup in accordance with table 1. Turn XPDR MODE control knob to A position. Turn PRF thumbwheels to 450 Hz.	N/A
	10.14	>90%		Observe reply. Tr as specif Switch th STANDB	the ATCRBS % ne value shall be ied. e UUT to Y mode.	Mode S Inteface <u>Panel</u> STBY/ON: STBY		Read the % reply display on the ATC-1400A. The display shall indicate as specified. On the Mode S interface panel, toggle the STBY/ON switch to the STBY position.	>98%
	10.15	<10%		Observe the ATCRBS % reply. The value shall be as specified.				Read the % reply display on the ATC-1400A. The display shall indicate as specified.	<2%
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REV	TEST		s	PECIFICATION	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	11	11 ATCRBS FREQUE AND POV		ATCRBS FREQUEI <u>AND POV</u>	MTL, REPLY NCY, DELAY, <u>VER</u>			ATCRBS MTL, REPLY FREQUENCY, DELAY, <u>AND POWER</u>	
			Perform f accordan and 5, ex DATA B s		est setup in ce with tables 1 cept set CNTL source as shown.	CNTL DATA E 429 source settings SPD: low PAR: odd RATE: 168 ms LBL: 016 DATA: 007E38	3	Perform test setup in accordance with tables 1 and 5, except set CNTL DATA B source as shown.	
						<u>S-1403</u> FUNC 1			
				Remove t system fro Turn on th	he emulation om the UUT. ne UUT.			Remove the HP 64000 emulation system by performing the following steps:	
								 Type "END" on the HP 64000 keyboard. Turn off UUT. Remove UDE connector from the UUT. Turn on UUT. 	
				Interrogat an ATCRI interrogat standard	te the UUT with BS Mode A ion at the rate.	<u>ATC-1400A</u> XPDR MDE A PRF: 0450		Turn XPDR MODE control to MODE A position. Turn PRF thumbwheels to 450 Hz.	
				ATCRBS <u>1029.8 M</u> I	MTL at <u>Hz</u>			ATCRBS MTL at <u>1029.8 MHz</u>	
	11.1	N/A Set the i frequence		Set the in frequency	terrogation v to 1029.8 MHz.	ATC-1400A DSPLY SEL: FREQ FREQ and DELTA F: as required		Turn thumbwheels on FREQ SELECT and DELTA F until FREQ DISPLAY indicates 1029.80 MHz.	N/A
	Set the int to -70 dBr			terrogation level m.	<u>ATC-1400A</u> RF Lvl: -70		Set ATC-1400A RF level to -70 dBm.		
	<u>. </u>			·	AW/CRITICAL	NOTATION			
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REV	TEST	TEST SPECIFICATIO			N			PROCEDURE	SPECIFICATION			
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS			
	11.2	ID 0077		Observe tl returned b Code shal specified.	ne 4096 code y the UUT. I be as	ATC-1400A CW/NORM/ OFF: NORM DPLY SEL: XPDR CODE		Observe the XPDR CODE display. Display shall indicate as specified.	ID 0077			
	11.3	250 TO 630 Watts		Measure a bottom an power. Th be within t specified.	and record the tenna F1 pulse ne power shall he limits	<u>ATC-1400A</u> F2/P2 F1/P1: F1		On the ATC-1400A, observe and record the XMTR PWR display. This measurement is the bottom antenna F1 power, and shall be within the limits specified.	280 to 550 Watts			
	11.4	-76 ± 4 dBm	4 dBm Slowly d interroga steps un less than incremen interroga as speci value.		crement the on level in 1-dB the % reply is 90%, then by 1 dB. The on level shall be ed. Record the			Slowly decrement the RF LEVEL knob on the ATC-1400A in 1-dB steps until the % reply indicates less than 90%, then increment the RF level by 1 dB. The RF level shall indicate as specified. Record the value.	-76 ± 4 dBm			
			Interroga an ATCF interroga standaro		e the UUT with 3S Mode A on at the ate.	<u>ATC-1400A</u> XPDR MDE A PRF: 0450		Turn XPDR MODE control to MODE A position. Turn PRF thumbwheels to 450 Hz.				
				ATCRBS MTL at <u>1030.0 MHz</u>				ATCRBS MTL at <u>1030.0 MHz</u>				
	11.4.1	.4.1 N/A Set the i frequence		Set the int frequency	errogation to 1030.0 MHz.	ATC-1400A DSPLY SEL: FREQ FREQ and DELTA F: as required		Turn thumbwheels on FREQ SELECT and DELTA F until FREQ DISPLAY indicates 1030.00 MHz.	N/A			
		Set the to -70 di		Set the int to -70 dBn	errogation level n.	<u>ATC-1400A</u> RF Lvl: -70		Set ATC-1400A RF level to -70 dBm.				
	11.4.2	ID 0077		Observe the 4096 code returned by the UUT. Code shall be as specified.		ATC-1400A CW/NORM/ OFF: NORM DPLY SEL: XPDR CODE		Observe the XPDR CODE display. Display shall indicate as specified.	ID 0077			
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REV	/ TEST SPECIFICATIO				N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	11.4.3	-76 ± 4 dBm		Slowly de interrogat steps unt less than incremen interrogat as specifi value.	ecrement the tion level in 1-dB il the % reply is 90%, then t by 1 dB. The tion level shall be ied. Record the			Slowly decrement the RF LEVEL knob on the ATC-1400A in 1-dB steps until the % reply indicates less than 90%, then increment the RF level by 1 dB. The RF level shall indicate as specified. Record the value.	-76 ± 4 dBm
	11.4.4	N/A		Interrogat an ATCR interrogat standard Set the in	te the UUT with BS Mode C tion at the rate. Iterrogation level	ATC-1400A XPDR MDE C CW/NORM: NORM ATC-1400A		Turn XPDR MODE control to MODE C position, CW/NORM/OFF to NORM. Set ATC-1400A RF level	N/A
			to -70 dB		m.	RF LvI: -70		to -70 dBm.	
	11.4.5	-76 ± 4 dBm		Slowly de interrogat steps unt less than incremen interrogat as specifi value.	ecrement the tion level in 1-dB il the % reply is 90%, then t by 1 dB. The tion level shall be ied. Record the			Slowly decrement the RF LEVEL knob on the ATC-1400A in 1-dB steps until the % reply indicates less than 90%, then increment the RF level by 1 dB. The RF level shall indicate as specified. Record the value.	-76 ± 4 dBm
	11.4.6	<1.0 dB		Compare values obtained in tests 11.4.3 and 11.4.5 Value obtained in 11.4.3 shall be equal to that obtained in 11.4.5 ±1.0 dB. ATCRBS MTL at 1030.2 MHZ, Top and Bottom <u>Antennas Switched</u>				Compare values obtained in tests 11.4.3 and 11.4.5. Value obtained in 11.4.3 shall be equal to that obtained in 11.4.5 ±1.0 dB. ATCRBS MTL at 1030.2 MHZ, Top and Bottom Antennas Switched	<1.0 dB
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REV	TEST		S	PECIFICATIO	N		1	PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	C	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS	
	11.5	N/A		Disable in turn off th	nterrogations and ne UUT.	ATC-1400A CW/NORM: OFF		Turn CW/NORM/OFF switch to OFF position on the ATC-1400A and turn off the UUT.	N/A	
				Reverse cables co Mode S t	the antenna onnected to the est equipment.			Reverse the antenna cables connected to the IFR ANT A and ANT B ports.		
				Turn on t	he UUT.			Turn on UUT.		
				Interroga an ATCR interroga standard	te the UUT with BS Mode C tion at the rate.	ATC-1400A XPDR MDE (CW/NORM: NORM	C	Turn XPDR MODE control to MODE C position, CW/NORM/OFF to NORM.		
				Set the ir frequenc	nterrogation y to 1030.2 MHz.	ATC-1400A DSPLY SEL: FREQ FREQ and DELTA F: as required	5	Turn thumbwheels on FREQ SELECT and DELTA F until FREQ DISPLAY indicates 1030.20 MHz.		
				Set the ir to -70 dB	nterrogation level m.	<u>ATC-1400A</u> RF Lvl: -70		Set ATC-1400A RF level to -70 dBm.		
	11.6 7710 Observe returned Code sha specified		returned by the UUT. Code shall be as specified.		Ξ	Observe the XPDR CODE display. Display shall indicate as specified.	7710			
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REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS
	11.7	-76 ± 4 dBm		Slowly de interroga steps unt less than incremen interroga as specifi value.	ecrement the tion level in 1-dB il the % reply is 90%, then t by 1 dB. The tion level shall be ied. Record the			Slowly decrement the RF LEVEL knob on the ATC-1400A in 1-dB steps until the % reply indicates less than 90%, then increment the RF level by 1 dB. The RF level shall indicate as specified. Record the value.	-76 ± 4 dBm
				Return th level to -	e interrogation 50 dBm.	ATC-1400A RF Lvl: -50		Set ATC-1400A RF level to -50 dBm.	
				Return th frequency	e interrogation y to 1030.0 MHz.	DSPLY SEL: FREQ		Turn thumbwheels on FREQ SELECT and DELTA F until FREQ DISPLAY indicates 1030.00 MHz.	
				ATCRBS Frequenc	Reply ≳y			ATCRBS Reply <u>Frequency</u>	
	11.8 N/A Change to B source 1s (7777 with SPI				he 429 Cntl Data to provide an all) Mode A reply (ident).	Cntl Data B 429 source <u>settings</u> SPD: low PAR: odd RATE: 168 ms LBL: 016 DATA: 1FFE48		Change the 429 Cntl Data B source to provide an all 1s (7777) Mode A reply with SPI (ident).	N/A
	11.9	1090 ± 1 MHz		Measure reply freq Frequenc specified	and record the juency. cy shall be as	ATC-1400A XPDR MDE A	A	Observe the XMTR FREQ display on the ATC-1400A. Display shall indicate as specified. Record the value.	1090 ± 1 MHz
				ATCRBS	Reply Delay			ATCRBS Reply Delay	
	11.10	N/A		This test reply dela	measures the ay.				N/A
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS	
	11.11	2.5 to 3.5 µs		Measure reply dela be as spe	and record the ay. Delay shall ecified.			Observe the reply delay field on the S-1403. Field shall indicate as specified. Record the value.	2.5 to 3.5 µs	
				ATCRBS	Reply Power			ATCRBS Reply Power		
	11.12	N/A		This test reply pow	measures the /er.				N/A	
	11.13	250 to 630 Watts		Measure peak repl F1 pulse.	and record the y power of the	ATC-1400A F2/P2 F1/P1: F1		On the ATC-1400A, select the F1 pulse for power detection. Observe and record the XMTR PWR display. Display shall indicate as shown.	280 to 550 Watts	
				Select the pulse for	e ATCRBS F2 power detection.	<u>S-1403</u> CONTROL MENU 2, Pulse Power Gate: F2		On the S-1403, change the pulse power gate (PPG) field to F2.		
	11.14	250 to 630 Watts		Measure top anten power.	and record the na F2 pulse	ATC-1400A F2/P2 F1/P1: F2		On the ATC-1400A, select the F2 pulse for power detection. Observe and record the XMTR PWR display. This measurement is the top antenna F2 pulse power.	280 to 550 Watts	
	11.15	<1 dB		Calculate absolute transmitte using the formula: Droop = 7 P1 - Log1 and P2 a measuren tests 11.7 respectiv shall not specified	e and record the value of the er droop (in dB), following ABS [10(Log ₁₀ ₀ P2)], where P1 re the power ments taken in 13 and 11.14, ely. The droop exceed the limits.			Calculate and record the absolute value of the transmitter droop (in dB), using the following formula: Droop = ABS [10(Log ₁₀ P1 - Log ₁₀ P2)], where P1 and P2 are the power measurements taken in tests 11.13 and 11.14, respectively. The droop shall not exceed the specified limits.	<1 dB	
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REV	TEST		S	PECIFICATIO	Ν			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS
	12			ATCRBS INTERRO <u>WIDTH T</u>	DGATION PULSE OLERANCE			ATCRBS INTERROGATION PULSE <u>WIDTH TOLERANCE</u>	
				Perform t accordan and 5.	est setup in ce with tables 1			Perform test setup in accordance with tables 1 and 5.	
				Interroga an ATCR interroga P1, P3 pu Interroga standard	te the UUT with BS Mode A tion with variable ulse width. te at the rate.	ATC-1400A XPDR MDE A PRF: 0450 XPDR PLSE WDTH: VAR	A	Turn DISPLAY SELECT control to MODE A position. Turn PRF thumbwheels to 450 Hz. Set the XPDR PULSE WIDTH switch to VAR.	
	12.1 >90% Use the set the F widths. reply shate >90% 0.70 µs <10%			Use the f set the P widths. T reply sha	ollowing table to 1, P3 pulse The % ATCRBS Il be as specified.	ATC-1400A CW/NORM/ OFF: NORM		Use the following table to set the P1, P3 pulse widths (on ATC-1400A, adjust the XPDR PULSE WIDTH thumbwheels). The % ATCRBS reply shall indicate as specified.	
				<u>P1, P3 P</u> 0.70 μs 0.90 μs 0.30 μs	<u>ulse Width</u>			<u>P1, P3 Pulse Width</u> 0.70 μs 0.90 μs 0.30 μs	>98% >98% <2%
				Return th width to t setting.	e P1, P3 pulse he nominal	ATC-1400A XPDR PLSE WDTH: CAL		On the ATC-1400A, return the XPDR PULSE WIDTH switch to the CAL position.	
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LTR	NO.	OPR LIMITS	С	TEST	DESC	RIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS		
	13	ATCRBS INTERRO POSITIO (MODE A			DGAT N TC <u>N</u>	FION PULSE DLERANCE			ATCRBS INTERROGATION PULS POSITION TOLERANCE (MODE A) Perform test setup in	E		
				accordan and 5.	ce w	ith tables 1			accordance with tables 1 and 5.			
				Interroga an ATCR interroga standard adjustabl	te the BS M tion a rate, e P1-	e UUT with Aode A at the with -P3 spacing.	ATC-1400A XPDR MDE A PRF: 0450		Turn XPDR MODE contro to the A position. Turn PRF thumbwheels to 450 Hz.	51		
	Use the set the s P1-P3 puresulting shall be			ollow bacin Ilses. ATCI as sp	ring table to g of the The RBS % reply ecified.	<u>ATC-1400A</u> CW/NORM/ OFF: NORM		Use the following table to set the XPDR P2/P3 DEX thumbwheels and P3 switch on the ATC-1400A The resulting ATCRBS % reply shall be as specified	, d.			
	13.1	>90% >90% <10% <10%		Dev D 0.20 0.20 1.00 1.00	<u>elta</u> - + -	<u>P1-P3</u> 7.8 μs 8.2 μs 9.0 μs 7.0 μs			Dev Delta P1-P3 0.20 - 7.8 μs 0.20 + 8.2 μs 1.00 + 9.0 μs 1.00 - 7.0 μs	>98% >98% <2% <2%		
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REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	14			ATCRBS INTERRC POSITIO (MODE C	OGATION PULSE N TOLERANCE			ATCRBS INTERROGATION PULSE POSITION TOLERANCE (MODE C)	
				Perform t accordan and 5.	est setup in ce with tables 1			Perform test setup in accordance with tables 1 and 5.	
				Interrogat an ATCR interrogat standard adjustable	te the UUT with BS Mode C tion at the rate, with e P1-P3 spacing.	ATC-1400A XPDR MDE C PRF: 0450		Turn XPDR MODE control to the C position. Turn PRF thumbwheels to 450 Hz.	
	Use the f set the sp P3 pulses ATCRBS as specif			ollowing table to bacing of the P1- s. The resulting % reply shall be ed.	ATC-1400A CW/NORM/ OFF: NORM		Use the following table to set the XPDR P2/P3 DEV thumbwheels and P3 switch on the ATC-1400A. The resulting ATCRBS % reply shall be as specified.		
	14.1	>90% >90% <10% <10%		Dev D 0.20 0.20 1.00 1.00	elta <u>P1-P3</u> - 20.8 μs + 21.2 μs + 22.0 μs - 20.0 μs			DevDeltaP1-P30.20-20.8 μs0.20+21.2 μs1.00+22.0 μs1.00-20.0 μs	>98% >98% <2% <2%
	15			ATCRBS <u>CAPABIL</u>	REPLY RATE ITY			ATCRBS REPLY RATE CAPABILITY	
				Perform t accordan and 5.	est setup in ce with tables 1 AW/CRITICAL			Perform test setup in accordance with tables 1 and 5.	
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REV	TEST SPECIFICATION PROCEDURE						SPECIFICATION		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	15 1	N/A		<u>Burst</u>	to the LILIT with a	ATC 1400A		Burst	N/A
	15.1	N/A		burst of 1 Mode A in rate of 12 and powe	20 ATCRBS nterrogations at a 200 per second er of -30 dBm.	CW/NORM/ OFF: OFF XPDR MDE A PRF: 1200 RF Lvl: -30	A	to MODE A position. Turn PRF thumbwheels to 1200 Hz. Turn RF LEVEL knob to -30 dBm.	N/A
						<u>S-1403</u> FUNC 7, ATC, 120 <u>ATC-1400A</u> CW/NORM/ OFF: NORM		Select FUNC 7 (BURST). Using the cursor keys and slew knob, change the fields in the FUNC 7 menu as follows: FUNC 7 ATC 120.	
						<u>S-1403</u> Press BURST	Г	On the S-1403, press the BURST key to initiate the interrogations.	
	15.2 >90% Observe reply. T as speci		Observe reply. Th as specif	the % ATCRBS he value shall be ied.			Read the ATCRBS % reply display on the S-1403. The display shall indicate as specified.	>98%	
				<u>Continuo</u>	<u>us</u>			<u>Continuous</u>	
	15.3	N/A		Interroga an ATCR interroga 500 per s	te the UUT with BS Mode A tion at a rate of second.	ATC-1400A XPDR MDE A PRF: 0500	•	On the ATC-1400A, turn the XPDR MODE knob to the A position. Adjust the PRF thumbwheels to 500 Hz.	N/A
						<u>S-1403</u> FUNC 1		On the S-1403, select FUNC 1 (ATCRBS).	
╞	<u>I</u>	<u> </u>	<u> </u>	<u> </u>	AW/CRITICAL I	NOTATION		1	
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REV	TEST		s	PECIFICATIO)N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	15.4	>90% ID = 7777		Observe reply. Th as specifi be as spe	the % ATCRBS le value shall be led. Data shall ecified.	ATC-1400A CW/NORM/ OFF: NORM		Read the % reply display on the ATC-1400A. The display shall indicate as specified. Data shall be as specified.	>98% ID = 7777
				Reply Ra	<u>te Limit</u>			Reply Rate Limit	
	15.5	N/A		Interrogat an ATCR interrogat 1500 per	te the UUT with BS Mode A tion at a rate of second.	ATC-1400A XPDR MDE A PRF: 1500		On the ATC-1400A, turn the XPDR MODE knob to the A position. Adjust the PRF thumbwheels to 1500 Hz.	N/A
						<u>S-1403</u> FUNC 1		On the S-1403, select FUNC 1 (ATCRBS).	
	15.6	>34%		Observe ATCRBS shall be a	and record the % reply. The value as specified.	ATC-1400A CW/NORM/ OFF: NORM		Read and record the % reply display on the ATC-1400A. The display shall indicate as specified.	43 to 46%
				ATCRBS Characte	SLS <u>ristics</u>			ATCRBS SLS Characteristics	
	15.7 N/A Perform accordar and 5.			Perform t accordan and 5.	est setup in ce with tables 1			Perform test setup in accordance with tables 1 and 5.	N/A
				<u>Amplitude</u>	9			<u>Amplitude</u>	
	15.8	N/A		Interrogat an ATCR interrogat pulse of a amplitude the stand	te the UUT with BS Mode C tion with P2 SLS adjustable e. Interrogate at ard rate.	ATC-1400A XPDR MDE C PRF: 0450 SLS/ECHO: -0, ON	;	Turn XPDR MODE control to MODE C position. Turn PRF thumbwheels to 450 Hz. Turn SLS/ECHO switch to ON position.	N/A
				adjust the RF level. reply for e be as spe	e interrogation The % ATCRBS each case shall ecified.	CW/NORM/ OFF: NORM		adjust the interrogation RF level. The % ATCRBS reply for each case shall be as specified.	
				<u> </u>					
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REV	TEST		SPECIFICATION PROCEDURE S					SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS
	15.9	<10% <10% <10% <10%		<u>RF Level</u> -68 dBm -40 dBm -21 dBm -50 dBm	he emplitude of	ATC 1400A		<u>RF Level</u> -68 dBm -40 dBm -21 dBm -50 dBm	<2% <2% <2% <2%
				P2 in 1-d % reply is 90%.	B steps until the s greater than	SLS/ECHO: -X		Adjust the SLS thumbwheels on the ATC-1400A in 1-dB steps until the % reply display indicates >90%.	
	15.10	-1 to -9 dB		Record th attenuation shall be w specified	ne amount of P2 on. This value within the limits	ATC-1400A CW/NORM/ OFF: NORM		Record the value shown on the SLS thumbwheels. This value shall be within the limits specified.	-1 to -9 dB
				Adjust the RF level	e interrogation to -21 dBm.	ATC-1400A RF Lvl: -21 SLS/ECHO: -0		Adjust the interrogation RF level to -21 dBm.	
				Reduce t P2 in 1-d % reply is 90%.	he amplitude of B steps until the s greater than	ATC-1400A CW/NORM/ OFF: NORM		By adjusting the SLS thumbwheels on the ATC-1400A, reduce the amplitude of P2 in 1-dB steps until the % reply display reads >90%.	
	15.11	-1 to -9 dB		Record th attenuation shall be w specified	ne amount of P2 on. This value within the limits			Record the value shown on the SLS thumbwheels. This value shall be within the limits specified.	-1 to -9 dB
								1	<u> </u>
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	C WORK STEPS	MFG LIMITS	
	<u>NO.</u> 15.12	OPR LIMITS	C	Position Set the P 0 dB. Adjust th P2 pulse following indicates which the	2 attenuation to 2 attenuation to e position of the as shown in the table. P2 Dev the amount by e P2 position will	<u>SWITCH POS</u> ATC-1400A SLS: -0, ON RF LvI: -50 <u>ATC-1400A</u> CW/NORM/ OFF: NORM		 WORK STEPS Position Set the SLS/ECHO thumbwheels on ATL-1400A to -0, ON. Adjust the P2 DEV thumbwheels and switch as shown in the following table. P2 Dev indicates the amount by which the P2 position will move and 	N/A	
				whether f added or to the no The % re position s shown.	the deviation is subtracted (+/-) minal position. ply for each shall be as			Delta indicates whether the deviation is added or subtracted (+/-) to the nominal position. The % reply for each position shall be as shown. (Read the % reply on the ATC-1400A.)	1	
	15.13	>90% >90% <10% <10%		P2 Dev 0.70 0.70 0.15 0.15	Delta - + -			P2 Dev Delta 0.70 - 0.70 + 0.15 + 0.15 -	>98% >98% <2% <2%	
-			<u> </u>	<u> </u>	AW/CRITICAL	NOTATION	<u> </u>			
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REV	EV TEST SPECIFICATION PROCEDURE				SPECIFICATION				
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS
	16			ATCRBS CALL, SH <u>POSITIO</u>	ONLY ALL- HORT P4 <u>N</u>			ATCRBS ONLY ALL- CALL, SHORT P4 POSITION	
				Perform f accordan and 5.	test setup in Ice with tables 1			Perform test setup in accordance with tables 1 and 5.	
				Interroga an ATCR interroga of adjusta Interroga standard	te the UUT with BS Mode A tion with short P4 able position. te at the rate.	ATC-1400A PRF: 0450 SLS/ECHO: OFF XPDR MDE A	A	Turn PRF thumbwheels to 450 Hz. Turn SLS/ECHO switch to OFF position. Turn the XPDR MODE CNTL to the MODE A position.	
						<u>S-1403</u> FUNC 3, (ACS, <u>A</u>) P4:CAL, Dv = 0		Select FUNC 3 (ACS). Within the FUNC 3 menu, select ACS, <u>A</u> and P4:CAL.	
				Adjust the P4 pulse shown in P4 Dv ind amount b position v nominal. reply for shall be a	e position of the on S-1403 as the table below. dicates the by which the P4 will move from The % ATCRBS each position as shown.	ATC-1400A CW/NORM/ OFF: NORM		On the S-1403, adjust the position of the P4 pulse as shown in the table below. P4 Dv indicates the amount by which the P4 position will move from nominal. The % ATCRBS reply for each position shall be as shown. (Read the % reply display on the ATC-1400A.)	
	16.1	<10% >90% <10% <10% >90%		<u>P4 Dv</u> 0 μ: +0.3 μ: +0.05 μ: -0.05 μ: -0.3 μ:	S S S S			P4 Dv μs 0 μs +0.3 μs +0.05 μs -0.05 μs -0.3 μs	<2% >98% <2% <2% >98%
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REV	TEST		s	PECIFICATIO	N			PROCEDURE		SPECIF	ICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	- 0	c wo	ORK STEPS	MFG	LIMITS
	17			ATCRBS	DIVERSITY			ATCRBS	DIVERSITY		
				Perform t accordan and 5.	est setup in ce with tables 1			Perform to accordance and 5.	est setup in ce with tables 1		
				Simultane interrogat antennas ATCRBS standard	eously te both UUT with an Mode A at rate.	ATC-1400A PRF: 0450 S-1403 FUNC 1 ANT B: 0 RF LvI: 0		Set PRF t 0450 on A FUNC 1 n turn on ar ensure Rf zero.	humbwheels to ATC-1400A. In nenu on S-1403, ntenna B and F level is set to		
				See para	graph 3.4.			See parag	graph 3.4.		
	Use the set the ritiming of interroga ATCRBS the spec antenna		ollowing table to elative power and the two tions. The % reply shall be at fied value and	ATC-1400A CW/NORM/ OFF: NORM	I	Use the for set the rel timing of t interrogat ATCRBS the specif antenna. reply on th	bllowing table to ative power and he two ions. The % reply shall be at ied value and Read the % he ATC-1400A.				
	17.1	ANTA ANTB <10% N/A >90% N/A >90% N/A <10% N/A <10% N/A <10% N/A <10% N/A		ANT A <u>RF LVL</u> -56 dBm -44 dBm -70 dBm -56 dBm -44 dBm -44 dBm	ANT B <u>DELAY</u> +0.05 µs +0.45 µs -0.45 µs -0.05 µs -0.45 µs -0.45 µs			ANT A <u>RF LVL</u> -56 dBm -44 dBm -70 dBm -56 dBm -44 dBm -44 dBm	ANT B <u>DELAY</u> +0.05 µs +0.45 µs -0.45 µs -0.05 µs -0.45 µs -0.45 µs	<u>ANTA</u> <2% >98% >98% <2% <2% <2%	ANTB N/A N/A N/A N/A N/A N/A
		1	<u> </u>		AW/CRITICAL	NOTATION				<u></u>	
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	<u>NO.</u> 18	OPR LIMITS	C		MTL REPLY	SWITCH POS	C		MFG LIMITS
	10			FORMAT	, REPLY			FORMAT, REPLY	
				POWER,	AND			POWER, AND	
				SQUITTE	ERS			SQUITTERS	
				Perform f accordan and 5.	test setup in ice with tables 1			Perform test setup in accordance with tables 1 and 5.	
				Interroga Mode S I at the sta	te the UUT with JF 5 interrogation andard rate.	ATC-1400A PRF: 0045		Select 0045 on the PRF thumbwheels of the ATC-1400A.	
						<u>S-1403</u> FUNC 2 ANT B: OFF		Select FUNC 2 (SEQ - MODE S ONLY) on the S-1403.	
				Mode S M	MTL			Mode S MTL	
	18.1	N/A		Set interr 70 dBm.	rogation level to -	<u>ATC-1400A</u> RF Lvl: -70		Set the RF level on the 1400A to -70 dBm.	N/A
	18.2 DF = 05 Observe FS = 5 DR = 00 outle = 00 UM = 00 ID = 7777 ADD = 2525255		the Mode S reply ta shall be as	ATC-1400A CW/NORM/ OFF: NORM	I	Observe the decoded Mode S reply on the S-1403 sequence menu (S MENU) display. Data shall indicate as specified.	DF = 05 FS = 5 DR = 00 UM = 00 ID = 7777 ADD = 25252525		
	18.3	-77 ± 3 dBm		Slowly de interroga steps unt less than incremen interroga as specif value.	ecrement the tion level in 1-dB til the % reply is 90%, then it by 1 dB. The tion level shall be ied. Record the			Slowly decrement the RF LEVEL knob on the ATC-1400A in 1-dB steps until the % reply display indicates less than 90%, then increment the RF level by 1 dB. The RF level shall indicate as specified. Record the value.	-77 ± 3 dBm
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	18.3.1	N/A		Mode S F Format Return th knob on t to -70 dB Interroga Mode S U	Reply the RF LEVEL the ATC-1400A m. te the UUT with a JF 4	ATC-1400A RF LvI: -70 dBm <u>S-1403</u> UF = 04		Mode S Reply <u>Format</u> Return the RF LEVEL knob on the ATC-1400A to -70 dBm. Select UF#04 on the S-1403.	N/A
	18.3.2	DF = 04 FS = 5 DR = 00 UM = 00 AC = +20200 ADD = 25252525		Observe Mode S r shall be a Verify the agrees w altitude n paragrap = 7710 in	the decoded eply data. Data as specified. AC value ith the coded neasured in h 11.6. (+20200 i Gilliam code)			Observe the decoded Mode S reply on the S-1403 sequence menu (S MENU) display. Data shall indicate as specified. Verify the AC value agrees with the coded altitude measured in paragraph 11.6. (+20200 = 7710 in Gilliam code)	DF = 04 FS = 5 DR = 00 UM = 00 AC = +20200 ADD = 25252525
	18.3.3DF = 04 FS = 5 DR = 00Observe Mode S shall be		Interroga Mode S U interroga Observe Mode S r shall be a	te the UUT with a JF 20 tion. the decoded eply data. Data as specified.	<u>S-1403</u> UF = 20		Select UF#20 on the S-1403. Observe the decoded Mode S reply on the S-1403 sequence menu	DF = 04 FS = 5 DR = 00	
		UM = 00 AC = +20200 ADD = 25252525						(S MENU) display. Data shall indicate as specified.	UM = 00 AC = +20200 ADD = 25252525
				Interroga Mode S U interroga	te the UUT with a JF 21 tion.	<u>S-1403</u> UF = 21		Select UF#21 on the S-1403.	
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS		
	18.3.4	DF = 05 FS = 5 DR = 00 UM = 00 ID = 7777 ADD = 25252525		Observe t Mode S re shall be a Verify that with the II paragraph	he decoded eply data. Data s specified. t this ID agrees D data in n 15.4.			Observe the decoded Mode S reply on the S-1403 sequence menu (S MENU) display. Data shall indicate as specified. Verify that this ID agrees with the ID data in paragraph 15.4.	DF = 05 FS = 5 DR = 00 UM = 00 ID = 7777 ADD = 25252525		
				Interrogat Mode S U interrogat	e the UUT with a F 11 ion	<u>S-1403</u> UF =11 ADD = 77777777		Select UF#11 on the S-1403.			
			Perform accorda and 5 e: 1 to AIR normal test is c		est setup in ce with tables 1 ept set AIR/GND Restore to sition after this npleted.	AIR/GND 1 to air or open	D	Perform test setup in accordance with tables 1 and 5 except set AIR/GND 1 to AIR. Restore to normal position after this test is completed.			
	18.3.5	DF = 11 AA = 25252525 PI = 00000000 CA = 1 for -902 1 for -903 1 for -904 0 for -905		Observe t Mode S re shall be a	he decoded eply data. Data s specified.			Observe the decoded Mode S reply on the S-1403 sequence menu (S MENU) display. Data shall indicate as specified.	DF = 11 AA = 25252525 PI = 00000000 CA = 1 for -902 1 for -903 1 for -904 0 for -905		
				Mode S R <u>Frequenc</u>	eply Y			Mode S Reply <u>Frequency</u>			
	18.4	N/A		This test r S reply fre	measures Mode equency.			This test measures Mode S reply frequency.	N/A		
	18.5	1090 ± 1 MHz		Measure a reply frequency specified.	and record the uency. y shall be as <u>eply Delay</u>	ATC-1400A RF Lvl: -50		Observe and record the XMTR FREQ display on the ATC-1400A. Display shall indicate as shown.	1090 ± 1 MHz		
	18.6	N/A		This test r S reply de	measures Mode elay.			This test measures Mode S reply delay.	N/A		
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS		
	18.7	128 ± 0.25 μs		Measure a reply dela be as spe	and record the y. Delay shall cified.			Observe and record the reply field on the S-1403. Field shall indicate as specified.	128 ± 0.25 μs		
				Mode S R	eply Power			Mode S Reply Power			
	18.8	N/A		This test r S reply po	measures Mode ower.			This test measures Mode S reply power.	N/A		
	18.9	250 to 630 Watts		Measure a peak powe preamble Mode S re shall be as	and record the er of the first pulse of the eply. The power s specified.	S-1403 CONTROL MENU 2, Pulse Power Gate: p1 <u>ATC-1400A</u> F2/P2 F1/P1: F2		On the S-1403, change the pulse power gate to p1. Observe and record the XMTR PWR display on the ATC-1400. The display shall indicate as specified.	280 to 550 Watts		
	18.10	250 to 630 Watts		Measure a peak powe data pulse reply. The a specified	and record the er of the last e of the Mode S e power shall be d.	S-1403 CONTROL MENU 2, Pulse Power Gate: P60 ATC-1400A F2/P2 F1/P1: F2		On the S-1403, change the pulse power gate to P60. Observe and record the XMTR PWR display on the ATC-1400. The display shall indicate as specified.	280 to 550 Watts		
	18.11	<2 dB	Calculate absolute of transmitte using the formula: Droop = A P1 - Log ₁₀ and P2 ar measuren tests 18.9 respective shall not e specified			Calculate absolute v transmitte using the formula: Droop = A P1 - Log ₁₀ and P2 ard measurem tests 18.9 respective shall not e specified I	and record the value of the r droop (in dB), following ABS [10(Log ₁₀ p P2)], where P1 e the power nents taken in and 18.10, ely. The droop exceed the limits.			Calculate and record the absolute value of the transmitter droop (in dB), using the following formula: Droop = ABS [10(Log ₁₀ P1 - Log ₁₀ P2)], where P1 and P2 are the power measurements taken in tests 18.9 and 18.10, respectively. The droop shall not exceed the specified limits. Squitter Monitor	<2 dB
	18.12	N/A				<u>S1403</u> FUNC 2 ANT B: +.00		On S-1403, enter FUNC 2 and turn on ANT B.	N/A		
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LTR	NO.	OPR LIMITS	с	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS	
	18.13	PASS		Observe interval o 10 secon shall not from 0.8	the squitter over a period of ids. The value exceed the limits to 1.2 seconds.			Observe the squitter interval on the S-1403 over a period of 10 seconds. The value shall not exceed the limits from 0.8 to 1.2 seconds.	PASS	
						<u>S-1403</u> ANT B: OFF				
	19			MODE S <u>CAPABIL</u>	REPLY RATE <u>.ITY</u>			MODE S REPLY RATE CAPABILITY		
				Perform f accordan and 5.	test setup in ace with tables 1			Perform test setup in accordance with tables 1 and 5.		
				Interroga UF 5 inte rate of 17	te the UUT with a progation at a 77 per second.	ATC-1400A PRF: 0177		On the ATC-1400A, adjust the PRF thumbwheels to 0177.		
						<u>S-1403</u> FUNC 2		On the S-1403, select FUNC 2 (SEQ - MODE S ONLY).		
			Observe reply cha values sh specified		the following tracteristics. The nall be as	ATC-1400A CW/NORM/ OFF: NORM	1	Observe the following reply characteristics on the ATC-1400A displays. The values shall be as specified.		
	19.1 PASS Over a seconds reply sh 90%.		Over a po seconds, reply sha 90%.	eriod of 10 the Mode S % II not fall below			Over a period of 10 seconds, the Mode S % reply shall not fall below 98%.	PASS		
	19.2	PASS		Reply Fre 1090 ± 1	equency MHz			Reply Frequency 1090 ± 1 MHz	PASS	
	19.3PASSReply Po250 to 63		Reply Po 250 to 63	wer 30 Watts			Reply Power 280 to 550 Watts	PASS		
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LTR	NO.	OPR LIMITS	С	TEST DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS		
	20	OPREIMITS		LONG MODE S Perform test setup in accordance with tables 1 and 5. Interrogate the UUT with a long Mode S interrogation at the standard rate.	ATC-1400A PRF: 0045 SLS/ECHO: OFF <u>S-1403</u> FUNC 2, ANT B: OFF S-MENU: SO1; UF = 16 RL = 1 MU = 06000 0000000		LONG MODE S Perform test setup in accordance with tables 1 and 5. Turn PFR thumbwheels to 45 Hz, turn SLS/ECHO switch to OFF position. Select FUNC 2 and set the S-MENU: SO1; UF = 16 RL = 1 MU = 0600000000 0000001 ADD = 25252525			
	20.1	>90%		Observe the % Mode S reply. It shall not fall below 90%.	0000001 ADD = 2525 2525 <u>ATC-1400A</u> CW/NORM/ OFF: NORM		Observe the % Mode S reply. It shall not fall below 98%.	>98%		
	20.2			With an oscilloscope, look at both the suppression pulse output and the Mode S reply pulse train coming from the UUT.			With an oscilloscope look at both the suppression pulse output (Sup Pulse connector on the Mode S interface panel) and the Mode S reply transmitter output on the ATC-1400A. Use a 50-ohm impedance on the transmitter and 1M ohm on the suppression.			
	20.3	PASS		Verify that the suppression pulse brackets the Mode S reply.			Verify that the suppression pulse brackets the Mode S reply.	PASS		
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	20.4	500 ± 50 ns		Measure 50-50% p P1 pulse reply. Th shall be a	and record the ulse width of the in the Mode S e measurement s specified.			Measure and record the 50-50% pulse width of the P1 pulse in the Mode S reply. The measurement shall be as specified.	500 ± 40 ns
	21			ATCRBS/ CALL, LC <u>AMPLITU</u>	MODE S ALL- NG P4 <u>DE</u>			ATCRBS/MODE S ALL- CALL, LONG P4 <u>AMPLITUDE</u>	
				Perform to accordance and 5 exco 1 to AIR.	est setup in ce with tables 1 ept set AIR/GND	AIR/GND 1 to air or open		Perform test setup in accordance with tables 1 and 5 except set AIR/GND 1 to AIR.	
				Interrogat an ATCR interrogat of adjusta Interrogat standard	e the UUT with BS Mode A ion with long P4 ble amplitude. e at the rate.	ATC-1400A PRF: 0045 SLS/ECHO: OFF XPDR MDE A		Turn PRF thumbwheels to 45 Hz. Turn SLS/ECHO switch to OFF position. Turn the XPDR MODE CONTROL to MODE A.	
						<u>S-1403</u> FUNC 4, (ACL) P4:VAR, Dv = CAL		Select FUNC 4 (ACL). Within the FUNC 4 menu, select ACL and P4:VAR.	
				Use the for adjust the RF level a of P4 atte Mode S ro case shal	ollowing table to interrogation and the amount nuation. The % eply for each I be as specified.	ATC-1400A CW/NORM/ OFF: NORM		Use the following table to adjust the interrogation RF level and the amount of P4 attenuation (SLS/ECHO thumbwheels on ATC-1400A). The % Mode S reply for each case shall be as specified. Read the value on the S-1403 Mode S % reply field.	
	21.1	<10% >90% >90% <10% >90%		RF Level -21 dBm -21 dBm -40 dBm -40 dBm -68 dBm -68 dBm	<u>P4</u> -6 -1 -1 -6 -6 -1			RF Level P4 -21 dBm -6 -21 dBm -1 -40 dBm -1 -40 dBm -6 -68 dBm -6 -68 dBm -1	<2% >98% >98% <2% <2% >98%
	1	1	<u> </u>	<u>ı</u>	AW/CRITICAL	NOTATION	<u> </u>	1	<u>I</u>
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
REV	TEST NO. 22	OPR LIMITS	S	PECIFICATIO TEST Return Pa amplitude MODE S TOLERA Perform t accordan and 5. Interroga Mode S U at the sta power. Use the f vary the p SPR. Th Mode S r shown.	N DESCRIPTION 4 to full (0 attenuation). SPR POSITION NCE rest setup in ce with tables 1 te the UUT with a JF 5 interrogation indard rate and ollowing table to position of the e resulting % eply shall be as	<u>Switch Pos</u> <u>S-1403</u> P4:CAL P4:CAL PRF 0045 RF LvI: -50 <u>S-1403</u> FUNC 2, SPR, Dv = as indicated <u>ATC-1400A</u> CW/NORM/ OFF: NORM		PROCEDURE WORK STEPS Return P4 to full amplitude. MODE S SPR POSITION TOLERANCE Perform test setup in accordance with tables 1 and 5. On the ATC-1400A, adjust the PRF thumbwheels to 45 Hz. Use the following table to move the SPR from its nominal position. (Vary the SPR Dv field of the S-1403.) The resulting % Mode S reply shall be as shown. Read the % reply on the ATC-1400A.	SPECIFICATION MFG LIMITS
	22.1	<10% >90% >90% <10%		<u>SPR Dev</u> +0.25 μs +0.05 μs -0.05 μs -0.25 μs	iation			<u>SPR Deviation</u> +0.25 μs -0.05 μs -0.25 μs	<2% >98% >98% <2%
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REV TEST SPECIFICATI			PECIFICATION	1			PI	ROCEDURE	SPECIF	ICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS		с	WORK STEPS	MFG	LIMITS
	23			MODE S I Perform te accordanc and 5.	DIVERSITY est setup in ce with tables 1	ATC-1400A		 	MODE S DIVERSITY Perform test setup in accordance with tables 1 and 5.		
				interrogate antennas UF 5 inter standard r	e both UUT with a Mode S rogation at rate.	PRF: 0045 <u>S-1403</u> FUNC 2, ANT B: 0 RF LvI: 0		(50045 on ATC-1400A. In FUNC 2 menu on S-1403, turn on antenna B and ensure RF level is set to zero.		
			See para		graph 3.4.			:	See paragraph 3.4		
				Use the for set the rel timing of t interrogati Mode S re the specifi antenna.	ollowing table to lative power and he two ions. The % eply shall be at ied value and			 	Use the following table to set the relative power and timing of the two interrogations. The % Mode S reply shall be at the specified value and antenna. Read ANT A % reply on the ATC-1400A. Read ANT B % reply on the S-1403.		
	23.1	ANTA ANTB <10% >90% >90% <10% >90% <10% <10% >90% <10% >90% <10% >90% <10% >90% <10% >90%		ANT A <u>RF LVL</u> -56 dBm -44 dBm -70 dBm -56 dBm -44 dBm -44 dBm	ANT B <u>DELAY</u> +0.05 µs +0.45 µs -0.05 µs -0.05 µs -0.45 µs -0.45 µs				ANT A ANT B <u>RF LVL</u> <u>DELAY</u> -56 dBm +0.05 µs -44 dBm +0.45 µs -70 dBm +0.45 µs -56 dBm -0.05 µs -44 dBm -0.05 µs -44 dBm -0.45 µs -44 dBm -0.45 µs	ANTA <2% >98% >98% <2% <2% <2%	ANTB >98% <2% <2% >98% >98% >98%
			-		AW/CRITICAL	NOTATION					
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
	NO.	OPR LIMITS	C	IESI	DESCRIPTION	SWITCH POS	0	WORK STEPS	MEGLIMITS
	24			TRANSA Perform t	CTION RESET est setup in			TRANSACTION RESET	
				accordan and 5.	ce with tables 1			accordance with tables 1 and 5.	
				Interroga Mode S U followed ATCRBS interroga this doub shall be 4	te the UUT with a JF 5 interrogation 75 μs later by Mode A tion. The rate of le interrogation 5 Hz.	ATC-1400A DBL INTER: OFF, 075.0 XPDR MDE A PRF: 0045	~	On the ATC-1400A, adjust the DBL INTERR/INTRF thumbwheels to 075.0 µs and OFF. Adjust the PRF thumbwheels to 0045. Turn the XPDR MODE knob to A.	
						<u>S-1403</u> FUNC 6, 1ST = SEQ 2ND = ATC ANT B: OFF		On the S-1403, select FUNC 6 (DI). In this menu, select 1ST = SEQ and 2ND = ATC.	
	24.1	ATCRBS: <10% Mode S: >90%		Observe both Mod ATCRBS values sh	the % reply for e S and replies. The all be shown.	ATC-1400A CW/NORM/ OFF: NORM		Read the % reply fields for both Mode S and ATCRBS on the S-1403 display. The values shall indicate as shown.	ATCRBS: <2% Mode S: >98%
	Ch: S in			Change 1 S interroo	bit of the Mode gation address.	<u>S-1403</u> S01: ADD = 152525		In the sequence menu (S01) of the S-1403, change 1 bit in the interrogation address (ADD = 152525).	
	24.2	4.2 ATCRBS: >90% Mode S: <10% Observe both Mod ATCRBS values s		the % reply for e S and replies. The all be as shown.	<u>S-1403</u> FUNC 6		Read the % reply fields for both Mode S and ATCRBS on the S-1403 display. The values shall indicate as shown.	ATCRBS: >98% Mode S: <2%	
	Change S S interrog			bit of the Mode gation address.	<u>S-1403</u> S01: ADD = 152521		In the sequence menu (S01) of the S-1403, change 1 bit in the interrogation address (ADD = 152521).		
	24.3	ATCRBS: >90% Mode S: <10%	Observe t both Mode ATCRBS values sh		the % reply for e S and replies. The all be as shown.	<u>S-1403</u> FUNC 6		Read the % reply fields for both Mode S and ATCRBS on the S-1403 display. The values shall indicate as shown.	ATCRBS: >98% Mode S: <2%
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REV	TEST		s	PECIFICATIO	N		PROCEDURE		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	25			SUPPRE	<u>SSION BUS</u> test setup in			SUPPRESSION BUS Perform test setup in	
				accordan and 5.	ice with tables 1			accordance with tables 1 and 5.	
				Interroga ATCRBS interroga standard	te UUT with an Mode A tion at the rate and power.	<u>S-1403</u> FUNC 1 <u>ATC-1400A</u> PRF: 0450 XPDR MDE <i>A</i>	Ą	On the ATC-1400A, set the PRF thumbwheels to 0450 Hz and the XPDR MODE knob to A.	
				With an o at both th pulse out ATCRBS coming fi	oscilloscope, look ne suppression put and the reply pulse train rom the UUT.	ATC-1400A CW/NORM/ OFF: NORM		With an oscilloscope, look at both the suppression pulse output (Sup Pulse connector on the Mode S interface panel) and the ATCRBS reply (Xmtr output on the ATC-1400A). Use a 50-ohm impedance to look at the Xmtr output and a 1M ohm impedance for the suppression pulse.	
	25.1	PASS		Verify tha suppress brackets reply.	at the ion pulse the ATCRBS			Verify that the suppression pulse brackets the ATCRBS reply.	PASS
	25.2	>18 V <32 V		Measure amplitude suppress amplitude specified	and record the e of the ion pulse. The e shall be as			Measure and record the amplitude of the suppression pulse. The amplitude shall be as specified.	28 ± 3 V
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS		
	26			REPLY P CHARAC Perform 1 accordan and 5. Interroga an ATCR interroga standard	PULSE <u>CTERISTICS</u> test setup in te with tables 1 te the UUT with BS Mode A tion at the rate and power.	ATC-1400A PRF: 0450 XPDR MDE A <u>S-1403</u> FUNC 1		REPLY PULSE <u>CHARACTERISTICS</u> Perform test setup in accordance with tables 1 and 5. On the ATC-1400A, set the PRF thumbwheels to 0450 Hz and the XPDR MODE knob to A. On the S-1403, enter FUNC 1. Using an oscilloscope with 50-ohm impedance, look at the XMTR output of the ATC 1400A			
	26.1	Rise: <0.1 μs Fall: <0.2 μs		Measure 10% to 9 times of t pulse, F1 measure specified	and record the 0% rise and fall the first framing . The ments shall be as			Measure and record the 10% to 90% rise and fall times of the first framing pulse, F1. The measurements shall be as specified.	Rise: <0.1 μs Fall: <0.2 μs		
	26.2	450 ± 100 ns		Measure 50-50% g first fram The meas be as spe	and record the oulse width of the ing pulse, F1. surement shall ecified.			Measure and record the 50-50% pulse width of the first framing pulse, F1. The measurement shall be as specified.	450 ± 80 ns		
	26.3	20.3 ± 1 μs		Measure spacing b framing p F2. The shall be a	and record the between the two bulses, F1 and measurements as specified.			Measure and record the spacing between the two framing pulses, F1 and F2. The measurements shall be as specified.	20.3 ± 1 μs		
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	27	OPR LIMITS		ANTENN Perform t accordan and 5. Turn UUT Remove ANT and TOP ANT open circ terminato	<u>A TEST</u> test setup in ice with tables 1 Γ off. cable from TOP terminate the Γ port with an cuit (infinite ohm) or. Γ on.	SWITCH POS		PROCEDURE WORK STEPS ANTENNA TEST Perform test setup in accordance with tables 1 and 5. Turn UUT off. Remove cable from TOP ANT port on Mode S interface panel, and terminate the TOP ANT port with an open circuit (infinite ohm) terminator. Turn UUT on.	SPECIFICATION MFG LIMITS	
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS		
	27.1	ON		Verify tha XPDR FA lamps ar when the pushed r any versi a -905 M	at TOP ANT and AIL indicator e illuminated TEST button is nomentarily for ion that is prior to OD E.			Press the TEST button on the UUT. Verify that, on the front panel of the Mode S Transponder, the TOP ANT and XPDR FAIL indicator lamps are illuminated when the TEST button is pushed momentarily for any version that is prior to a -905 MOD E.	ON		
				Verify tha and XPD lamps ar when the pushed n -905 MO	at TOP ANT FAIL R PASS indicator e illuminated TEST button is nomentarily for a D E.			Press the TEST button on the UUT. Verify that, on the front panel of the Mode S Transponder, the TOP ANT FAIL and XPDR PASS indicator lamps are illuminated when the TEST button is pushed momentarily for a -905 MOD E.			
				Turn UU	T off.			Turn UUT off.			
				Replace cable. R from the and term ANT port circuit (in terminato Turn UU	the TOP ANT emove cable BOT ANT port inate the BOT with an open finite ohm) or. T on.			On the Mode S inter-face panel, replace the TOP ANT cable. Remove cable from the BOT ANT port and terminate the port with an open circuit (infinite ohm) terminator. Turn UUT on.			
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION			
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS			
	27.2	ON		Verify tha and XPD lamps are when the pushed n any versi a -905 M Verify tha and XPD lamps are when the pushed n -905 MO	at the BOT ANT R FAIL indicator e illuminated TEST button is nomentarily for on that is prior to OD E. At BOT ANT FAIL R PASS indicator e illuminated TEST button is nomentairly for a D E.			Press the TEST button on the UUT. Verify that, on the front panel of the Mode S Transponder, the BOT ANT and XPDR FAIL indicator lamps are illuminated when the TEST button is pushed momentarily for any version that is prior to a -905 MOD E. Press the TEST button on the UUT. Verify that, on the front panel of the Mode S Transponder, the BOT ANT FAIL and XPDR PASS indicator lamps are	ON			
								illuminated when the TEST button is pushed momentarily for a -905 MOD E.				
				Turn off I	JUT.			Turn off UUT.				
				Replace ANT port	cable on BOT			On the Mode S interface panel, replace BOT ANT cable.				
	28			SYSTEM	STATUS			SYSTEM STATUS				
				Perform accordan and 5.	test setup in ice with tables 1			Perform test setup in accordance with tables 1 and 5.				
				With Moo running, button or of the UL	de S software press the TEST n the front panel JT.	INTFC PNL AIR/GND 1: GROUND AIR/GND 2: GROUND		With Mode S software running, press the TEST button on the front panel of the UUT.				
				Observe LEDs. T illuminate test), the specified	the front panel hey should all briefly (lamp n display as			Observe the front panel LEDs. They should all illuminate briefly (lamp test), then display as specified.				
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REV	TEST		SPECIFICATIO)N				PROCEDURE	SP	ECIFICATION
LTR	NO.	OPR LIMITS	C TEST	T DESCRIPTION	SWITCH	POS	С	WORK STEPS	N	AFG LIMITS
	TEST NO. 28.1 28.2	OPR LIMITS	SPECIFICATIO	IN T DESCRIPTION Ame ISS IL L test 3 to clear any ogged in E ² . TEST	SWITCH	POS		WORK STEPS Signal Name XPDRPASS XPDRFAIL CNTLPNL TOPANT BOTANT ALTSIG	SP On off off off	ECIFICATION MFG LIMITS
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APPENDIX A ALTERNATIVE TEST PROCEDURE FOR PC EMULATION SYSTEM

- **A1.** Notes on operation of the PC emulation system are as follows:
- A1.1 All tests requiring the PC emulation system shall be performed while executing the SDP-185 monitor software. In the engineering environment, the monitor is invoked by executing the 'SCP MDS' command file.
- **A1.2** All address and data information should be entered in Hex with a leading # for CACHE locations and a leading 1# for local memory. See example in 3.4.3.
- A1.3 Most emulation commands are invoked by pressing the softkeys. Only addresses, data, and 'MDS' are typed using the standard typewriter keys. For example, to change the contents of location C000 (Hex) to the value AAAA (Hex), the following PC command would be entered:

modify memory MDS word 0#C000 to AAAA

In this example, the words (modify, memory, word, and to) are entered with single-keystroke softkeys.

- A2. Notes and a list of PC command files (Honeywell Part No. MT4061400-101, revision a) are as follows:
- A2.1 The total execution time of this Integrated Test Procedure may be greatly reduced by using the PC command files listed in paragraph 3.11.3. These command files automatically execute many keystrokes found in certain tests and match statement-for-statement (excluding 'pause' statements) the steps found in the IT work steps.
- **A2.2** After executing the 'SCP MDS' batch file (see paragraph 3.4), execute the command file by typing the filename (using all caps) into the PC and pressing ENTER. Most command files pause to allow the operator to record data. Press any key to resume command file execution.

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LTR	AD 2	IT Test N	10	PC Command Filoname			
	A2.3	<u>n restru</u>	<u>10.</u>	PC Command Filename	2		
		1		IT10 IT20			
		3		IT30 (See note 1.)			
		4					
		5 6		IT60			
		7		IT70 (Shall be executed twi	ce to cor	nplete test 7.)	
		8 8.1		IT80 IT81			
		9		IT90			
		10		IT100 (See note 2.)			
		<u>NOTE 1:</u>	Before rur at the con	nning IT30, test A3 must be npletion of the IT to clear E ²	perform	ed. IT30 must also be perf	ormed
		<u>NOTE 2:</u>	Before rur (Resume	nning IT100, end out of emu testing at test 10.13.)	lation m	ode by entering "END" into	the PC.
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS
				ALTERN PROCED	ATIVE TEST DURES			ALTERNATIVE TEST PROCEDURES	
	A1			<u>SOFTWA</u>	RE LOAD			SOFTWARE LOAD	
				Run the S program paragrap	SCP MDS (refer to h 3.4.1).			Run the SCP MDS program (refer to paragraph 3.4.1).	
	A1.1	7FDE:580D 7FDF:EEE0		Observe in progra locations 7FDFH). be as spe	the CRC words m ROM (memory 7FDEH and The values shall ecified for a -902.			Enter "display memory 07FDEH thru 07FDFH" The values shall be as specified for a -902.	7FDE:580D 7FDF:EEE0
	A1.2	7FDE:718F 7FDF:3150		Observe in progra locations 7FDFH). be as spe	the CRC words m ROM (memory 7FDEH and The values shall ecified for a -903.			Enter "display memory 07FDEH thru 07FDFH" The values shall be as specified for a -903.	7FDE:718F 7FDF:3150
	A1.3	7FDE:0E2D 7FDF:F202		Observe in progra locations 7FDFH). be as spe	the CRC words m ROM (memory 7FDEH and The values shall ecified for a -904.			Enter "display memory 07FDEH thru 07FDFH" The values shall be a specified for a -904.	7FDE:0E2D 7FDF:F202
	A1.4	7FDE:4353 7FDF:F5DF		Observe in progra locations 7FDFH). be as spe not MOD	the CRC words m ROM (memory 7FDEH and The values shall ecified for a -905 E.			Enter "display memory 07FDEH thru 07FDFH" The values shall be as specified for a -905 not MOD E.	7FDE:4353 7FDF:F5DF
	A1.5 7FDE:6C8B 7FDF:BA1D Observe in progra locations 7FDFH). be as sp MOD E.			the CRC words m ROM (memory 7FDEH and The values shall ecified for a -905			Enter "display memory 07FDEH thru 07FDFH" The values shall be as specified for a -905 MOD E.	7FDE:6C8B 7FDF:BA1D	
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	A2			CACHE I Modify re 0. If this IT	RAM gister CACHE to is being			CACHE RAM	
				performe automate complete If this IT performe manual to complete	d using an ed test facility, test 2.1. is being d using the est fixture, test 2.2.			performed using an automated test facility, complete test 2.1. If this IT is being performed using the manual test fixture, complete test 2.2.	
	A2.1	N/A		Automate Write the address (3FFFH) t in cache thru 23FF	ed Procedure cache RAM (0000H thru o each location RAM (20000H FFH).			Automated Procedure Write the cache RAM address (0000H thru 3FFFH) to each location in cache RAM (20000H thru 23FFFH).	N/A
	A2.1 .1	0000H thru 3FFFH		Verify tha RAM loca correct d Write the each cac (FFFFH t each loca RAM (20 23FFFH)	at each cache ation contains the ata. complement of he RAM address hru C000H) to a-tion in cache 000H thru			Verify that each cache RAM location contains the correct data. Write the complement of each cache RAM address (FFFFH thru C000H) to each loca-tion in cache RAM (20000H thru 23FFFH).	0000H thru 3FFFH
A2.1 FFFH thru Verify th .2 C000H RAM loc correct of			Verify tha RAM loca correct d	at each cache ation contains the ata.			Verify that each cache RAM location contains the correct data.	FFFH thru C000H	
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LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS	
н	A2.2	N/A		<u>Manual T</u> Modify m thru 23Ff	est Procedure emory 20000H FH to 0AAAAH.			Manual Test Procedure Enter "modify memory MDS word 0#20000 thru 0#23EEE to AAAA"	N/A	
	A2.2 .1	ААААН		Display n thru 2007	nemory 20000H 7FH.			Enter "display memory MDS word from 0#20000 to 0#23FFF"	ААААН	
Н				Modify m thru 23FF	emory 20000H FH to 5555H.			Enter "modify memory MDS word 0#20000 thru 0#23FFF to 5555"		
	A2.2 .2	5555H		Display n thru 2007	nemory 20000H 7FH.			Enter "display memory MDS word from 0#20000 to 0#2007F"	5555H	
	A2.2 .3	5555H		Display n thru 23FF	nemory 23E80H FFH.			Enter "display memory MDS word from 0#23E80 to 0#23FFF"	5555H	
	A3	N/A		EEPRON	1			EEPROM	N/A	
н				Write LRI serial nur EEPROM 9FCOH to respectiv for dash i stands fo	U dash and mbers to the l locations o 9FCAH ely. D/N stands number and S/N or serial number.			Enter "modify memory MDS word 0#20000 (1st digit of D/N), (2nd digit of D/N), (3rd digit of S/N), (1st digit of S/N), (2nd digit of S/N), (2nd digit of S/N), (3rd digit of S/N), (3rd digit of S/N), (3rd digit of S/N), (3rd digit of S/N), (6th digit of S/N), (6th digit of S/N), (7th digit of S/N), (8th digit of S/N)." D/N stands for dash number and S/N for serial number. (Digit space comma space) Example: for unit with dash number 905 and serial number 90100257, enter modify memory MDS word 0#20000 9, 0, 5, 9, 0, 1, 0, 0, 2, 5, 7		
		=	_ 1		AW/CRITICAL I	NOTATION	1		I	
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REV	TEST		SPECIFIC	CATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	c	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
H	TEST NO. A3.1	OPR LIMITS N/A	SPECIFIC C C NOT 8 bits If 16 spec signi Thus as 34 Write addro to ea EEPI 9FFF locat Write each (FFH locat (8OC Verif	E: E s wid bits fican s, FE 4H. e the ess (ach lo ROM FH). tion. e the i EFF thru tion i DOH y eac contained thru tion i DOH ty eac	EEPROM is only le, instead of 16. of data are , only the least at 8 bits are used. 34H is the same EEPROM (OOH thru FFH) ocation in 1 (8000H thru Verify each complement of PROM address J OOH) to each n EEPROM thru 9FFFH). ch location. value FFH to ation in EEPROM thru 9FFFH). ch location.	SWITCH POS		PROCEDURE WORK STEPS Enter "SCP MDS" Enter "load verify MDS IT30P.RA" Enter "modify registers MDS E0 80E" Enter "modify registers MDS P 109" Enter "Set discretes MDS to 1FA" Enter "run MDS" Program will take several minutes to complete. Front panel LEDs shall flash in a repetitive pattern while the program is executing. When the program is complete all six LEDs shall be illuminated.	SPECIFICATION MFG LIMITS N/A
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REV	V TEST SPECIFICATIO			N			F	ROCEDURE	SPECIFICATION		
LTR	NO.	OPR LIMITS	с	TEST	DESCRIPTION	SWITCH POS	c	С	WORK STEPS	MFG LIMITS	
	A3.2	5041 5353		Write the serial nur format, to locations 9FCAH.	LRU dash and mbers, in ISO-5 o the EEPROM 9FCOH to				Enter "display memory MDS word from 0#20010 to 0#20011" The memory locations shall be as indicated.	5041 5353	
	A3.3	PASS		Verify tha in locatio 9FCAH c proper da numbers	at each 8-bit word ns 9FCOH thru ontains the ash and serial				Enter "display memory MDS word from 1#9FC0 to 1#9FCA" Verify that the 8 bits for each word are as follows: - Lower 4 bits from the serial number and dash number of the UUT. - Upper 4 bits contain a 3 Hex. Enter "END" Enter "SCP MDS"	PASS	
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REV	TEST		s	PECIFICATIO	N	PROCEDURE		SPECIFICATION		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
REV	TEST NO. A4	OPR LIMITS	000H thru 00FHVerify th RAM loc correct of Write the address COOFH).000H thru 		SEE THE - T DESCRIPTION AM A Video RAM (CO00H thru to each location RAM (CO00H thru to each location RAM (CO00H thru at each video ation contains the ata. Complement of eo RAM address hru 3FF0H) to ation in video	SWITCH POS		PROCEDURE WORK STEPS VIDEO RAM Enter "modify memory MDS word 1#C000 0C000, 0C001, 0C002, 0C003" Enter "modify memory MDS word 1#C0004 0C004, 0C005, 0C006, 0C007" Enter "modify memory MDS word 1#C00B 0C008, 0C009, 0C00A, 0C008" Enter "modify memory MDS word 1#C00C 0C00C, 0C00D, 0C00E, 0C00F" Enter "display memory MDS word from 1#C000 to 1#C00F Verify that each video RAM location contains the correct data. Enter "modify memory MDS word 1#C000 3FFF, 3FFE, 3FFD, 3FFC"	SPECIFICATION MFG LIMITS MFG LIMITS	
				RAM (C0 C00FH).	00H thru			Enter "modify memory MDS word 1#C004 3FFB, 3FFA, 3FF9, 3FF8" Enter "modify memory MDS word 1#C008 3FF7, 3FF6, 3FF5, 3FF4" Enter "modify memory MDS word 1#C00C 3FF3, 3FF2, 3FF1, 3FF0" Enter "display memory MDS word from 1#C000 to 1#C00F"		
	₽₽	ΛΠΟνιν		11	AW/CRITICAL N					
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REV	TEST		S	PECIFICATIO	N		-	PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS
	A4.2	3FFFH thru 3FF0H		Verify tha RAM loca correct da	at each video ation contains the ata.			Verify that each video RAM location contains the correct data.	3FFFH thru 3FF0H
	A5			<u>INPUT D</u>	ISCRETES			INPUT DISCRETES	
				Perform t accordan	est setup in ce with table 2.			Perform test setup for the transponder interface panel in accordance with table 2.	
				Read the memory I	following ocations:			Enter "display memory MDS word from 1#D600 to 1#D605" The memory locations shall indicate as follows: (X = don't care)	
	A5.1 X355H or XB55H		0D600H				0D600H	X355H or XB55H	
	X4AAH or 0D601H XCAAH 0D602H 5555H 0D602H			0D601H				0D601H	X4AAH or XCAAH
			0D602H				0D602H	5555H	
	55XXH 0D603H						0D603H	55XXH	
	6AXXH XABFH, bit 12 = 0		0D604H				0D604H	6AXXH	
			0D605H				0D605H	XABFH, bit 12 = 0	
				Complem discrete i the UUT.	nent all of the nput stimuli to	Mode S Interface <u>Panel</u> Table 3		Complement all of the discrete input stimuli to the UUT by performing test setup in accordance with table 3.	
								Enter "display memory MDS word from 1#D600 to 1#D605"	
	Read th memory		Read the memory I	following ocations:			The memory locations shall indicate as follows: (X = don't care)		
								<u> </u>	
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REV	TEST		SF	PECIFICATION			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS
	A5.2	X4AAH or XCAAH X355H or XB55H AAAAH AAXXH 95XXH X744H, bit 12 = 1		0D600H 0D601H 0D602H 0D603H 0D603H 0D605H			OD600H 0D601H 0D602H 0D603H 0D604H 0D605H	X4AAH or XCAAH X355H or XB55H AAAAH AAXXH 95XXH X744H, bit 12 = 1
	A5.3	X5XXH		While pressing the Push To Test button on the front panel of the UUT, read memory location D605H. The value shall be as indicated. (X = don't care)			While pressing the Push To Test button on the front panel of the UUT, enter "display memory MDS word from 1#D605 to 1#D605" The value shall be as indicated. (X = don't care)	Х5ХХН
A6 OUTPU				OUTPUT DISCRETES			OUTPUT DISCRETES	
Perform accorda				Perform test setup in accordance with table 2.			Perform test setup in accordance with table 2.	
				Write to Output Data Word 0 and Configuration Register 3 as shown:			Write to Output Data Word 0 and Configuration Register 3 as shown: (Enter "modify memory MDS word <address> <data>")</data></address>	
				Address Data 0D607H 0012H 0D400H 2A00H			AddressData1#D60700121#D4002A00	
				Verify the state of the following UUT front panel LEDs:			Verify the state of the following UUT front panel LEDs:	
				Signal Name			Signal Name	
	A6.1	on off off on off on		XPDRPASS XPDRFAIL CNTLPNL TOPANT BOTANT ALTSIG			XPDRPASS XPDRFAIL CNTLPNL TOPANT BOTANT ALTSIG	on off off on off on
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REV	TEST		SF	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
				Verify the signals. shall be a	e following The signal sense as specified.			Verify the following signals. The indicator lamps on the Mode S interface panel (or the signals themselves) shall be as specified.	
		<u>Sig/Lamp</u>		<u>Sig/Lamp</u>	<u>)</u>			<u>Sig/Lamp</u>	<u>Sig/Lamp</u>
	A6.2 OPN/ON (Green) GND/OFF OPN/OFF GND/ON (Green) ALT FAIL XPDR F, XPDR F, XPDR F,		ALT FAIL ALT FAIL XPDR FA XPDR FA	. 1 . 2 NIL 1 NIL 2			ALT FAIL 1 ALT FAIL 2 XPDR FAIL 1 XPDR FAIL 2	OPN/ON (Green) GND/OFF OPN/OFF GND/ON (Green)	
	Write to Word 0 a Register			Dutput Data nd Configuration 3 as shown:			Write to Output Data Word 0 and Configuration Register 3 as shown: (Enter "modify memory MDS word <address> <data>")</data></address>		
				<u>Address</u> 0D607H 0D400H	<u>Data</u> 0001H 1500H			AddressData1#D60700011#D4001500	
				Verify the following LEDs:	e state of the UUT front panel			Verify the state of the following UUT front panel LEDs:	
	Signal N		<u>Signal Na</u>	ame			<u>Signal Name</u>		
			_ =		AW/CRITICAL	NOTATION			Ι
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REV	TEST		S	PECIFICATIO	N		-	PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS
	A6.3	off on off on off		XPDRPA XPDRFA CNTLPN TOPANT BOTANT ALTSIG Verify the signals. shall be a	SS IL L e following The signal sense as specified.			XPDRPASS XPDRFAIL CNTLPNL TOPANT BOTANT ALTSIG Verify the following signals. The indicator lamps on the Mode S interface panel (or the signals themselves) shall be as specified.	off on off on off
		<u>Sig/Lamp</u>		Signal Na	ame			Signal Name	<u>Sig/Lamp</u>
	A6.4	GND/OFF OPN/ON(Red) +5V/ON(Red) OPN/OFF	ND/OFF PN/ON(Red) V/ON(Red) N/OFF ALT FAI ALT FAI ALT FAI ALT FAI XPDR F		- 1 - 2 AIL 1 AIL 2			ALT FAIL 1 ALT FAIL 2 XPDR FAIL 1 XPDR FAIL 2	GND/OFF OPN/ON(Red) +5V/ON(Red) OPN/OFF
	A7			SYNCHR	<u>RO ALTITUDE</u>			SYNCHRO ALTITUDE	
				Perform f accordan	test setup in ice with table 2.			Perform test setup in accordance with table 2.	
				Stimulate SYNCHR SYNCHR with a 26 sinusoida Stimulate synchro a with a se sinusoid synchron reference Apply this the follow inputs wi side of th connecte	e the UUT RO REF H and RO REF C signals V ac, 400 Hz al waveform. e the UUT altitude inputs cond 400 Hz that is rous with the e signal (above). s signal across vng UUT synchro th the negative re signal ed to the Z input:	Mode S Interface <u>Panel</u> 26 VAC POL = +		On the Mode S Interface Panel, connect the 26 V ac REF + and – signals to the Sync Alt 1/2 RH and RC signals. Connect the TEST VAC + signal to the FX, FY, CX, and CY signals. Connect the TEST VAC – signal to the FZ and CZ signals. Adjust the amplitude of the TEST VAC signal to 9 V rms.	
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS
				SYNC AL SYNC AL SYNC AL SYNC AL SYNC AL SYNC AL SYNC AL SYNC AL	T 1 FINE X-Z T 1 FINE Y-Z T 1 CRSE X-Z T 1 CRSE Y-Z T 2 FINE X-Z T 2 FINE Y-Z T 2 CRSE X-Z T 2 CRSE Y-Z				
				1. Adjust this sig	the amplitude of gnal to 9 V rms.				
	Read the each syn using the procedur		Read the each syn using the procedur	digital value for chro source, following e:			Read the digital value for each synchro source, using the following procedure:		
				 Write t data (s table) locatio Wait a Read I Record 	the appropriate see following to memory on D607H. t least 75 µs. ocation D606H. d the value.			 Write the appropriate data (see following table) to memory location D607H. Enter "modify memory MDS word 1#D607 <value from="" table="">"</value> Enter "display memory MDS word from 1#D606 to 1#D606" Record the value. 	
				The data the range Limits. Write To D607H	should fall within shown in the Source Being Tested			The data should fall within the range shown in the Limits. Write Source To Being D607H Tested	
				<u>D607H</u>	lested			D607H <u>Tested</u>	
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REV TEST						PROCEDURE	
REV TEST LTR NO. A7.1	OPR LIMITS CD5 ± 6FH H H CD5 ± 6FH H CD5 ± 6FH H	SPECIFICAcTI(I)0100H(I)(I)(I)0120H(I)(I)(I)0120H(I)(I)(I)0140H(I)(I)(I)0160H(I)(I)(I)0180H(I)(I)(I)0180H(I)(I)(I)0120H(I)(I)(I)0120H(I)	ALT 1 FIN X-Z per 12 bits) ALT 1 FIN X-Z per 12 bits) ALT 1 FIN Y-Z per 12 bits) ALT 1 CRS X-Z per 12 bits) ALT 1 CRS X-Z per 12 bits) ALT 1 CRS Y-Z per 12 bits) ALT 2 FIN X-Z per 12 bits) ALT 2 FIN X-Z per 12 bits) ALT 2 FIN Y-Z per 12 bits) ALT 2 CRS X-Z per 12 bits) ALT 2 CRS X-Z per 12 bits) ALT 2 CRS Y-Z per Y-X ALT 2 CRS Y-Z PER Y-X ALT 2 CRS Y-X	Mode S Interface Panel 26 VAC POL = -		PROCEDURE WORK STEPS 0100 ALT 1 FIN X-Z (upper 12 bits) (lower 4 bits) 0120 ALT 1 FIN Y-Z (upper 12 bits) (lower 4 bits) 0140 ALT 1 CRS X-Z (upper 12 bits) (lower 4 bits) 0160 ALT 1 CRS Y-Z (upper 12 bits) (lower 4 bits) 0180 ALT 2 FIN X-Z (upper 12 bits) (lower 4 bits) 01A0 ALT 2 FIN Y-Z (upper 12 bits) (lower 4 bits) 01A0 ALT 2 CRS X-Z (upper 12 bits) (lower 4 bits) 01C0 ALT 2 CRS X-Z (upper 12 bits) (lower 4 bits) 01E0 ALT 2 CRS Y-Z (upper 12 bits) (lower 4 bits) Reverse the phase 180 degrees on the SYNCHRO REFERENCE input to the UUT. Read the digital value for each synchro source, using the following procedure: 1. Write the appropriate data (see following table) to memory location D607H. 2. Enter "modify memory MDS word 1#D607 <value from="" table="">" 3. Enter "display memory MDS word 1#D607 <value from="" table="">" 3. Enter "display memory MDS word 1#D607 <value from="" table="">"</value></value></value>	SPECIFICATIONMFG LIMITSCD5 \pm 6FHCD5 \pm 6FH2HCD5 \pm 6FH3HCD5 \pm 6FHCD5 \pm 6FHCD5 \pm 6FHCD5 \pm 6FHCD5 \pm 6FHCD5 \pm 6FHCD5 \pm 6FHCH <td< th=""></td<>
			AW/CRITICAL			4. Record the value.	
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REV TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
A7.2			The data the range Limits. Write To <u>D607H</u> 0100H	should fall within shown in the Source Being <u>Tested</u> ALT 1 FIN X-Z			The data should fall within the range shown in the Limits.WriteSource ToToBeingD607HTested0100ALT 1 FIN X-Z	
	329 ± 6FH OH 329 ± 6FH 329 ± 6FH 329 ± 6FH 3H 329 ± 6FH 329 ± 6FH 6H 329 ± 6FH 6H 329 ± 6FH 7H		(uppe (lower 0120H (uppe (lower 0140H (uppe (lower 0160H (uppe (lower 01AOH (uppe (lower 01COH (uppe (lower 01EOH (uppe (lower	r 12 bits) r 4 bits) ALT 1 FIN Y-Z r 12 bits) r 4 bits) ALT 1 CRS X-Z r 12 bits) r 4 bits) ALT 1 CRS Y-Z r 12 bits) r 4 bits) ALT 2 FIN X-Z r 12 bits) r 4 bits) ALT 2 FIN Y-Z r 12 bits) r 4 bits) ALT 2 CRS X-Z r 12 bits) r 4 bits) ALT 2 CRS Y-Z r 12 bits) r 4 bits) r 5 Y-Z r 12 bits) r 4 bits) r 7 Y-Z r 12 bits)			(upper 12 bits) (lower 4 bits) 0120 ALT 1 FIN Y-Z (upper 12 bits) (lower 4 bits) 0140 ALT 1 CRS X-Z (upper 12 bits) (lower 4 bits) 0160 ALT 1 CRS Y-Z (upper 12 bits) (lower 4 bits) 0180 ALT 2 FIN X-Z (upper 12 bits) (lower 4 bits) 01C0 ALT 2 CRS X-Z (upper 12 bits) (lower 4 bits) 01E0 ALT 2 CRS Y-Z (upper 12 bits) (lower 4 bits) 01E0 ALT 2 CRS Y-Z (upper 12 bits) (lower 4 bits)	329 ± 6FH OH 1H 329 ± 6FH 2H 329 ± 6FH 3H 329 ± 6FH 329 ± 6FH 6H 329 ± 6FH 6H 329 ± 6FH 7H
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS	
	A8			429 REC (INTERN Internal v 0 thru 7. Perform t	EIVER TESTS AL) vrap on receiver est setup in			429 RECEIVER TESTS (INTERNAL) Perform test setup in		
				accordan (Accept A masking)	ce with table 2.			accordance with table 2. (Accept AAH label with no masking)		
	A8.1	N/A		Perform i accordan	nitial setup in ce with table 6.			Perform initial setup in accordance with table 6.	N/A	
				Modify M Address OD210H OD211H	emory: <u>Data</u> AAAAH AAAAH			Enter "modify memory MDS word 1#D210 thru 1#D211 to AAAA"		
				Read the of memor D20FH.	following block ry: D200H thru			Enter "display memory MDS word from 1#D200 to 1#D20F"		
	A8.2	AAAAH 2AAAH		Even loca Odd loca	ations tions			Even locations Odd locations	AAAAH 2AAAH	
				(Reject e no label c	ven parity data, qualification)			(Reject even parity data, no label qualification)		
	A8.3	N/A Perform table 6 d <u>Address</u> D21CH D220H D22EH			nitial setup per kcept: <u>Data</u> 0249H 0044H			Perform initial setup per table 6 except: <u>Address Data</u> 1#D21C 0249 1#D220 - 1#D22E 0044	N/A	
				Modify M <u>Address</u> D210H D211H	emory: <u>Data</u> 5555H 5555H			Enter "modify memory MDS word 1#D210 thru 1#D211 to 5555"		
				Read the of memor D20FH.	following block ry: D200H thru			Enter "display memory MDS word from 1#D200 to 1#D20F"		
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REV	TEST		SPECIFICATIO)N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	C TEST	T DESCRIPTION	SWITCH P	OS	C WORK STEPS	MFG LIMITS
	A8.4	AAAAH 2AAAH	Even loc Odd loca (Reject n labels)	ations itions ionmatching			Even locations Odd locations (Reject nonmatching labels)	AAAAH 2AAAH (no change from test 8.2)
	A8.5	N/A	Perform accordar except: <u>Address</u> D220H - D22EH Modify M <u>Address</u> D210H D211H Read the of memo D20FH.	initial setup in fice with table 6 <u>Data</u> EF44H lemory: <u>Data</u> 5555H 5555H sfollowing block ry: D200H thru			Perform initial setup in accordance with table 6 except: <u>Address</u> <u>Data</u> 1#D220 - 1#D22E EF44 Enter "modify memory MDS word 1#D210 thru 1#D210 to 5555" Enter "display memory MDS word from 1#D200 to 1#D20F"	N/A
	A8.6	AAAAH 2AAAH (no change from test 8.2)	Even loc Odd loca (Reject d feature)	ations itions lue to disable			Even locations Odd locations (Reject due to disable feature)	AAAAH 2AAAH (no change from test 8.2)
	A8.7	N/A	Perform accordar except: <u>Address</u> D218H D21AH D220H - D22EH Modify M <u>Address</u> D210H D211H Read the of memo D20FH.	initial setup in face with table 6 Data 0000H EE44H lemory: Data 5555H 5555H s5555H e following block ry: D200H thru			Perform initial setup in accordance with table 6 except: <u>Address Data</u> 1#D218 0000 1#D21A 0000 1#D220 - 1#D22E EE44 Enter "modify memory MDS word 1#D210 thru 1#D211 to 5555" Enter "display memory MDS word from 1#D200 to 1#D20F"	N/A
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REV	TEST		SF	PECIFICATIO	N			PROCEDURE	SPECIFICATION			
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS			
	A8.8	AAAAH 2AAAH (no change)		Even loca Odd loca (Accept v	ations tions ralid data and			Even locations Odd locations (Accept valid data and	AAAAH 2AAAH (no change)			
	A8.9	N/A		Perform i accordan except: <u>Address</u> D220H - D22EH Modify M <u>Address</u> D210H D211H Read the of memor	nitial setup in ce with table 6 <u>Data</u> EE44H emory: <u>Data</u> 5555H 5555H 5555H			Iabel)Perform initial setup in accordance with table 6 except:AddressData 1#D220 - 1#D22E1#D22EEE44Enter "modify memory MDS word 1#D210 thru 1#D211 to 5555"Enter "display memory MDS word from 1#D200	N/A			
	A8.10	5555H D555H		D20FH. Even loca Odd loca	ations tions			to 1#D20F" Even locations Odd locations	5555H D555H			
	A8.11	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the	nitial setup in ce with table 6.1. emory: <u>Data</u> 8000H 0000H following block			Perform initial setup in accordance with table 6.1. Enter "modify memory MDS word 1#D210H to 8000H" Enter "modify memory MDS word 1#D211H to 0000H" Enter "display memory MDS word from 1#D200H	N/A			
	A8.12	8000H 0000H		OD20FH. Even loca Odd loca	ations tions			thru 1#D20FH" Even locations Odd locations	8000H 0000H			
					AW/CRITICAL	IOTATION						
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REV	TEST		SF	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS	
	A8.13	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H	nitial setup in ce with table 6.1. emory <u>Data</u> 4000H 0000H			Perform initial setup in accordance with table 6.1. Enter "modify memory MDS word 1#D210H to 4000H" Enter "modify memory MDS word 1#D211H to 0000H"	N/A	
	A8.14	4000H		of memor 0D20FH. Even loca	ry: 0D200H thru			MDS word from 1#D200H thru 1#D20FH" Even locations	4000H	
		0000H		Odd loca	tions			Odd locations	0000H	
	A8.15	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 2000H 0000H			Enter "modify memory MDS word 1#D210H to 2000H" Enter "modify memory MDS word 1#D211H to 0000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"		
	A8.16	2000H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	2000H 0000H	
	A8.17	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify m <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 1000H 0000H			Enter "modify memory MDS word 1#D210H to 1000H" Enter "modify memory MDS word 1#D211H to 0000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"		
	A8.18	1000H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	1000Н 0000Н	
			<u>. </u>		AW/CRITICAL I	NOTATION	<u> </u>			
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	с	WORK STEPS	MFG LIMITS		
	A8.19	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H	nitial setup in ce with table 6.1. emory: <u>Data</u> 0800H 0000H			Perform initial setup in accordance with table 6.1. Enter "modify memory MDS word 1#D210H to 0800H" Enter "modify memory MDS word 1#D211H to 0000H"	N/A		
	A8.20	0800H		Read the of memor 0D20FH. Even loca	following block ry: 0D200H thru ations			Enter "display memory MDS word from 1#D200H thru 1#D20FH" Even locations	0800H		
		0000H		Odd loca	tions			Odd locations	0000H		
	A8.21	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A		
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0400H 0000H			Enter "modify memory MDS word 1#D210H to 0400H" Enter "modify memory MDS word 1#D211H to 0000H"			
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"			
	A8.22	0400H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0400H 0000H		
	A8.23	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A		
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0200H 0000H			Enter "modify memory MDS word 1#D210H to 0200H" Enter "modify memory MDS word 1#D211H to 0000H"			
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH			
	A8.24	0200H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0200H 0000H		
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REV	TEST		s	PECIFICATIO	Ν			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	A8.25	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H	nitial setup in ce with table 6.1. emory: <u>Data</u> 0100H 0000H			Perform initial setup in accordance with table 6.1. Enter "modify memory MDS word 1#D210H to 0100H" Enter "modify memory MDS word 1#D211H to 0000H"	N/A	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"		
	A8.26	0100H 0000H		Even loca Odd loca	ations tions			Odd locations	0100H 0000H	
	A8.27	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0080H 0000H			Enter "modify memory MDS word 1#D210H to 0080H" Enter "modify memory MDS word 1#D211H to 0000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"		
	A8.28	0080H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0080H 0000H	
	A8.29	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A	
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0040H 0000H			Enter "modify memory MDS word 1#D210H to 0040H" Enter "modify memory MDS word 1#D211H to 0000H"		
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"		
	A8.30	0040H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0040H 0000H	
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REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	A8.31	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the of memor	nitial setup in ce with table 6.1. emory: <u>Data</u> 0020H 0000H following block ry: 0D200H thru			Perform initial setup in accordance with table 6.1. Enter "modify memory MDS word 1#D210H to 0020H" Enter "modify memory MDS word 1#D211H to 0000H" Enter "display memory MDS word from 1#D200H	N/A	
	A8.32	0020H 0000H		0D20FH. Even loca Odd loca	ations tions			thru 1#D20FH" Even locations Odd locations	0020H 0000H	
	A8.33 A8.34	N/A 0010H 0000H		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the of memor 0D20FH. Even loca Odd loca	nitial setup in ce with table 6.1. emory: <u>Data</u> 0010H 0000H following block ry: 0D200H thru ations			Perform initial setup in accordance with table 6.1. Enter "modify memory MDS word 1#D210H to 0010H" Enter "modify memory MDS word 1#D211H to 0000H" Enter "display memory MDS word from 1#D200H thru 1#D20FH" Even locations Odd locations	N/A 0010H 0000H	
					AW/CRITICAL	NOTATION				
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REV	TEST		SF	PECIFICATIO	N			PROCEDURE	SPECIFICATION		
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS		
	A8.35	N/A		Perform i accordan Modify M <u>Address</u> 0D210H 0D211H Read the	nitial setup in ce with table 6.1. emory: <u>Data</u> 0008H 0000H			Perform initial setup in accordance with table 6.1. Enter "modify memory MDS word 1#D210H to 0008H" Enter "modify memory MDS word 1#D211H to 0000H" Enter "display memory	N/A		
	A8.36	0008H 0000H		OD20FH. Even loca	ations			thru 1#D20FH" Even locations Odd locations	0008H 0000H		
	A8.37	N/A		Perform i accordan Modify M Address	nitial setup in ce with table 6.1. emory: Data			Perform initial setup in accordance with table 6.1. Enter "modify memory MDS word 1#D210H to	N/A		
				Read the oD20FH.	following block			0004H" Enter "modify memory MDS word 1#D211H to 0000H" Enter "display memory MDS word from 1#D200H thru 1#D20FH"			
	A8.38	0004H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0004H 0000H		
	A8.39	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A		
				Modify M Address 0D210H 0D211H	emory: <u>Data</u> 0002H 0000H			Enter "modify memory MDS word 1#D210H to 0002H" Enter "modify memory MDS word 1#D211H to 0000H"			
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"			
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	C	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS
	A8.40	0002H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0002H 0000H
	A8.41	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0001H 0000H			Enter "modify memory MDS word 1#D210H to 0001H" Enter "modify memory MDS word 1#D211H to 0000H"	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.42	0001H 0000H		Even loca Odd loca	ations tions			Even locations Odd locations	0001H 0000H
	A8.43 N/A Perform accordar				nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
	Modify M <u>Address</u> 0D210H 0D211H			Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 8000H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 8000H"	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.44	0000H 8000H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 8000H
	A8.45	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
	Modify M <u>Address</u> 0D210H 0D211H			emory: <u>Data</u> 0000H 4000H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 4000H"		
	Read the following block of memory: 0D200H thre 0D20FH.				following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
					AW/CRITICAL I		-		
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	с	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	A8.46	0000H 4000H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 4000H
	A8.47	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 2000H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 2000H"	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.48	0000H 2000H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 2000H
	A8.49	N/A Perform			nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 1000H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 1000H"	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.50	0000H 1000H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 1000H
	A8.51	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
		Modify M <u>Address</u> 0D210H 0D211H			emory: <u>Data</u> 0000H 0800H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0800H"	
	Read the following blue of memory: 0D200H t 0D20FH.				following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
		1				NOTATION			1
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS
	A8.52	0000H 0800H		Even loca Odd locat	ations tions			Even locations Odd locations	0000H 0800H
	A8.53	N/A		Perform in accordant	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify Mo <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0400H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0400H"	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.54	0000H 0400H		Even loca Odd locat	ations tions			Even locations Odd locations	0000H 0400H
	A8.55	N/A		Perform in accordant	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify Mo <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0200H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0200H"	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.56	0000H 0020H		Even loca Odd locat	ations tions			Even locations Odd locations	0000H 0200H
	A8.57	N/A		Perform in accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify Mo <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0100H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0100H"	
Read the fo of memory: 0D20FH.					following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
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REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	с	WORK STEPS	MFG LIMITS
	A8.58	0000H 0100H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0100H
	A8.59	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0080H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0080H"	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.60	0000H 0080H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0080H
	A8.61	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0040H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0040H"	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.62	0000H 0040H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0040H
	A8.63	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
	Modify M <u>Address</u> 0D210H 0D211H			Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0020H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0020H"	
	Read the f of memory 0D20FH.				following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	_		<u>.</u>		AW/CRITICAL N	IOTATION			
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REV	TEST	TEST SPECIFICATIO			N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	A8.64	0000H 0020H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0020H
	A8.65	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0010H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0010H"	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.66	0000H 0010H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0010H
	A8.67	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0008H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0008H"	
				Read the of memor 0D20FH.	following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.68	0000H 0008H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0008H
	A8.69	N/A		Perform i accordan	nitial setup in ce with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
	Modify M <u>Address</u> 0D210H 0D211H			Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0004H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0004H"	
Read the of memory 0D20FH.					following block y: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	A8.70	0000H 0004H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0004H
	A8.71	N/A		Perform i accordan	nitial setup in ice with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0002H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0002H"	
				Read the of memor 0D20FH.	following block ry: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.72	0000H 0002H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0002H
	A8.73	N/A		Perform i accordan	nitial setup in ice with table 6.1.			Perform initial setup in accordance with table 6.1.	N/A
				Modify M <u>Address</u> 0D210H 0D211H	emory: <u>Data</u> 0000H 0001H			Enter "modify memory MDS word 1#D210H to 0000H" Enter "modify memory MDS word 1#D211H to 0001H"	
				Read the of memor 0D20FH.	following block ry: 0D200H thru			Enter "display memory MDS word from 1#D200H thru 1#D20FH"	
	A8.74	0000H 0001H		Even loca Odd loca	ations tions			Even locations Odd locations	0000H 0001H
	A9			429 REC TRANSM (EXTERM	EIVER/ IITTER TESTS <u>IAL)</u>			429 RECEIVER/ TRANSMITTER TESTS <u>(EXTERNAL)</u>	
				Perform f accordan and 8.	test setup in Ice with tables 2			Perform test setup in accordance with tables 2 and 8.	
				<u>Xmtr 0/R</u>	<u>cvr 0</u>			<u>Xmtr 0/Rcvr 0</u>	
					AW/CRITICAL I				
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					SEE THE	TITLE PAGE FOR	PRO	PRIETARY AND DATA RIGHTS NOTA	ATIONS.
REV	REV TEST SPECIFICATIO				Ν	SPECIFICATION			
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
REV	TEST NO. A9.1	OPR LIMITS N/A	SPE С (([ECIFICATIO TEST (MAINT E DATA 1) Perform i accordan Modify re Address D220H Modify tra Address D210H D211H Data sho follows: Address	SEE THE DESCRIPTION DATA OUT/AIR nitial setup in ce with table 7. ceiver label: Data FFAAH ansmitter data: Data AAAAH AAAAH AAAAH Uld read as	SWITCH POS		PROCEDURE WORK STEPS (MAINT DATA OUT/AIR DATA 1) Connect Xmtr 0 (Maint Data Out) to Rcvr 0 (429 Air Data 1). Perform initial setup in accordance with table 7. Enter "modify memory MDS word 1#0D220 FFAA" Enter "modify memory MDS word 1#D210 thru 1#D211 to AAAA" Enter "display memory MDS word from 1#D200 to 1#D201" Data should read as follows: <u>Address</u>	ATIONS. SPECIFICATION MFG LIMITS N/A
					AW/CRITICAL 1	NOTATION			
	H	oneywe	ell		SECURITY NO	DTATION		SUPPLEMENTS	A-30 PAGE

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					SEE THE TITLE PAGE FOR PROPRIETARY AND DATA RIGHTS NOTATIONS.					
REV	TEST		s	PECIFICATIO)N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	T DESCRIPTION	SWITCH POS	с	WORK STEPS	MFG LIMITS	
	A9.2	OPR LIMITS AAAAH 2AAAH 5555H D555H		TEST D200H D201H On Mode panel, re connection #1 and re ADC #1. Modify re Address D220H Modify tr Address D210H D211H Data sho follows: Address D200H D201H Xmtr 0/R	e S interface move ons on 429 ADC econnect on 575 eceiver label: <u>Data</u> FF55H ansmitter data: <u>Data</u> 5555H 5555H	SWITCH POS		WORK STEPS D200H D201H On Mode S interface panel, remove connections on 429 ADC #1 and reconnect on 575 ADC #1. Enter "modify memory MDS word 1#0D220 FF55" Enter "modify memory MDS word 1#0D210 FF55" Enter "display memory MDS word from 1#D200 to 1#D201" Data should read as follows: Address D200H D201H Xmtr 0/Rcvr 1 Xmtr 0/Rcvr 1	MFG LIMITS AAAAH 2AAAH 5555H D555H D555H	
					AW/CRITICAL N				1	
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SECURITY NOTATION	SPEC NO.	IT4061400-907	SEE PAGE INDEX FOR THIS SHEET REV LETTER

SEE THE TITLE						TLE PAGE FOR PROPRIETARY AND DATA RIGHTS NOTATIONS.			
REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS
	TEST NO. A9.4	OPR LIMITS N/A AAAAH 2AAAH	SI	PECIFICATIO TEST (MAINT I DATA 2) Perform i accordan Modify re <u>Address</u> D222H Modify tra <u>Address</u> D210H D211H D211H D211H D212H D211H D211H D211H D212H D210H D211H D211H	SEE THE DESCRIPTION DATA OUT/AIR initial setup in ince with table 7. ceiver label: Data FFAAH ansmitter data: Data AAAAH AAAAH AAAAH ansmitter data: Data AAAAH AAAAAH AAAAAH AAAAH AAAAH AAAAH AAAAH AAAAH AAAAAH AAAAH AAAAA	TITLE PAGE FOR SWITCH POS		PRIETARY AND DATA RIGHTS NOTA PROCEDURE WORK STEPS (MAINT DATA OUT/AIR DATA 2) Perform initial setup in accordance with table 7. Enter "modify memory MDS word 1#0D222 FFAAH" Enter "modify memory MDS word 1#D210 thru 1#D211 to AAAA" Enter "display memory MDS word from 1#D202 to 1#D203" Data should read as follows: <u>Address</u> D202H D202H D203H On Mode S interface panel, remove connections on 429 ADC #2 and reconnect on 575	AAAAH 2AAAH
				On Mode panel, re connectio #2 and re ADC #2. Modify re <u>Address</u> D222H Modify tra <u>Address</u> D210H D211H	e S interface move ons on 429 ADC econnect on 575 eceiver label: <u>Data</u> FF55H ansmitter data: <u>Data</u> 5555H 5555H			On Mode S interface panel, remove connections on 429 ADC #2 and reconnect on 575 ADC #2. Enter "modify memory MDS word 1#0D222 FF55H" Enter "modify memory MDS word 1#D210 thru 1#D211 to 5555" Enter "display memory MDS word from 1#D202	
				Data sho follows: <u>Address</u>	uld read as			to 1#D203" Data should read as follows: <u>Address</u>	
					AW/CRITICAL N				
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REV	TEST		SPECIF	IFICATION			-	PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	C	TEST	DESCRIPTION	SWITCH POS	c	WORK STEPS	MFG LIMITS
	A9.6	5555H D555H	D20 D20	202H 203H				D202H D203H	5555H D555H
			<u>×m</u>		<u>VI Z</u>			XIIIII U/RCVI Z	
	A9.7	N/A	(MA DA	AINT D ATA B)	ATA OUT/CNTL			(MAINT DATA OUT/CNTL DATA B)	N/A
			Per acc	erform ir cordanc	nitial setup in e with table 7.			Perform initial setup in accordance with table 7.	
			Moo <u>Ado</u> D22	odify rec I <u>dress</u> 224H	eiver label: <u>Data</u> FFAAH			Enter "modify memory MDS word 1#0D224 FFAAH"	
			Moo <u>Ado</u> D2 ² D2 ²	odify tra I <u>dress</u> 210H 211H	nsmitter data: <u>Data</u> AAAAH AAAAH			Enter "modify memory MDS word 1#D210 thru 1#D211 to AAAA"	
								Enter "display memory MDS word from 1#D204 to 1#D205"	
Data she follows:			ata shou lows:	ld read as			Data should read as follows:		
	Address			ldress				<u>Address</u>	
	A9.8	AAAAH 2AAAH	D20 D20	204H 205H				D204H D205H	AAAAH 2AAAH
			Moo <u>Ado</u> D22	odify rec I <u>dress</u> 224H	eiver label: <u>Data</u> FF55H			Enter "modify memory MDS word 1#0D224 FF55H"	
			Moo <u>Ado</u> D2 ² D2 ²	odify tra I <u>dress</u> 210H 211H	nsmitter data: <u>Data</u> 5555H 5555H			Enter "modify memory MDS word 1#D201 thru 1#D211 to 5555"	
Data sho follows:								Enter "display memory MDS word from 1#D204 to 1#D205"	
				ata shou lows:	ld read as			Data should read as follows:	
			<u>Ado</u>	ldress				Address	
					AW/CRITICAL N				
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SEE THE TITLE PAGE FOR PROPRIETARY AND DATA RIGHTS NOTA							TIONS.		
REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS
	A9.9	5555H D555H		D204H D205H				D204H D205H	5555H D555H
				Xmtr 0/Ro	<u>cvr 3</u>			Xmtr 0/Rcvr 3	
	A9.10	N/A		(MAINT E OUT/MAI	DATA NT DATA IN)			(MAINT DATA OUT/MAINT DATA IN)	N/A
				Perform i accordan	nitial setup in ce with table 7.			Perform initial setup in accordance with table 7.	
				Modify re <u>Address</u> D226H	ceiver label: <u>Data</u> FFAAH			Enter "modify memory MDS word 1#0D226 FFAA"	
				Modify tra <u>Address</u> D210H D211H	ansmitter data: <u>Data</u> AAAAH AAAAH			Enter "modify memory MDS word 1#D210 thru 1#D211 to AAAA"	
								Enter "display memory MDS word from 1#D206 to 1#D207"	
				Data sho follows:	uld read as			Data should read as follows:	
				<u>Address</u>				<u>Address</u>	
	A9.11	ААААН 2АААН		D206H D207H				D206H D207H	AAAAH 2AAAH
				Modify re <u>Address</u> D226H	ceiver label: <u>Data</u> FF55H			Enter "modify memory MDS word 1#0D226 FF55"	
				Modify tra <u>Address</u> D210H D211H	ansmitter data: <u>Data</u> 5555H 5555H			Enter "modify memory MDS word 1#D210 thru 1#D211 to 5555"	
								Enter "display memory MDS word from 1#D206 to 1#D207"	
				Data sho follows:	uld read as			Data should read as follows:	
				Address				Address	
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REV <test< th="">SPECIFICATIONPROCEDURELTRNO.OPR LIMITSCTEST DESCRIPTIONSWITCH POSCWORK SA9.125555HD206HD207HD207HD207HD207HA9.13N/A(MAINT DATA OUT/FLIGHT ID)(MAINT DATA OUT/FLIGHT(MAINT DATA OUT/FLIGHT</test<>	A ID)
LTR NO. OPR LIMITS C TEST DESCRIPTION SWITCH POS C Work 3 A9.12 5555H D206H D207H D207H D207H D207H A9.13 N/A (MAINT DATA OUT/FLIGHT ID) (MAINT DATA OUT/FLIGHT (MAINT DATA	A N/A ID)
A9.125555H D555HD206H D207HD206H D207HA9.13N/A(MAINT DATA OUT/FLIGHT ID)(MAINT DATA OUT/FLIGHT	5555H D555H D555H ID) o in
A9.13 N/A (MAINT DATA OUT/FLIGHT ID) <u>Xmtr 0/Rcvr 6</u> (MAINT DATA OUT/FLIGHT ID)	A N/A ID) o in
A9.13 N/A (MAINT DATA (MAINT DATA OUT/FLIGHT ID) (MAINT DATA	A N/A ID) o in
	o in
Perform setup in accordance with table 7. Perform setup accordance with table 7.	
Modify receiver label: AddressEnter "modify MDS word 1# FFAAHD22CHFFAAH	memory 0D22C
Modify transmitter data:Enter "modifyAddressDataMDS word 1#D210HAAAAH1#D211 to AAD211HAAAAH1#D211 to AA	memory D210 thru AA"
Enter "display MDS word fro to 1#D20D"	memory m 1#D20C
Data should read as follows: Data should read as follows:	ead as
Address Address	
A9.14AAAAHD20CHD20CH2AAAHD20DHD20DH	AAAAH 2AAAH
Modify receiver label: AddressEnter "modifyAddressDataMDS word 1#D22CHFF55HFF55"	memory 0D22C
Modify transmitter data:Enter "modifyAddressDataMDS word 1#D210H5555H1#D211 to 55D211H5555H1#D211 to 55	memory D210 thru 55"
Enter "display MDS word fro to 1#D20D"	memory m 1#D20C
Data should read as Data should read as follows:	ead as
Address Address	
	T
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REV	TEST		SI	PECIFICATION	ESCRIPTION			PROCEDURE	SPECIFICATION
	A9.15	5555H D555H	U	D20CH D20DH	ESCRIPTION	SWICHPOS		D20CH D20DH	5555H D555H
				Xmtr 0/Rcv	<u>/r 7</u>			Xmtr 0/Rcvr 7	
	A9.16	N/A		(MAINT DA DATA A)	ATA OUT/CNTL			(MAINT DATA OUT/CNTL DATA A)	N/A
				Perform ini accordance	itial setup in e with table 7.			Perform initial setup in accordance with table 7.	
				Modify rece <u>Address</u> D22EH	eiver label: <u>Data</u> FFAAH			Enter "modify memory MDS word 1#0D22E FFAA"	
				Modify trar <u>Address</u> D210H D211H	nsmitter data: <u>Data</u> AAAAH AAAAH			Enter "modify memory MDS word 1#D210 thru 1#D211 to AAAA"	
								Enter "display memory MDS word from 1#D20E to 1#D20F"	
	Data sh follows: <u>Address</u>			Data shoul follows:	d read as			Data should read as follows:	
				<u>Address</u>				Address	
	A9.17	AAAAH 2AAAH		D20EH D20FH				D20EH D20FH	AAAAH 2AAAH
				Modify reco <u>Address</u> D22EH	eiver label: <u>Data</u> FF55H			Enter "modify memory MDS word 1#0D22E FF55"	
				Modify trar <u>Address</u> D210H D211H	nsmitter data: <u>Data</u> 5555H 5555H			Enter "modify memory MDS word 1#D210 thru 1#D211 to 5555"	
							Enter "display memory MDS word 1#D20E to 1#D20F"		
				Data shoul follows:	d read as			Data should read as follows:	
				Address				<u>Address</u>	
	▋▋	nnov a/			AW/ORITICAL I				
nuneyweii					SECURITY NOTATION			SUPPLEMENTS	A-36 PAGE

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REV	TEST		S	PECIFICATION				PROCEDURE	SPECIFICATION
	A9.18	5555H D555H		D20EH D20FH	JESCRIPTION	301101 103		D20EH D20FH	5555H D555H
				Xmtr 1/Rcv	<u>vr 4</u>			<u>Xmtr 1/Rcvr 4</u>	
	A9.19	N/A		(XT COOR	RD/TX COORD)			(XT COORD/TX COORD)	N/A
				Perform in accordance	itial setup in e with table 7.			Perform initial setup in accordance with table 7.	
				Modify rec <u>Address</u> D228H	eiver label: <u>Data</u> FFAAH			Enter "modify memory MDS word 1#0D228 FFAA"	
				Modify trar <u>Address</u> D212H D213H	nsmitter data: <u>Data</u> AAAAH AAAAH			Enter "modify memory MDS word 1#D212 thru 1#D213 to AAAA"	
								Enter "display memory MDS word from 1#D208 to 1#D209"	
	Data sho follows: <u>Address</u>			Data shoul follows:	ld read as			Data should read as follows:	
				<u>Address</u>				<u>Address</u>	
	A9.20	AAAAH 2AAAH		D208H D209H				D208H D209H	ААААН 2АААН
				Modify rec <u>Address</u> D228H	eiver label: <u>Data</u> FF55H			Enter "modify memory MDS word 1#0D228 FF55"	
				Modify trar <u>Address</u> D212H D213H	nsmitter data: <u>Data</u> 5555H 5555H			Enter "modify memory MDS word 1#D212 thru 1#D213 to 5555"	
	Data sho follows:							Enter "display memory MDS word from 1#D208 to 1#D209"	
				Data shoul follows:	ld read as			Data should read as follows:	
	Address			<u>Address</u>				Address	
	<u> </u>	1	<u>ı </u>	<u> </u>	AW/CRITICAL N	NOTATION		1	1
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CODE

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REV	TEST		s	PECIFICATION	DN			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST DESCRIP	TION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	A9.21	5555H D555H		D208H D209H Xmtr 2/Reyr 5				D208H D209H Xmtr 2/Boyr 5	5555H D555H	
	A9.22	N/A		(DATA LINK OUT LINK IN)	T/DATA			(DATA LINK OUT/DATA LINK IN)	N/A	
				Perform initial se accordance with	tup in table 7.			Perform initial setup in accordance with table 7.		
				Address D22AH FI	ata FAAH			MDS word 1#0D22A FFAA"		
				Modify transmitteAddressDaD214HAzD215HAz	er data: <u>ata</u> AAAH AAAH			Enter "modify memory MDS word 1#D214 thru 1#D215 to AAAA"		
								Enter "display memory MDS word from 1#D20A to 1#D20B"		
				Data should read follows:	las			Data should read as follows:		
				Address				<u>Address</u>		
	A9.23	AAAAH 2AAAH		D20AH D20BH				D20AH D20BH	AAAAH 2AAAH	
				Modify receiver la Address Da D22AH FI	abel: <u>ata</u> F55H			Enter "modify memory MDS from 1#0D22A FF55"		
				Modify transmitterAddressD2D214H55D215H55	er data: <u>ata</u> 555H 555H			Enter "modify memory MDS from 1#D214 thru 1#D215 to 5555"		
								Enter "display memory MDS word from 1#D20A to 1#D20B"		
				Data should read follows:	las			Data should read as follows:		
				<u>Address</u>				Address		
 	<u> </u>		<u> </u>	AW/	CRITICAL N	OTATION		<u> </u>	<u> </u>	
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REV	TEST		S	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
REV	теят NO. A9.24 A10	OPR LIMITS 5555H D555H	SC	PECIFICATIO TEST D20AH D20BH INTERNA SELF-TE Perform t accordan Set up vio according	AL RF <u>ST</u> est setup in ce with table 4. deo ASIC to table 4.	Mode S Interface <u>Panel</u> STBY/ON: ON		PROCEDURE WORK STEPS D20AH D20BH INTERNAL RF <u>SELF-TEST</u> Perform test setup in accordance with table 4. Enter "END" Enter "SCP MDS" Enter "load verify MDS IT100P.RA" Enter "modify registers MDS E0 80E" Enter "modify registers MDS P 109." Enter "Set discretes MDS to JEA "	SPECIFICATION MFG LIMITS 5555H D555H	
	A10.1	N/A		Short Mo <u>Bottom A</u> Initiate th writing to shown. <u>Address</u> OC02EH The recei data shal <u>Address</u>	de S <u>ntenna</u> e self-test by memory as <u>Data</u> 0031H wed interrogation I be as shown.			to IFA." Enter "run MDS" Program takes only a fraction of a second to run. When all six front panel LEDs are illuminated, program is complete. Short Mode S <u>Bottom Antenna</u> Enter "modify memory MDS word 1#0C02E 0031" Enter "display memory MDS word from 1#C000 to 1#C003" Data shall indicate as follows: <u>Address</u>	N/A	
					AW/CRITICAL I	ΝΟΤΑΤΙΟΝ				
	H	oneywe					0.01-		A-39 PAGE	

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					SEE THE	TITLE PAGE FOR	PRC	PRIETARY AND DATA RIGHTS NOTA	TIONS.
REV	TEST		SP	PECIFICATIO	N			PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS
	A10.2	5555H		0C000H t Long Mod Top Ante	thru 0C003H de S <u>nna</u>			0C000H thru OC003H Long Mode S <u>Top Antenna</u>	5555H
	A10.3	N/A		Initiate th writing to shown. <u>Address</u> 0C007H t 0C00DH 0C02EH The receind data shall	ived interrogation			Enter "modify memory MDS word 1#C007 thru 1#C00D to AAAA" Enter "modify memory MDS word 1#0C02E 0037" Enter "display memory MDS word from 1#C000 to 1#C007" Data shall indicate as follows:	N/A
				Audress				Address	
	A10.4	AAAAH		0C000H 1	thru 0C006H			0C000H thru 0C006H	AAAAH
				Mode S L Top Ante	₋ong Squitter nna Pwr Vld			Mode S Long Squitter Top Antenna Pwr Vld	
	A10.5	N/A		Set up re writing to shown. (pulses or <u>Address</u> 0C007H 0C008H to 0C00DH Initiate th writing to shown. <u>Address</u> 0C02EH	ply data by memory as Send preamble hy.) <u>Data</u> F000H thru 0000H re self-test by memory as <u>Data</u> 003FH	Mode S Interface <u>Panel</u> STBY/ON: ON		Enter "modify memory MDS word 1#C007 F000" Enter "modify memory MDS word 1#C008 thru 1#C00D to 0000" Enter "modify memory MDS word 1#0C02E 003F"	N/A
					AW/CRITICAL I	NOTATION	<u> </u>		
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					SEE THE	EE THE TITLE PAGE FOR PROPRIETARY AND DATA RIGHTS NOTATIONS.				
REV	TEST		s	PECIFICATIO	ON			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
	A10.6	XXX8H bits 4 and 5 = 1		Observe to power val reading th status wo 0C011H. be as spe	the top antenna lid signal by ne self-test rd, address This word shall ecified.			Enter "display memory MDS word from 1#C011 to 1#C011" Location shall be as specified.	XXX8H bits 4 and 5 = 1	
				(X = don'i	t care)			(X = don't care)		
				Mode S L <u>Bot Anter</u>	ong Squitter Ina Pwr Vld			Mode S Long Squitter Bot Antenna Pwr VId		
	A10.7	N/A		Initiate th writing to shown. <u>Address</u> 0C02EH	e self-test by memory as <u>Data</u> 003DH	Mode S Interface <u>Panel</u> STBY/ON: ON		Enter "modify memory MDS word 1#C02E 003D"	N/A	
	A10.8	XXXX4H bits 4 and 5 = 1		Observe f antenna p signal by test statu: 0C011H. be as spe	the bottom bower valid reading the self- s word, address This word shall ecified.			Enter "display memory MDS word from 1#C011 to 1#C011" This location shall be as specified.	XXXX4H bits 4 and 5 = 1	
				(X = don'i	t care)			(X = don't care)		
				ATCRBS Top Anter	Mode A nna			ATCRBS Mode A Top Antenna		
	A10.9	N/A		Set up int for Mode <u>Address</u> 0C007H 0C008H 0C009H 0C00AH 0C00BH 0C00CH 0C00DH Initiate th writing to shown. <u>Address</u> 0C02EH	errogation data A. <u>Data</u> E000H 0000H E000H 0000H 0000H 0000H 0000H 0000H 0000H			Modify memory according to the following table: (Enter "modify memory <address> to <data>") <u>Address</u> <u>Data</u> 1#C007 E000 1#C008 0000 1#C009 E000 1#C00B 0000 1#C00B 0000 1#C00C 0000 1#C00D 0000 Enter "modify memory MDS word 1#0C02E 0036"</data></address>	N/A	
		L	1		AW/CRITICAL	NOTATION			1	
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|--|

SPEC NO.

CAGE

CODE

					SEE THE	SEE THE TITLE PAGE FOR PROPRIETARY AND DATA RIGHTS NOTATIONS.				
REV	TEST		s	PECIFICATIO	N			PROCEDURE	SPECIFICATION	
LTR	NO.	OPR LIMITS	С	TEST	DESCRIPTION	SWITCH POS	С	WORK STEPS	MFG LIMITS	
н	A10 .10	ХХХ2Н		Observe t signal by test status 0C011H. as specifi (X = don't ATCRBS Bot Anter	the Mode A reading the self- s word, address Word shall be ed. t care) Mode C	IFR 1400 CW/NORM/ OFF: OFF		Turn IFR 1400 CW/NORM/OFF Switch to "OFF" position. Enter "display memory MDS word from 1#C011 to 1#C011" This location shall be as specified. (X = don't care) ATCRBS Mode C Bot Antenna	XXX2H	
	A10 .11	N/A		Set up int for Mode <u>Address</u> 0C007H 0C008H 0C009H 0C00AH 0C00BH 0C00CH 0C00DH Initiate th writing to shown.	errogation data C. <u>Data</u> E000H 0000H 0000H 0000H 0000H 0E00H 0000H			Modify memory according to the following table: (Enter "modify memory <address> to <data>") Address Data 1#C007 E000 1#C008 0000 1#C008 0000 1#C00B 0000 1#C00B 0000 1#C00C 0E00 1#C00D 0000 Enter "modify memory MDS word 1#0C02E 0030"</data></address>	N/A	
	A10 .12	XXX1H		Address 0C02EH Observe t signal by test statu: 0C011H. as specifi (X = don't	Data 0030H the Mode A reading the self- s word, address Word shall be ed. t care)			Enter "display memory MDS word from 1#C011 to 1#C011" This location shall be as specified. (X = don't care)	XXX1H	
				<u> </u>						
		OBOXA.			AW/CRITICAL					
						OTATION		SUPPLEMENTS	A-42 PAGE	

SECURITY NOTATION	SPEC
	NO.

CAGE

CODE

IT4061400-907 **58960**

SEE PAGE INDEX FOR THIS SHEET REV LETTER REV LTR

	1				SEE THE	TITLE PAGE FOR P	ROF	PRIETARY AND DATA RIGHTS NOTA	TIONS.
REV	TEST		S	PECIFICATIO				PROCEDURE	SPECIFICATION
LTR	NO.	OPR LIMITS	c	TEST	DESCRIPTION	SWITCH POS	C	WORK STEPS	MFG LIMITS
	A10 .13	N/A		Perform t accordan STBY/ON discrete (hardward Interroga an ATCR interroga standard	test setup in ce with table 1. e path) te the UUT with BS Mode A tion at the rate and power.	S-1403 FUNC 1 ATC-1400A XPDR MODE A PRF: 0450 RF LvI: -50 CW/NORM/ OFF: NORM Mode S		Perform test setup in accordance with table 1. Turn XPDR MODE control knob to A position. Turn PRF thumbwheels to 450 Hz.	N/A
						Interface <u>Panel</u> STBY/ON: ON			
	A10 .14	>90%		Observe reply. Th as specif	the ATCRBS % he value shall be ied.			Read the % reply display on the ATC-1400A. The display shall indicate as specified.	>98%
				Switch th STANDB	e UUT to Y mode.	Mode S Interface <u>Panel</u> STBY/ON: STBY		On the Mode S inter-face panel, toggle the STBY/ON switch to the STBY position.	
	A10 .15	<10%		Observe reply. Th as specif	the ATCRBS % he value shall be ied.			Read the % reply display on the ATC-1400A. The display shall indicate as specified.	<2%
				This is th emulatior Continue tests 11 t	e end of PC/CPI testing. testing UUT with hru 28.				
-		<u> </u>	<u> </u>				<u> </u>		
		000 0000			AW/CRITICAL				
	SUPPLEMENTS SECURITY NOTATION								