



11AX40SISO_Ant1_2422



11AX40SISO_Ant1_2437



11AX40SISO_Ant1_2452

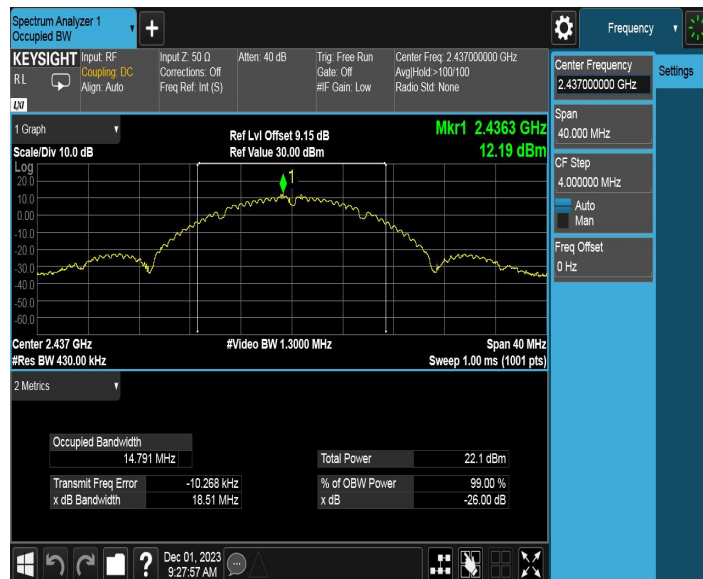


Test Graphs of Occupied Channel Bandwidth

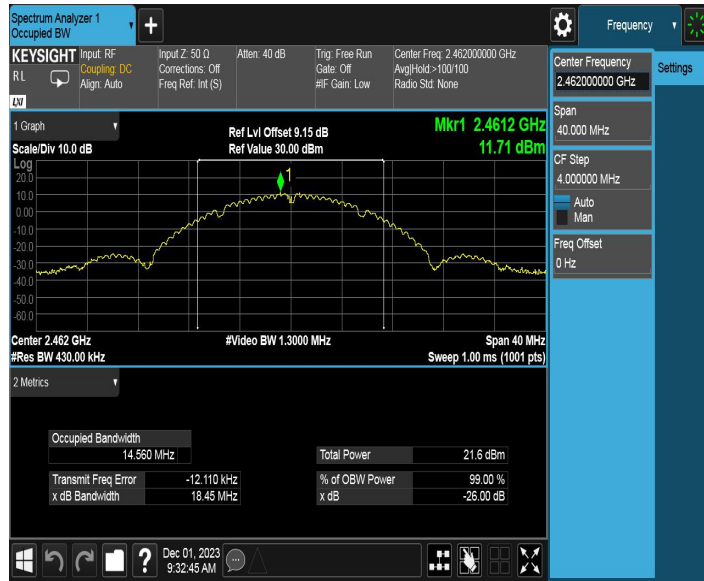
11B_Ant1_2412



11B_Ant1_2437



11B_Ant1_2462



11G_Ant1_2412



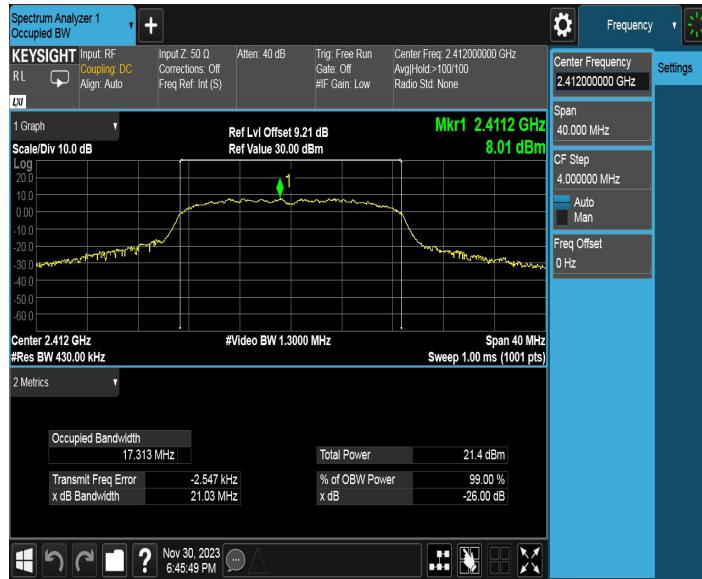
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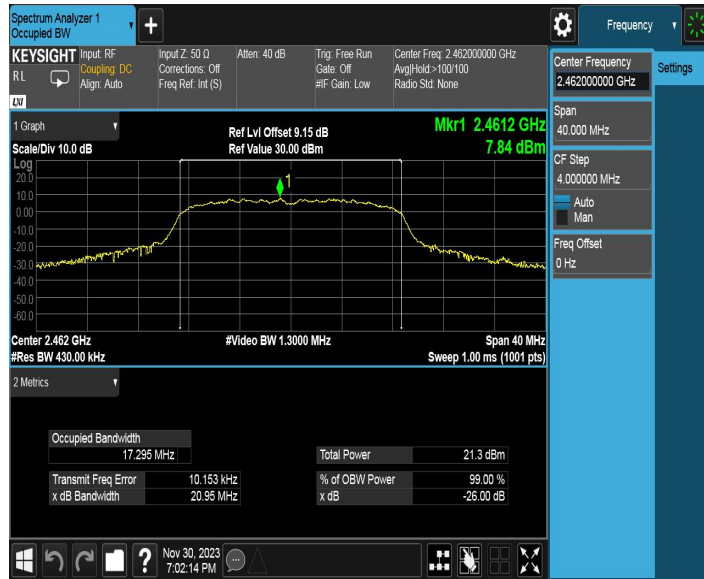
11N20SISO_Ant1_2412



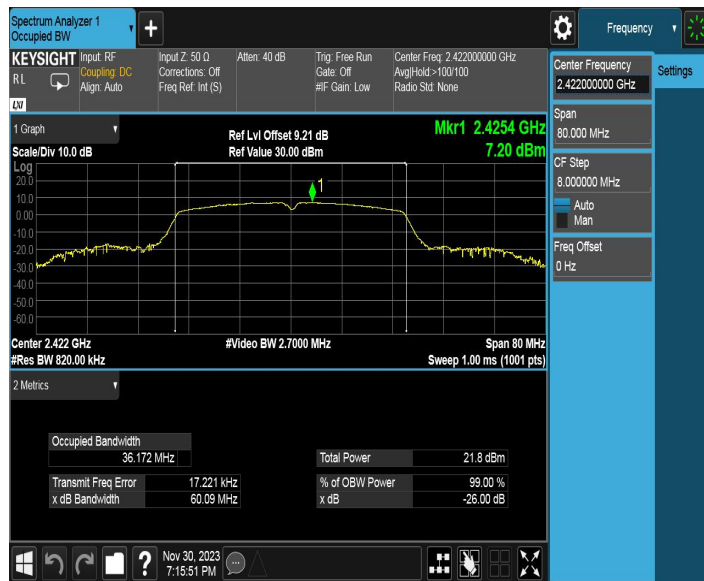
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7.3. Output Power Measurement

7.3.1. Test Limit

The maximum conducted output power is 1 Watt. And for antenna gain greater than 6dBi the limit shall reduce by the amount in dB that the directional gain of the antenna exceeds 6 dBi.

7.3.2. Test Procedure Used

ANSI C63.10-2013 – Section 11.9.2.2.4

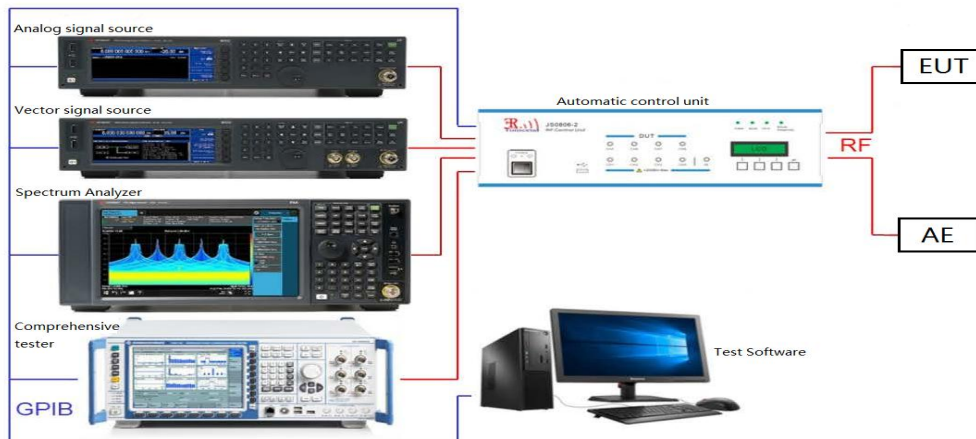
KDB 558074 D01 v05r02 – Section 8.3.2.2

7.3.3. Test Setting

1. Set span to at least 1.5 times the OBW..
2. Set RBW = 1% to 5% of the OBW, not to exceed 1 MHz.
3. Set VBW $\geq [3 \times \text{RBW}]$.
4. Number of points in sweep $\geq [2 \times \text{span} / \text{RBW}]$. (This gives bin-to-bin spacing $\text{RBW} / 2$, so that narrowband signals are not lost between frequency bins.)
5. Sweep time = auto.
6. Detector = RMS (i.e., power averaging), if available. Otherwise, use sample detector mode.
7. Do not use sweep triggering. Allow the sweep to “free run.”
8. Trace average at least 100 traces in power averaging (rms) mode; however, the number of traces to be averaged shall be increased above 100 as needed such that the average accurately represents the true average over the ON and OFF periods of the transmitter.
9. Compute power by integrating the spectrum across the OBW of the signal using the instrument’s band power measurement function with band limits set equal to the OBW band edges. If the instrument does not have a band power function, then sum the spectrum levels (in power units) at intervals equal to the RBW extending across the entire OBW of the spectrum. 10Add $[10 \log (1 / D)]$, where D is the duty cycle, to the measured power to compute the average power during the actual transmission times (because the measurement represents an average over both

the ON and OFF times of the transmission). For example, add $[10 \log (1/0.25)] = 6 \text{ dB}$ if the duty cycle is 25%.

7.3.4. Test Setup



7.3.5. Test Result

Power output test was verified over all data rates of each mode shown as below, and then choose the maximum power output (yellow marker) for final test of each channel.

N _{Tx}	Data Rate (Mbps)	
	802.11b	802.11g
1	1	6
1	2	9
1	5.5	12
1	11	18
1	--	24
1	--	36
1	--	48
1	--	54

N _{Tx}	MCS Index for 802.11n	Data Rate (Mbps)			
		20MHz Bandwidth		40MHz Bandwidth	
		800ns GI	400ns GI	800ns GI	400ns GI
1	0	6.5	7.2	13.5	15.0
1	1	13.0	14.4	27.0	30.0
1	2	19.5	21.7	40.5	45.0
1	3	26.0	28.9	54.0	60.0
1	4	39.0	43.3	81.0	90.0
1	5	52.0	57.8	108.0	120.0
1	6	58.5	65.0	121.5	135.0
1	7	65.0	72.2	135.0	150.0

N _{Tx}	MCS Index for 802.11ax	Data Rate (Mbps)			
		20MHz Bandwidth		40MHz Bandwidth	
		1600ns GI	800ns GI	1600ns GI	800ns GI
1	0	8.0	8.6	16.0	17.2
1	1	16.0	17.2	33.0	34.4
1	2	24.0	25.8	49.0	51.6
1	3	33.0	34.4	65.0	68.8
1	4	49.0	51.6	98.0	103.2
1	5	65.0	68.6	130.0	137.6

1	6	73.0	77.4	146.0	154.9
1	7	81.0	86.0	163.0	172.1
1	8	98.0	103.2	195.0	206.5
1	9	108.0	114.7	217.0	229.4
1	10	122.0	129.0	244.0	258.1
1	11	135.0	143.5	271.0	286.8

Note: Power output test was verified over all data rates of each mode shown as above, and then choose the maximum power output (yellow marker) for final test of each channel.

Test Result of Maximum conducted output power

Test Mode	Antenna	Frequency[MHz]	Average power [dBm]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11B	Ant1	2412	19.50	0.05	19.55	≤30.00	PASS
		2437	18.93	0.05	18.98	≤30.00	PASS
		2462	18.40	0.05	18.45	≤30.00	PASS
11G	Ant1	2412	15.09	0.06	15.15	≤30.00	PASS
		2437	15.03	0.03	15.06	≤30.00	PASS
		2462	14.73	0.06	14.79	≤30.00	PASS
11N20SISO	Ant1	2412	14.39	0.03	14.42	≤30.00	PASS
		2437	15.22	0.07	15.29	≤30.00	PASS
		2462	14.28	0.07	14.35	≤30.00	PASS
11N40SISO	Ant1	2422	14.45	0.07	14.52	≤30.00	PASS
		2437	14.13	0.07	14.20	≤30.00	PASS
		2452	14.27	0.07	14.34	≤30.00	PASS
11AX20SISO	Ant1	2412	15.16	0.20	15.36	≤30.00	PASS
		2437	14.97	0.20	15.17	≤30.00	PASS
		2462	14.86	0.24	15.10	≤30.00	PASS
11AX40SISO	Ant1	2422	14.82	0.07	14.89	≤30.00	PASS
		2437	14.03	0.07	14.10	≤30.00	PASS
		2452	13.76	0.07	13.83	≤30.00	PASS

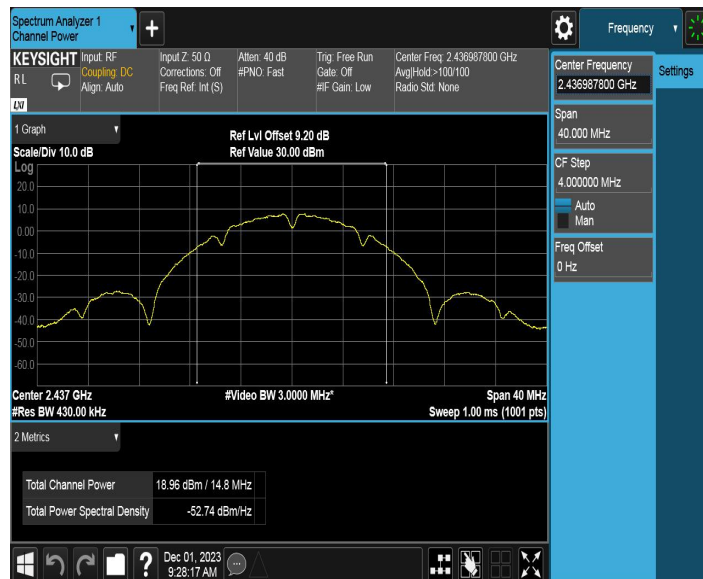
The Duty Cycle Factor is compensated in the Offset of graph.

Test Graphs

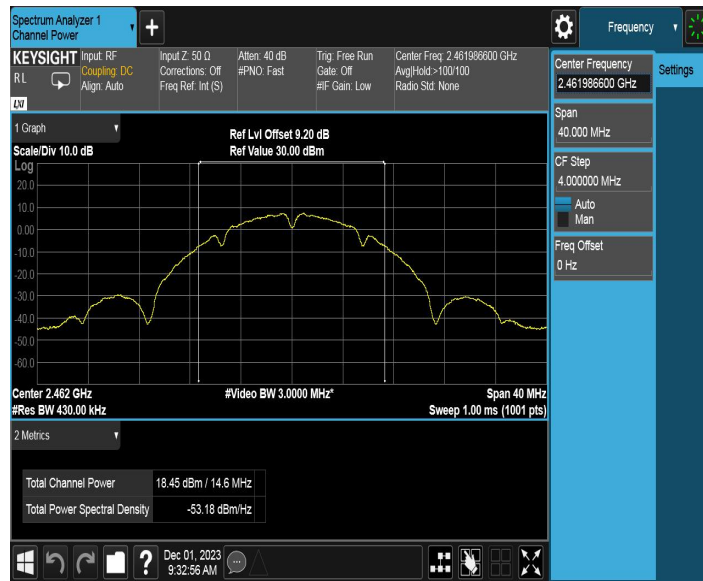
11B_Ant1_2412



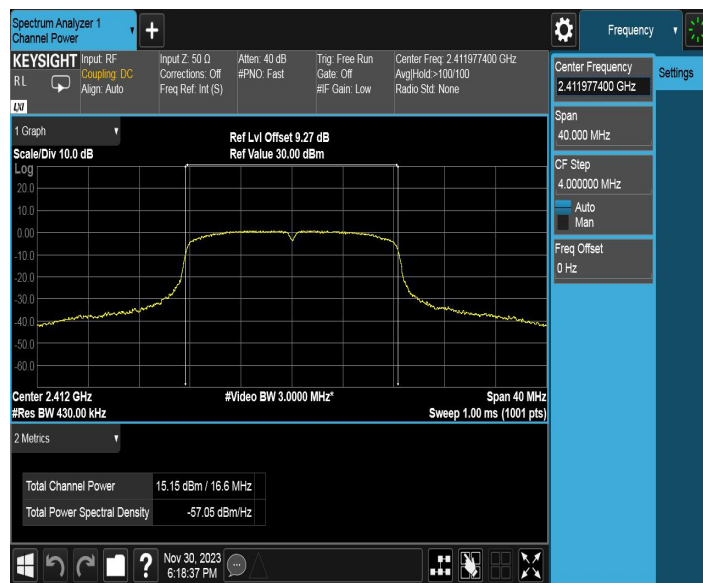
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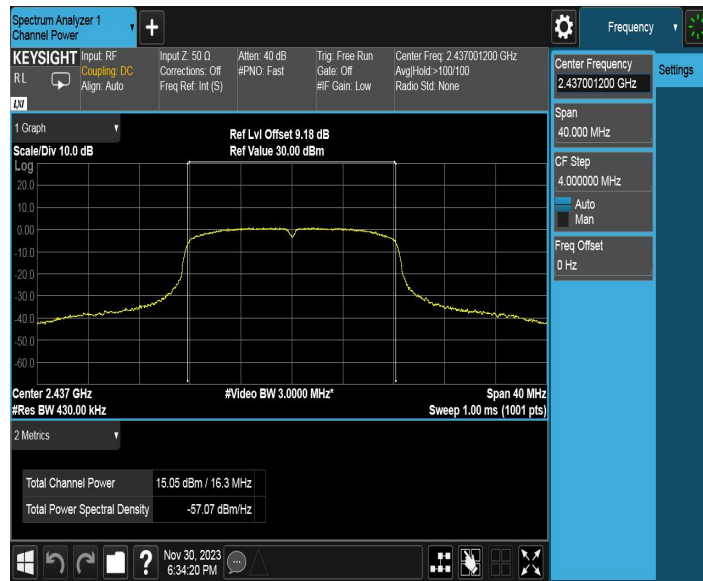
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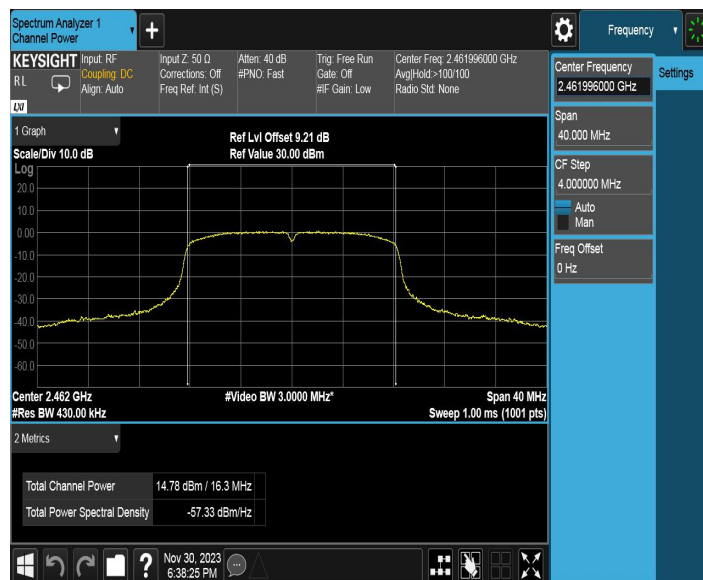
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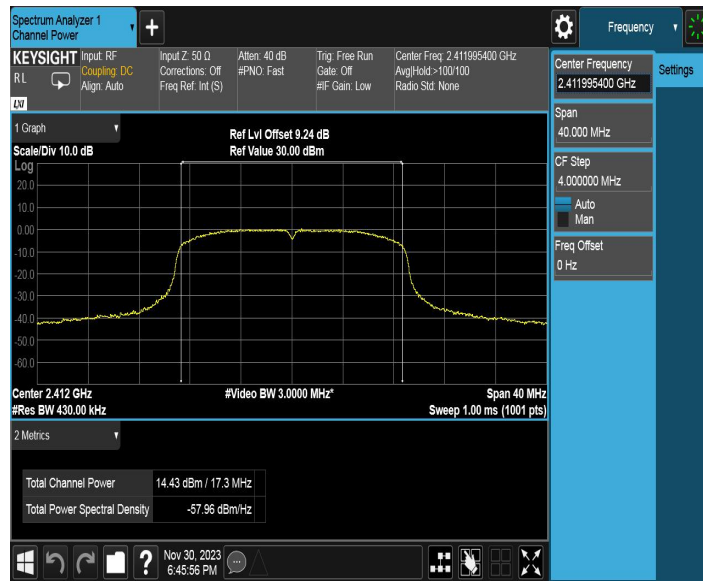
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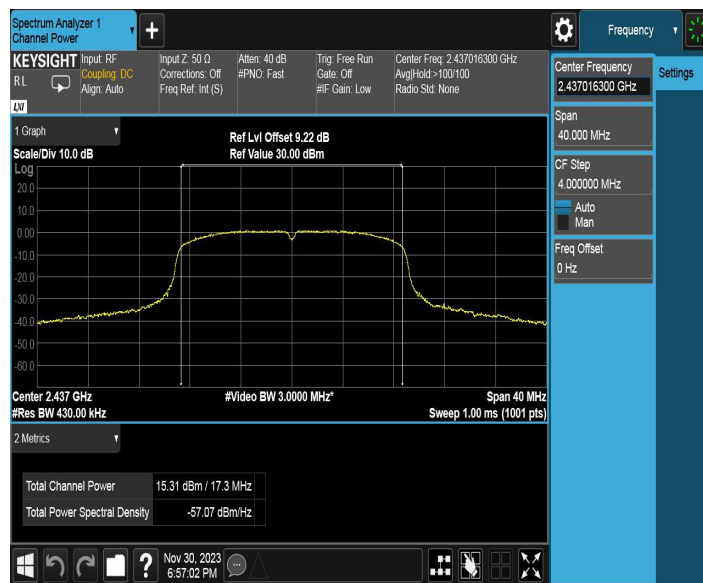
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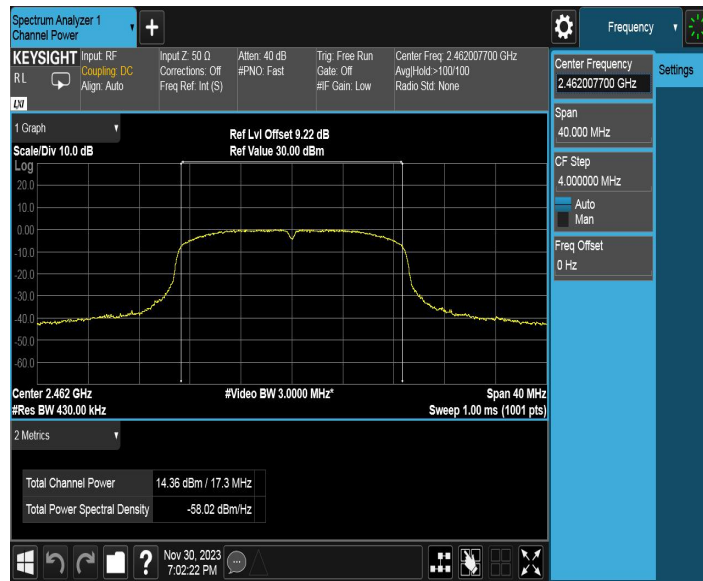
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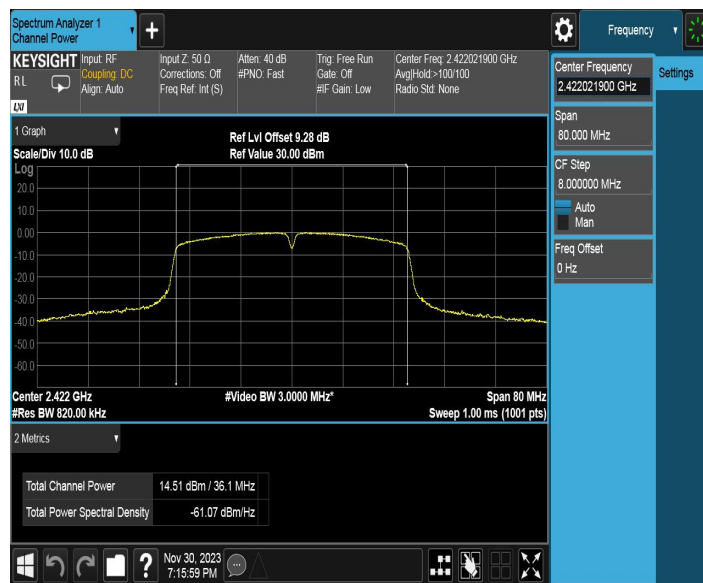
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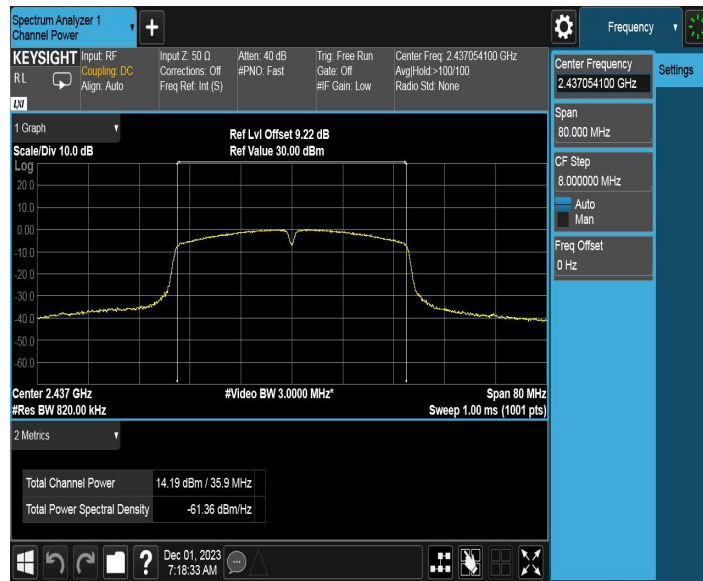
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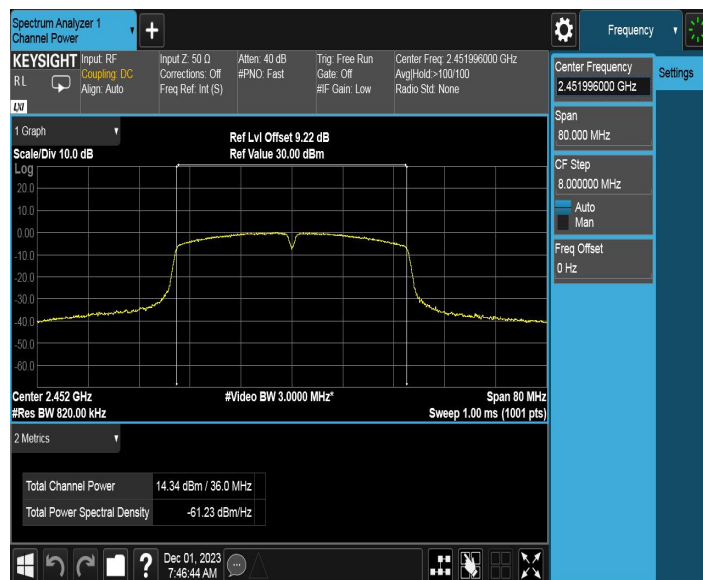
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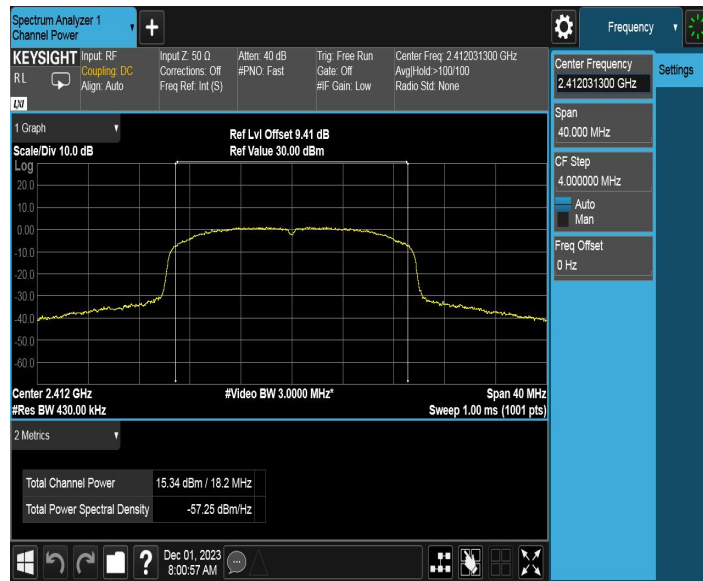
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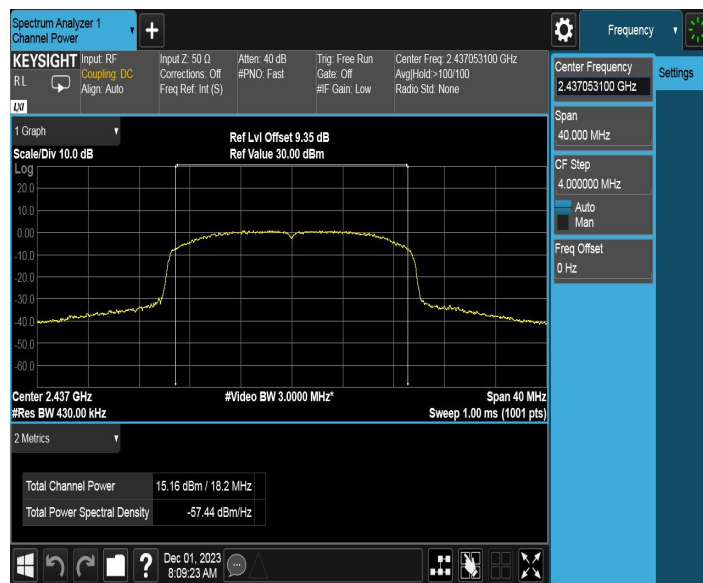
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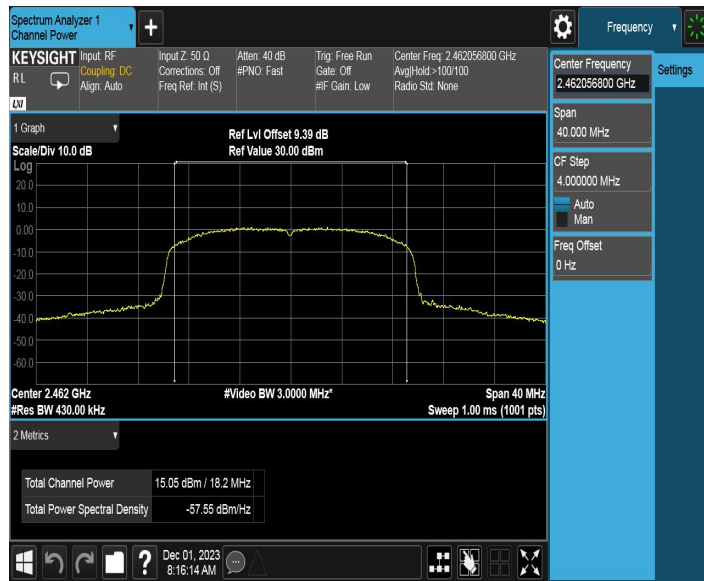
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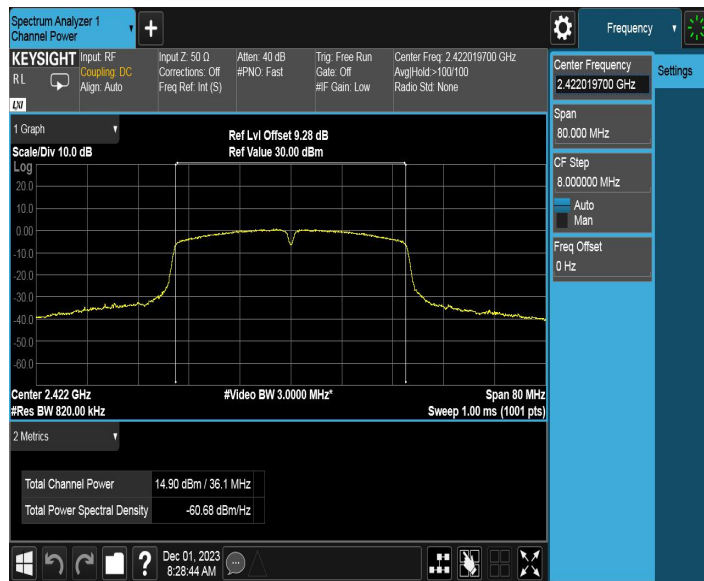
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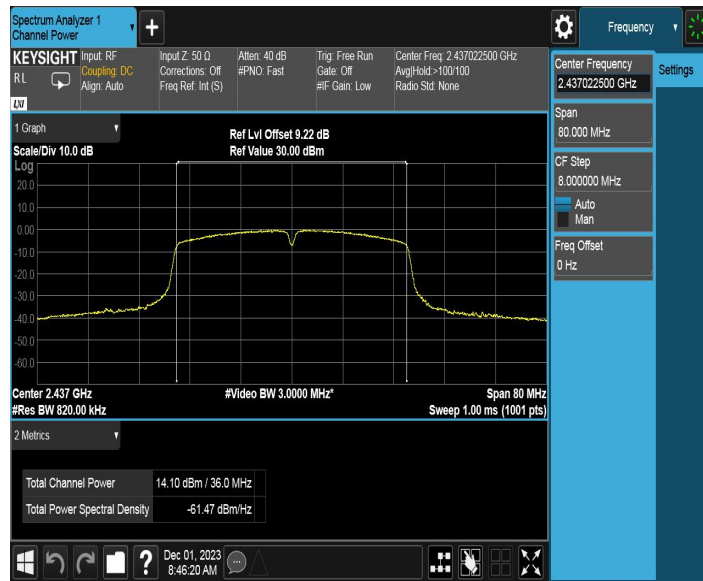
11AX20SISO_Ant1_2462



11AX40SISO_Ant1_2422



11AX40SISO_Ant1_2437



11AX40SISO_Ant1_2452

