

Description of the TS1000BS base station circuit

The TS1000BS base station is embedded with a CPU MC68332, EPROM 27C256, SRAM 43256, IC ST16C2552, RS232 driver MAX3238, and LED indicators. Digital circuit runs with 14.7456MHz clock. Data from one port among J0, J1, and J2 is processed and distributed into other ports. TS1000BS also provides an interface port for wireless transceiver that is integrated by a user.

Circuit Block Diagram

