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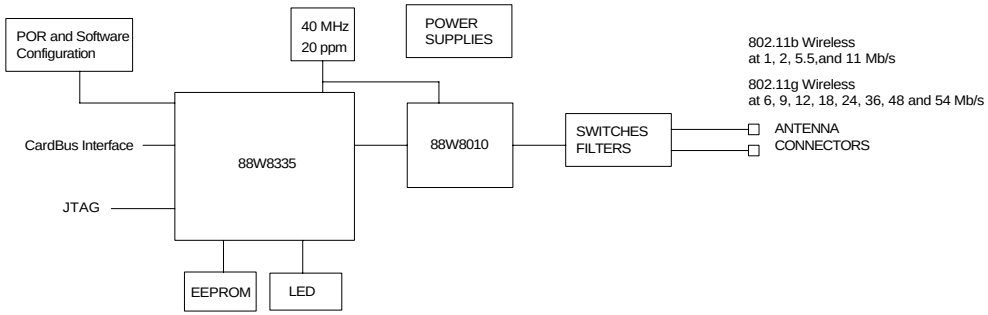
Block Diagram



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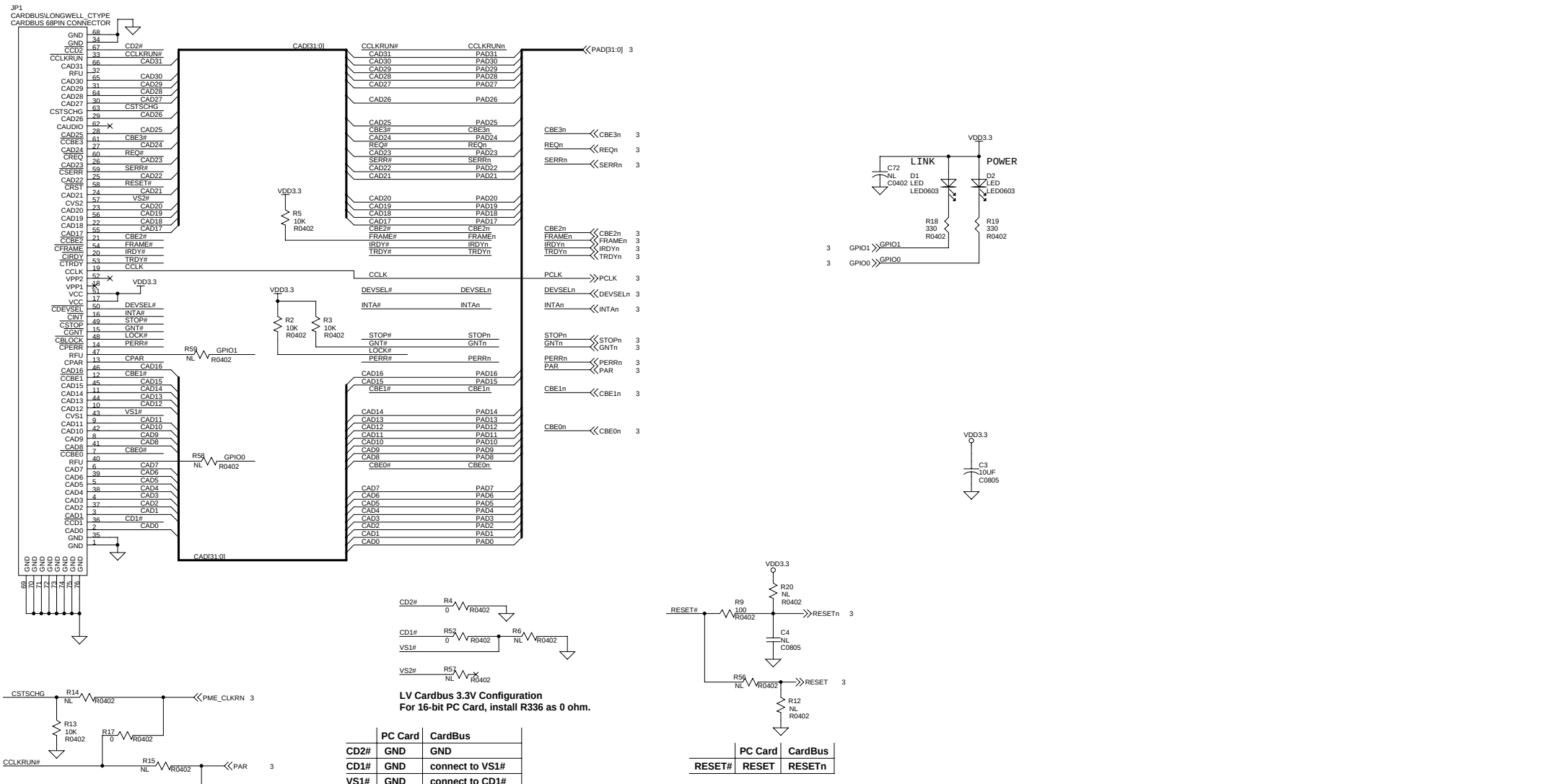
CARDBUS INTERFACE



LV Cardbus 3.3V Configuration
For 16-bit PC Card, install R336 as 0 ohm.

	PC Card	CardBus
CD2#	GND	GND
CD1#	GND	connect to VS1#
VS1#	GND	connect to CD1#
VS2#	open	open

	PC Card	CardBus
RESET#	RESET	RESETn

	PC Card	CardBus
CSTSCHG	PME_CLKRUN	LOW
CCLKRUN#	PAR	PME_CLKRUN
CPAR	LOW	PAR

Board Fiducials

Tooling holes

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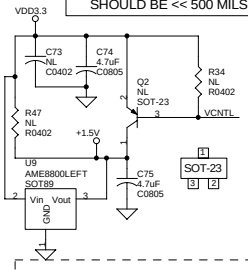
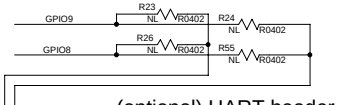
CRITICAL TRANSISTOR PLACEMENT:
- ROUTED LENGTH OF VCNT AND VSNS SHOULD BE << 500 MILS.

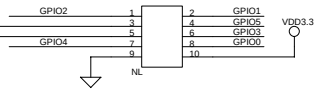
Voltage Regulator Pass Transistors



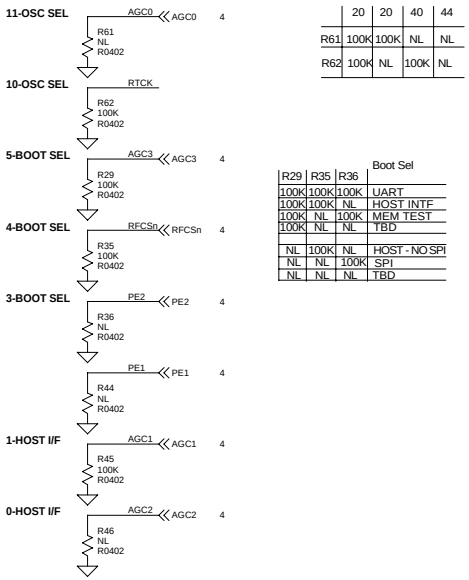
R24, R26 -> 0 ohm for DTE
R23, R55 -> 0 ohm for DCE



(optional) UART header (CMOS signal levels)



GPIO	HDR	DB9	Signal	Full Name
2	1	1	CD	Carrier Detect
1	2	6	DSR	Data Set Ready
9	3	2	RD	Receive Data
5	4	7	RTS	Request To Send
8	5	3	TD	Transmit Data
3	6	8	CTS	Clear To Send
4	7	4	DTR	Data Terminal Ready
0	8	9	RI	Ring Indicator
	9	5	SG	Signal Ground



Oscillator Freq (MHz)

	20	20	40	44
R61	100K	100K	NL	NL
R62	100K	NL	100K	NL

Boot Sel

R29	R35	R36	
100K	100K	100K	UART
100K	100K	NL	HOST INTF
100K	NL	100K	MEM TEST
100K	NL	NL	TBD
NL	100K	NL	HOST - NO SPI
NL	NL	100K	SPI
NL	NL	NL	TBD

	PC Card	CardBus
AGC1 pulldown	NL	100K
AGC2 pulldown	100K	NL

RESET CONFIGURATION

Note: R28 used as test point

MOUNT THIS RESISTOR CLOSE TO THE CHIP

Note: R38 used as test point

PLACE R CLOSE TO PIN

88W8335

CRITICAL: ROUTE 88W8010 CTRL SIGNALS AWAY FROM CLOCK LINES AND OTHER NOISE SOURCES.

88W8335 BYPASS CAPACITORS

(THESE MUST BE AS CLOSE TO THE VDD PINS AS POSSIBLE)

JTAG TEST PURPOSES ONLY

R66-R69 = 10K OHM
R70 = 0 OHM

SPI EEPROM

1024 x 8bit

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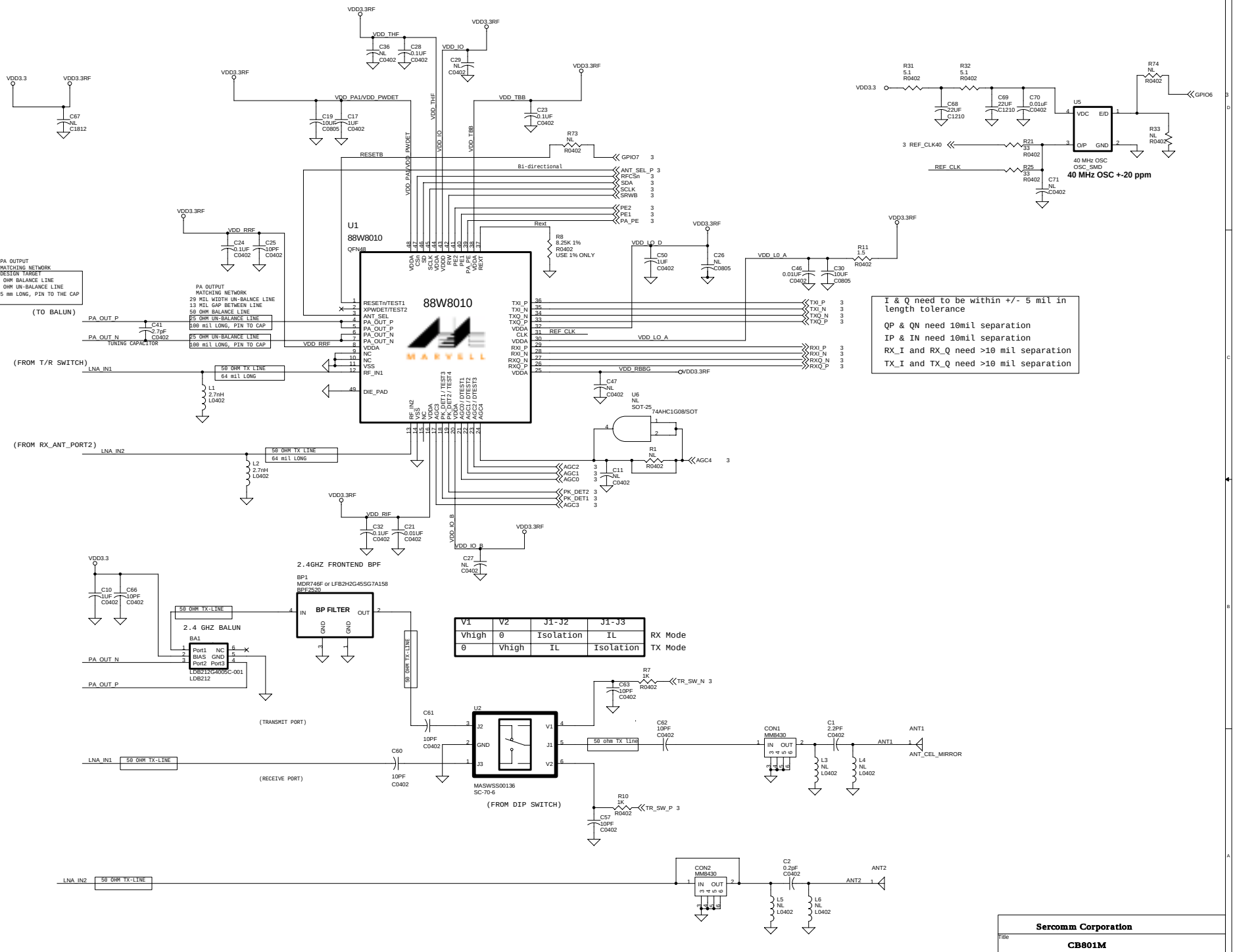

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