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Federal Communications Commission Authorization and Evaluation Division

Request to Supersede the Block Diagram for FCC ID: OZFACXX20.

To whom it may concern,

An error in the block diagram that was submitted for the filing of OZFACXX20 was not noticed until after the 30 day grace period. The block diagram erroneously indicates that there is a Tx/Rx switch before a single antenna, where in actuality there is no switch but instead a separate antenna for both the Rx and Tx paths. All other exhibits for the filing are correct. I apologise for this error and kindly request that you will replace the erroneous exhibit with the new one.

Sincerely,

Mark Fairburn

RF Design Engineer

Tantalus Systems Corp.