

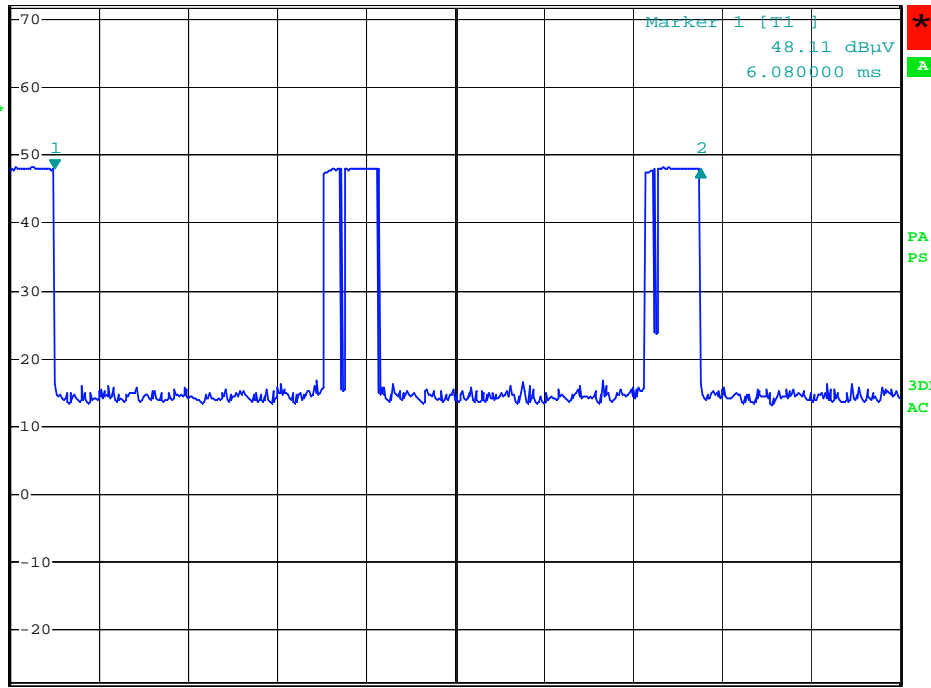
Controlled by EMC32

RBW 3 MHz Delta 2 [T1]
*VBW 3 MHz -0.22 dB
SWT 120 ms 87.040000 ms

Ref 72 dB μ V

*Att 0 dB

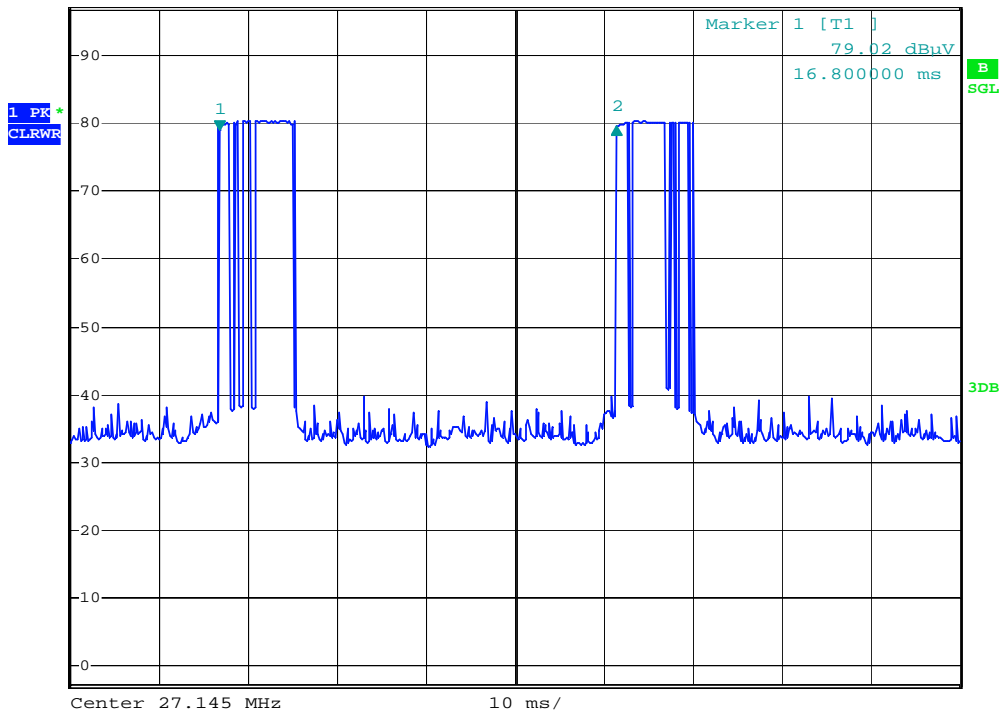
1 PK *
VIEW



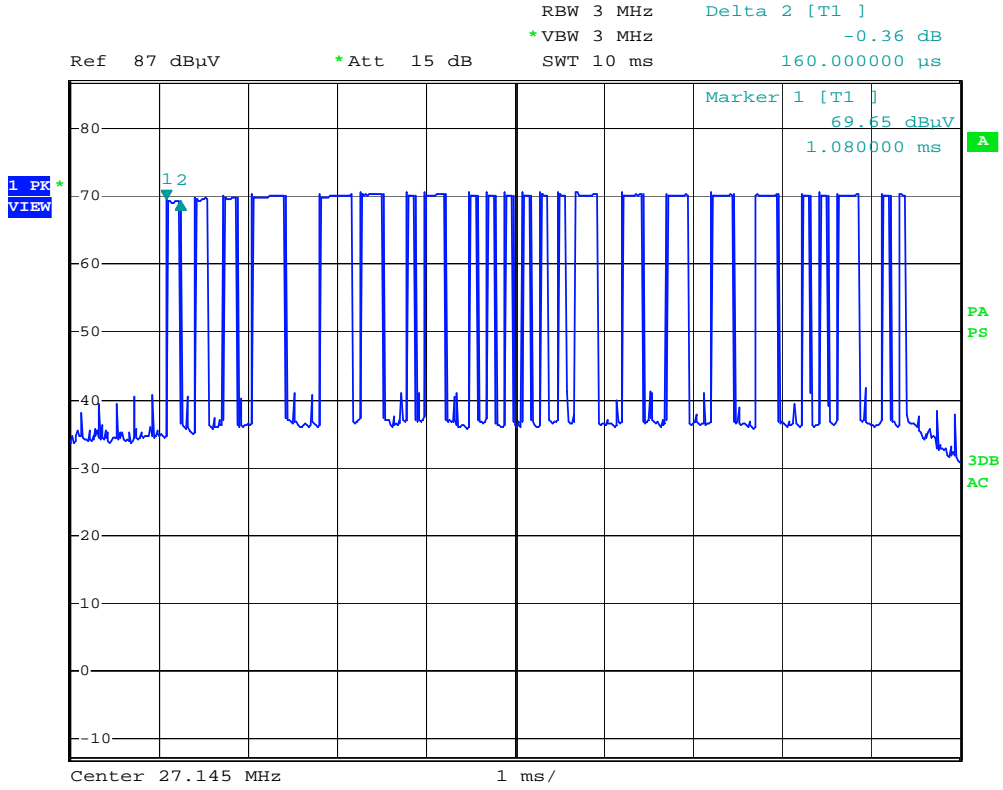
Date: 14.JUN.2008 11:10:02



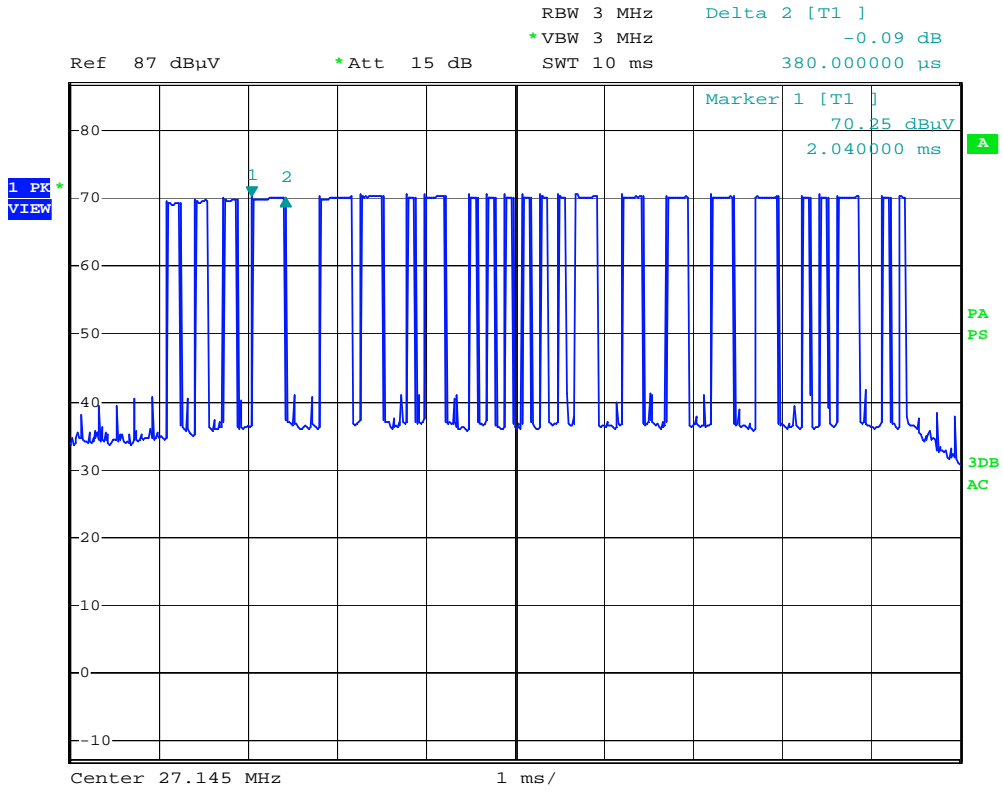
Ref 97 dB μ V *Att 0 dB RBW 3 MHz Delta 2 [T1]
*VBW 3 MHz 0.43 dB
SWT 100 ms 44.600000 ms



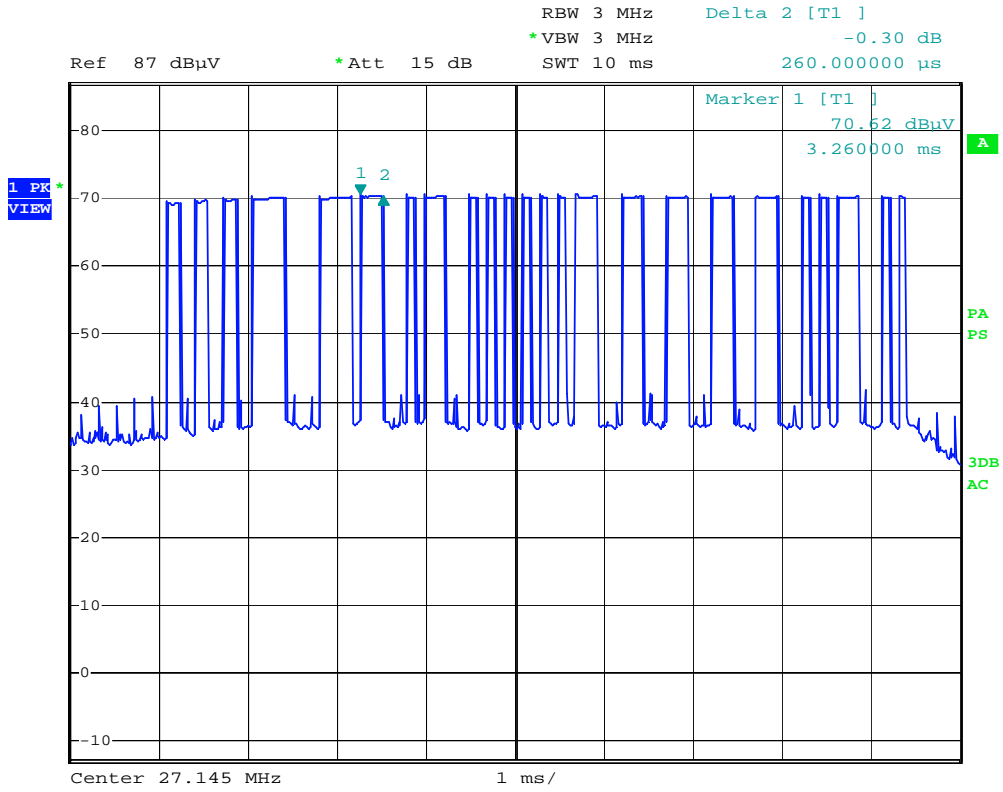
Date: 24.JUN.2008 09:37:00



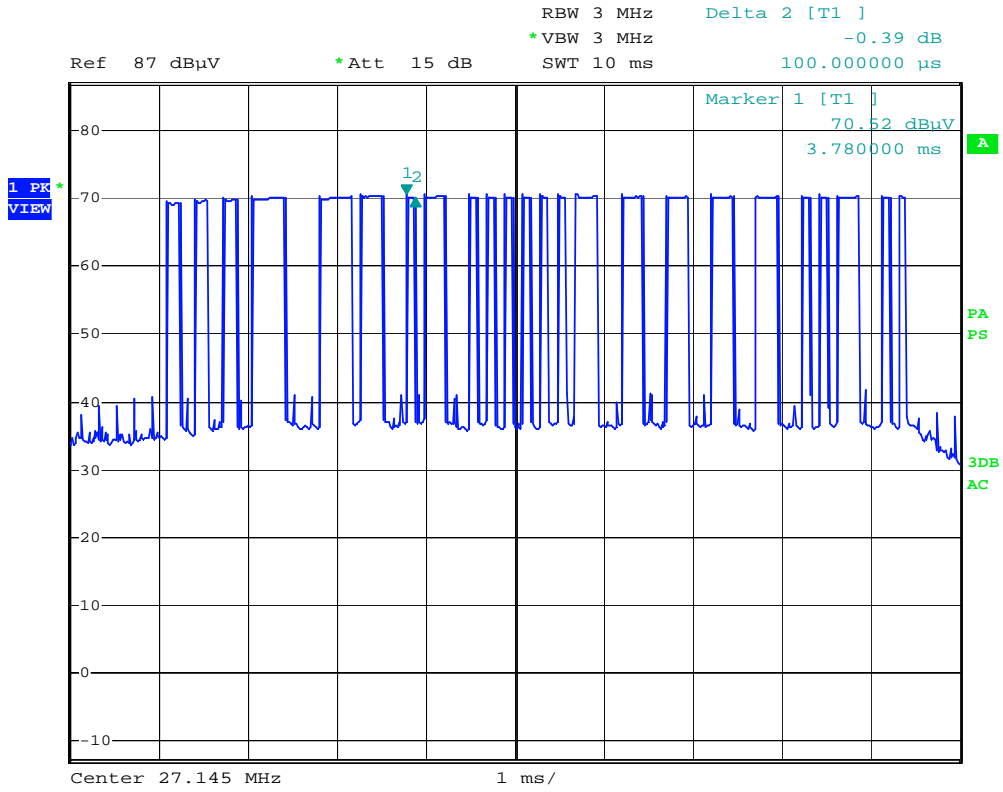
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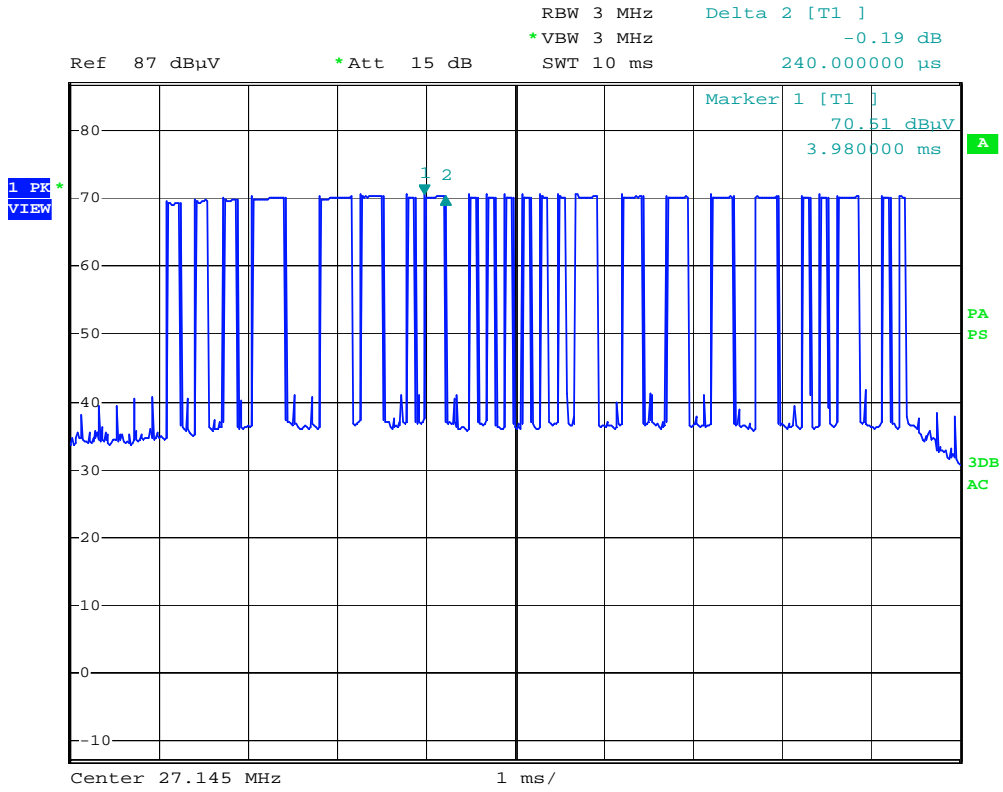
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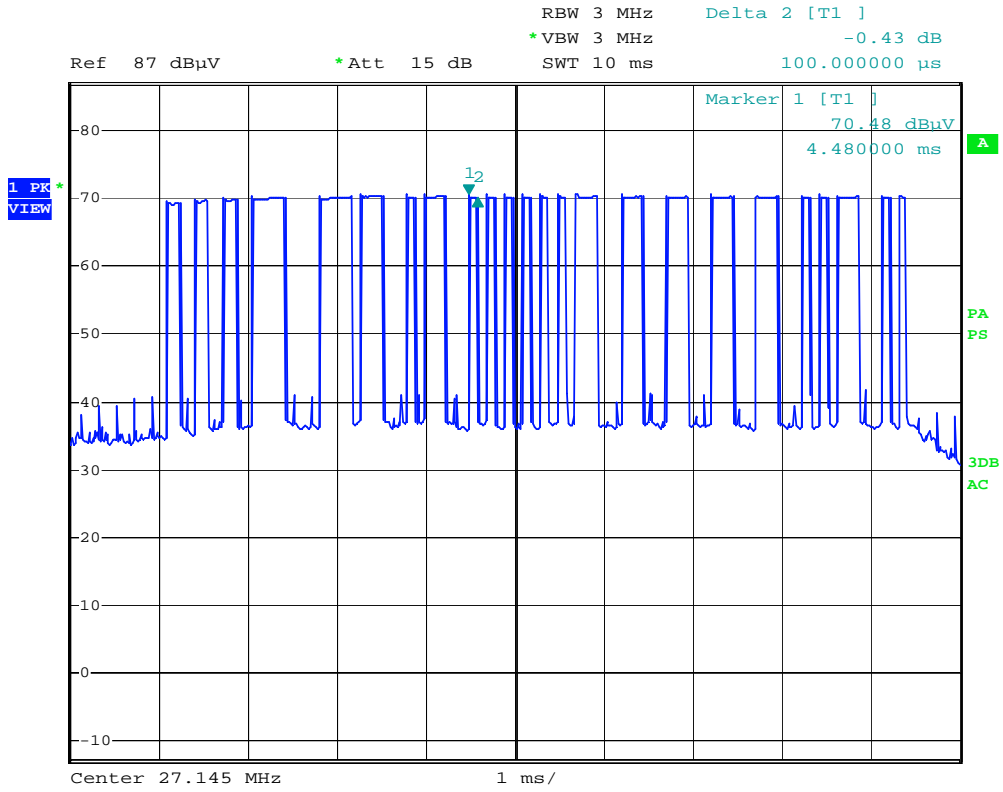
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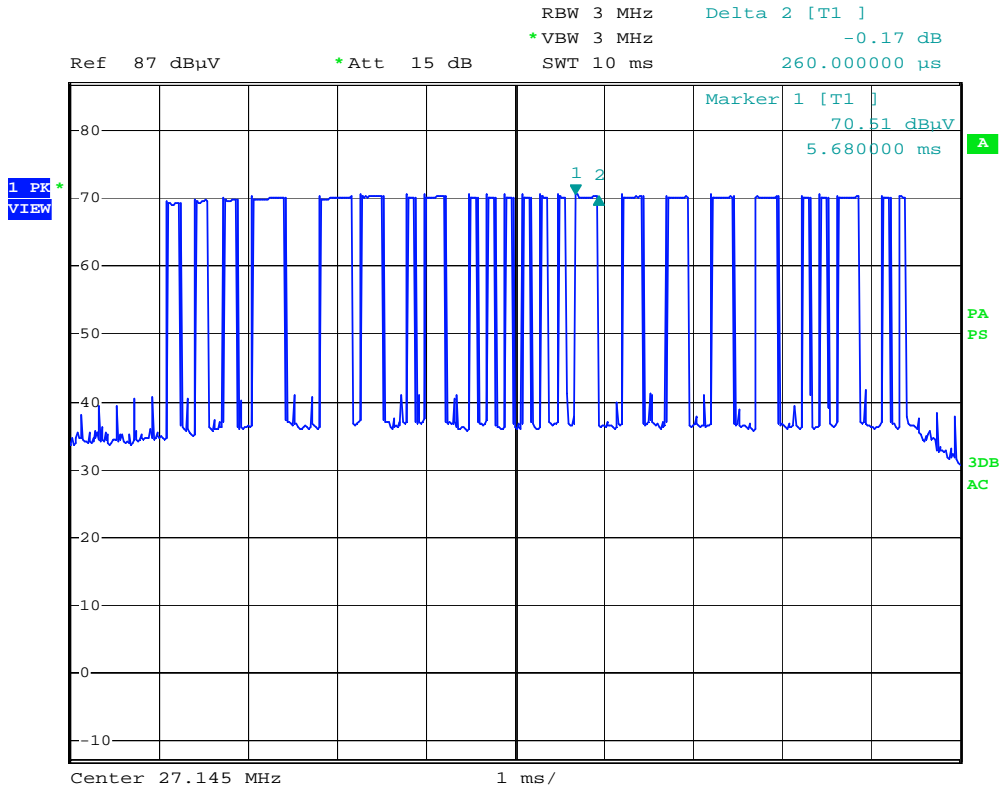
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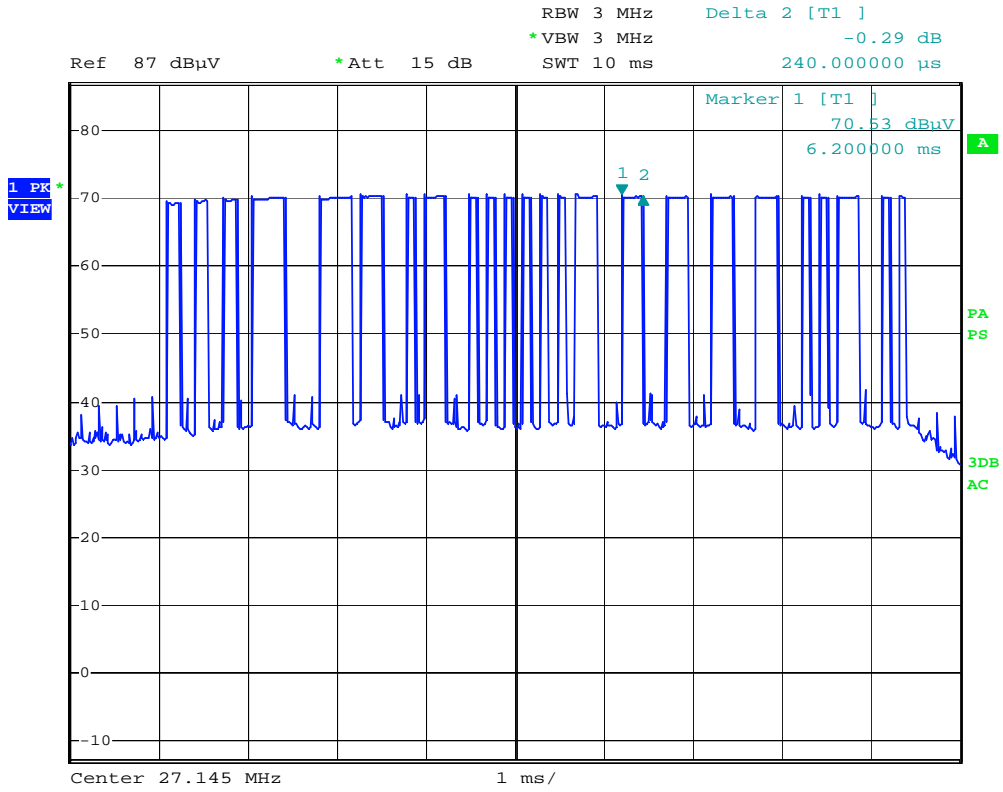
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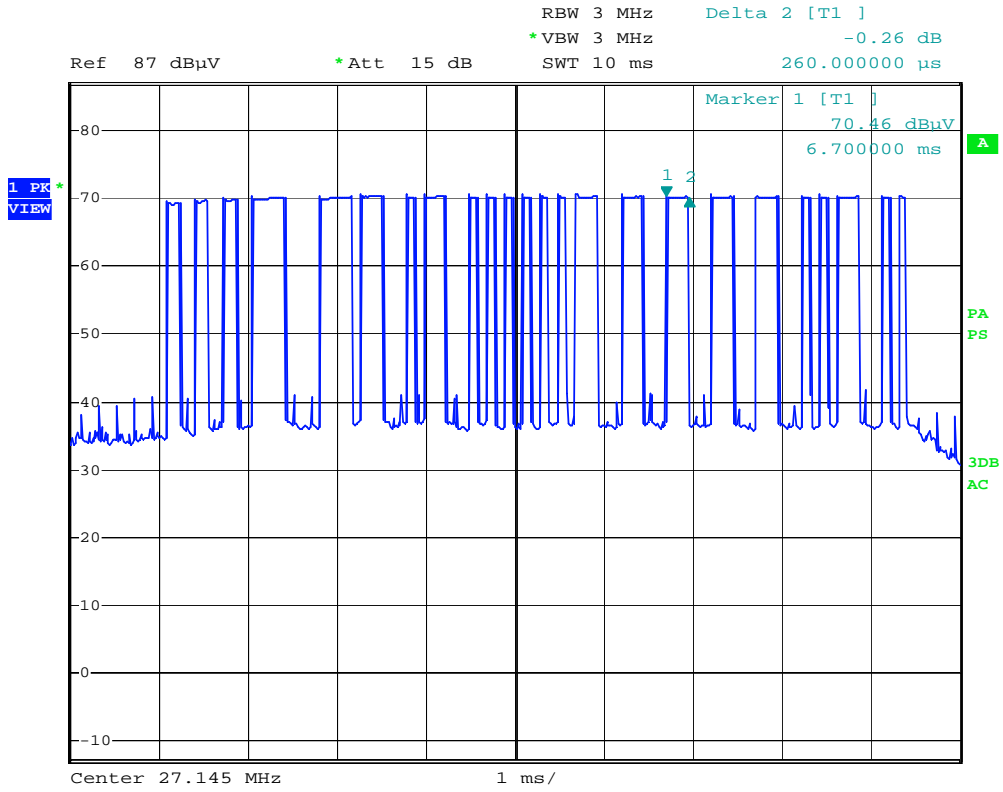
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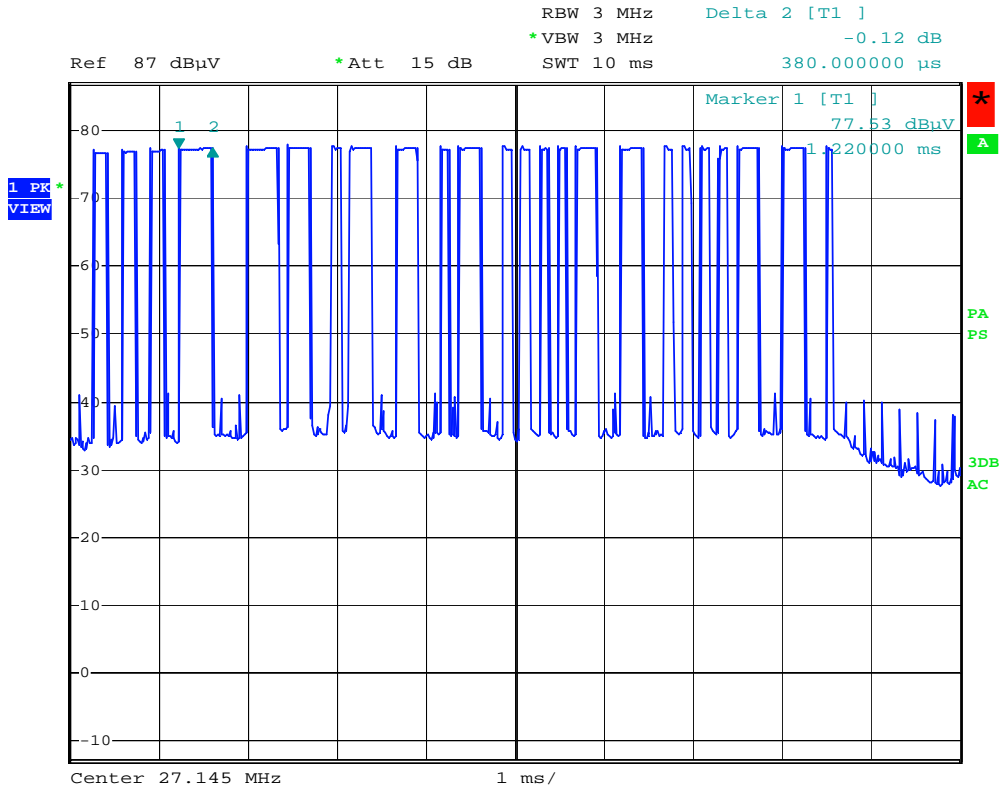
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Date: 14.JUN.2008 14:34:32



Date: 14.JUN.2008 14:34:59



Date: 14.JUN.2008 14:44:54