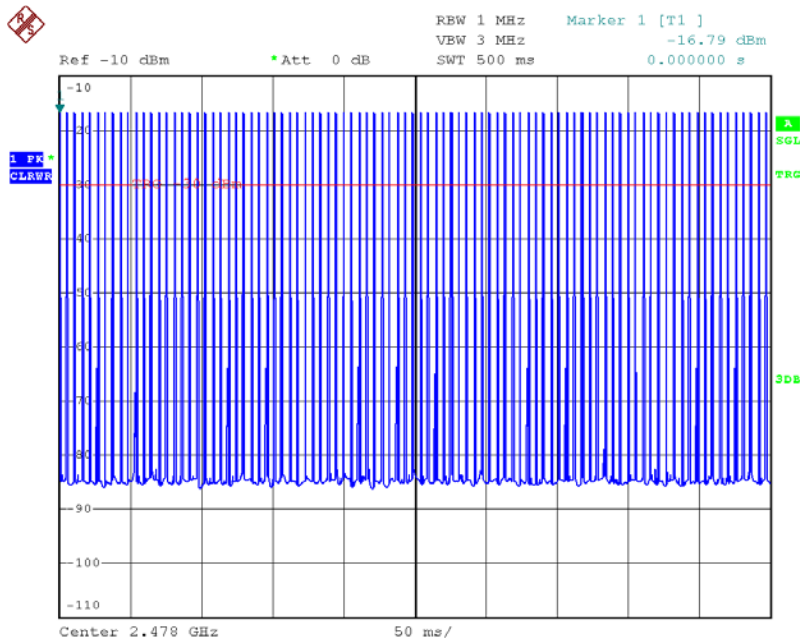
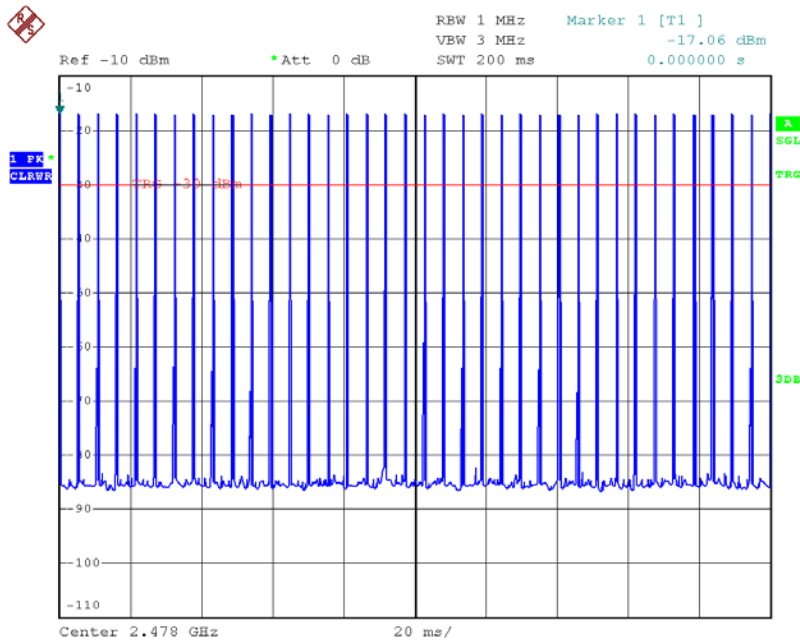


INTERTEK TESTING SERVICES

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Sample No.: 1/2



Date: 25.MAY.2011 00:12:25

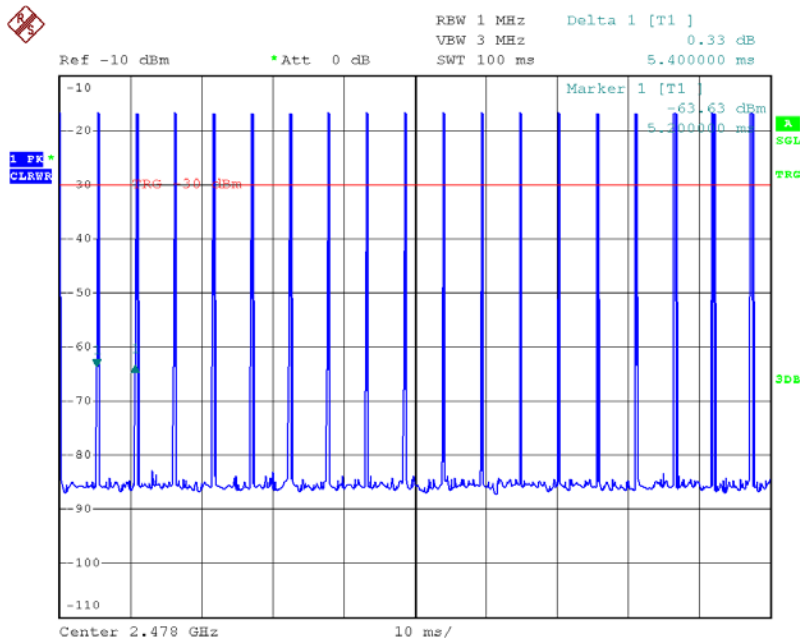


Date: 25.MAY.2011 00:13:25

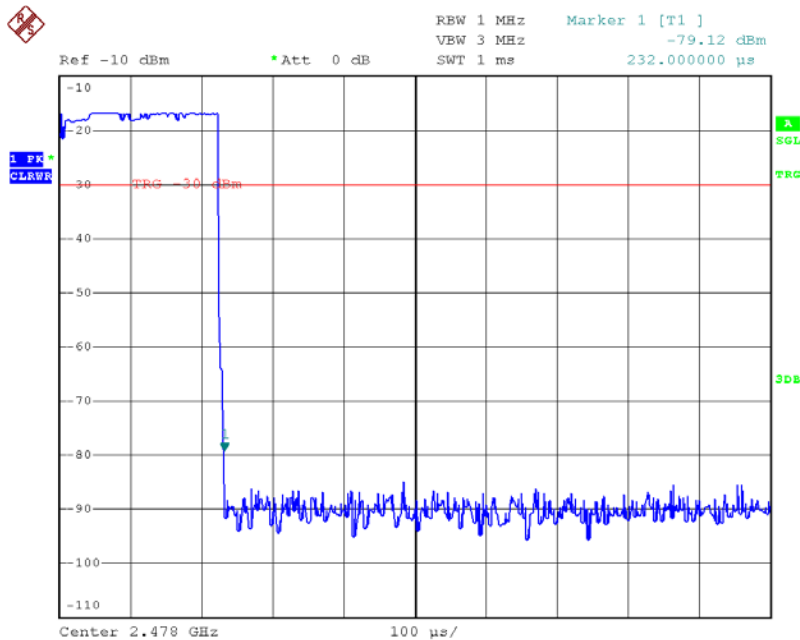
Average Factor Plot - Pursuant to FCC Part 15 Section 15.249 Requirement (Pass/Fail)

INTERTEK TESTING SERVICES

Report No.: HK11051169-1
Sample No.: 1/2



Date: 25.MAY.2011 00:14:13



Date: 25.MAY.2011 00:16:00

Duty Cycle:

$$= \frac{232\text{us} \times 19}{100\text{ms}} = 44.08\text{ms}$$

Average Factor:

$$= 20\log(44.08\text{ms})$$

$$= -27.1\text{dB}$$

Average Factor Plot - Pursuant to FCC Part 15 Section 15.249 Requirement (Pass/Fail)