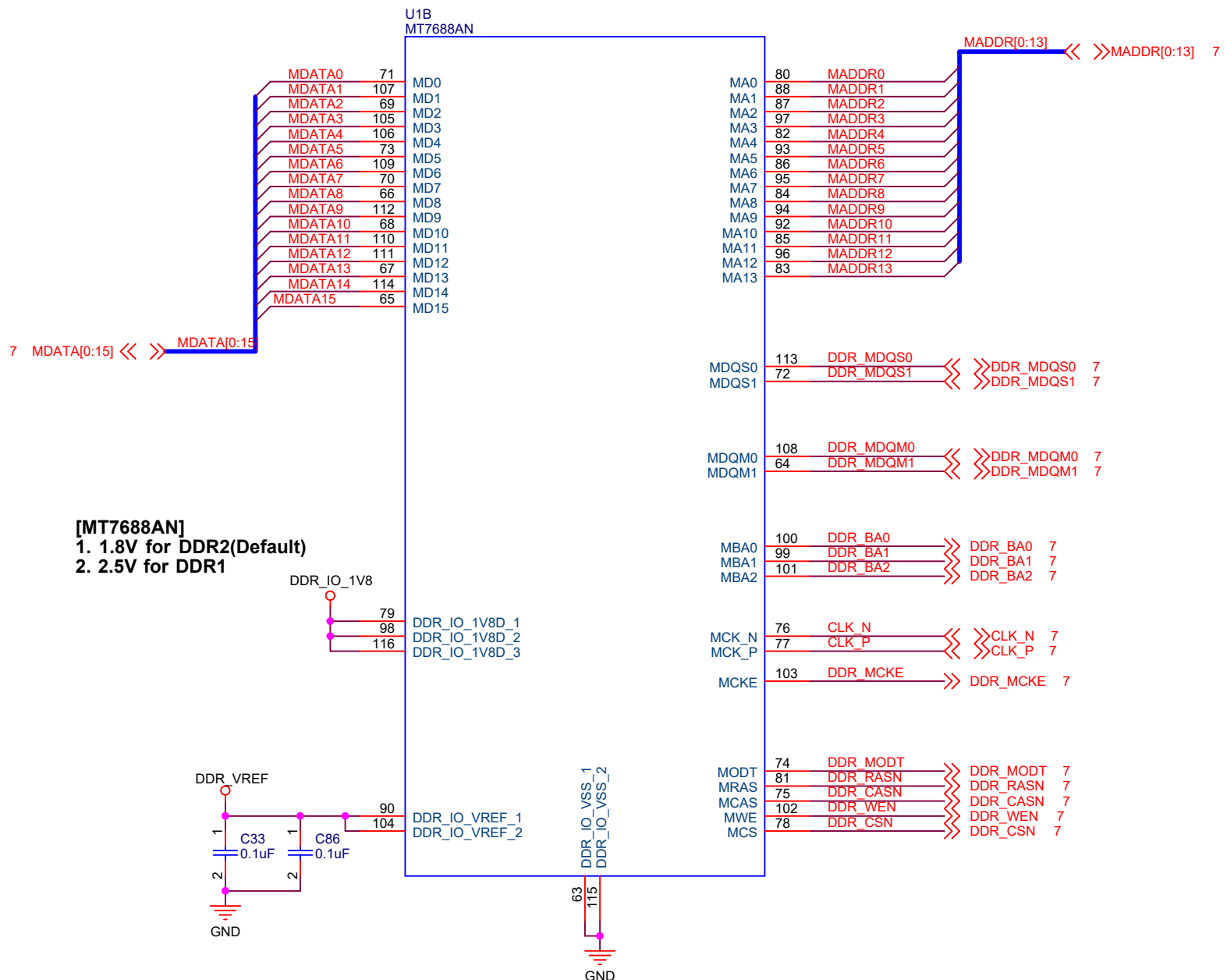
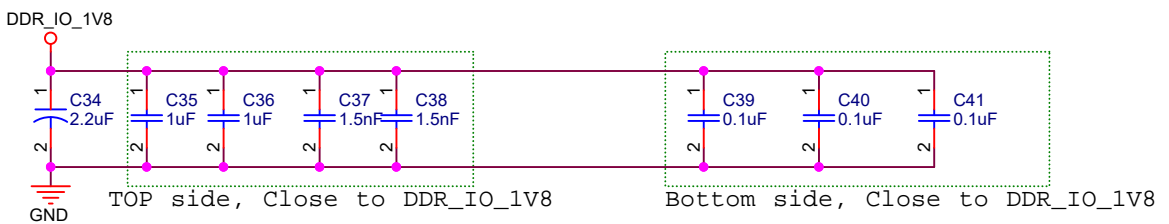
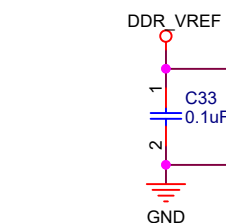


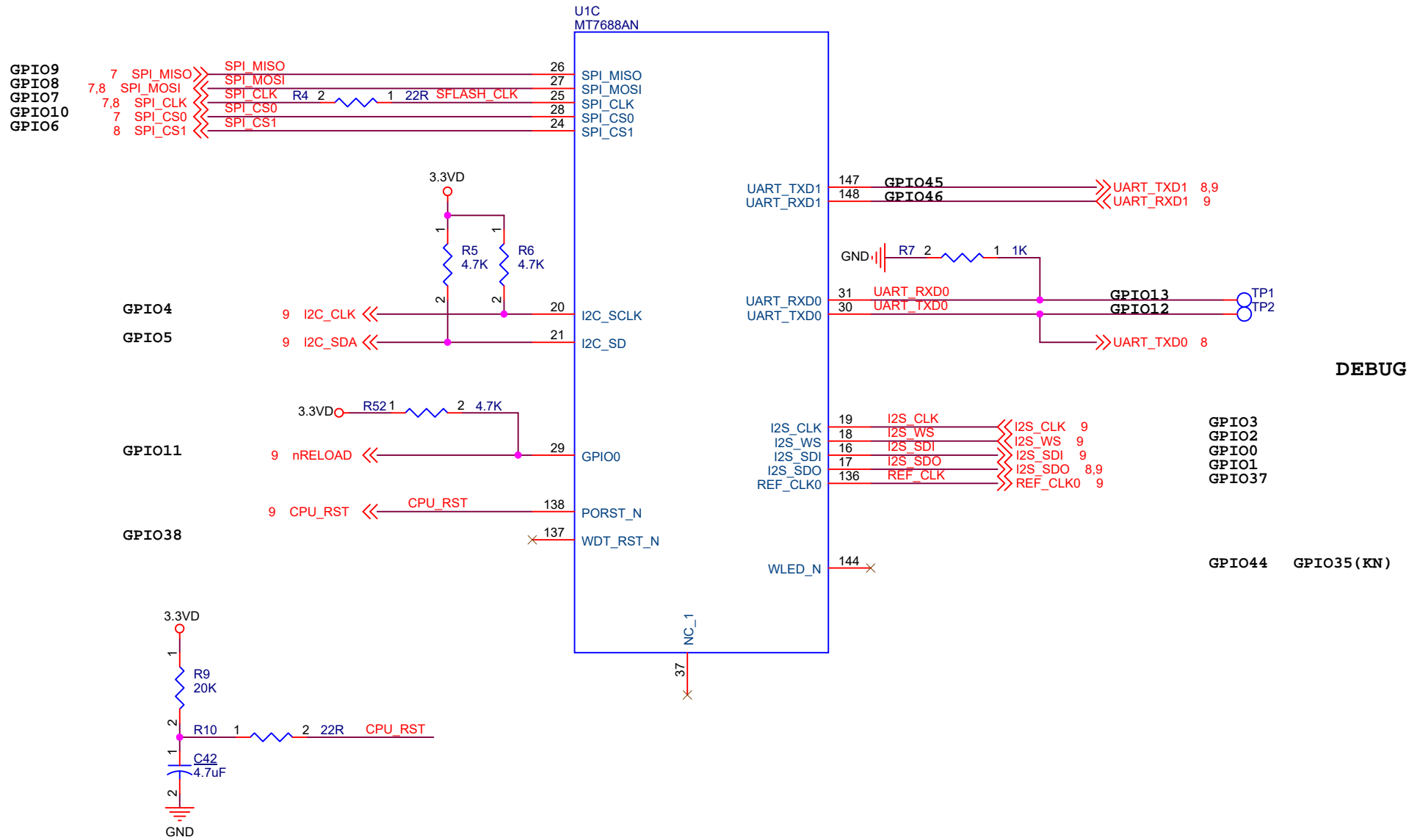
Title		
WiFiAudio A31 MODULE(MT7688AN)		
Size	Document Number	Rev
A3	RF FRONT END	03



**[MT7688AN]**  
**1. 1.8V for DDR2(Default)**  
**2. 2.5V for DDR1**



Title		
WIFIAudio A31 MODULE(MT7688AN)		
Size	Document Number	Rev
A4	DDR INTERFACE	03

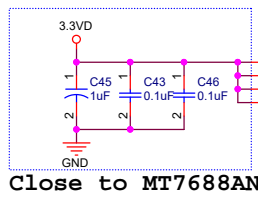


**DEBUG**

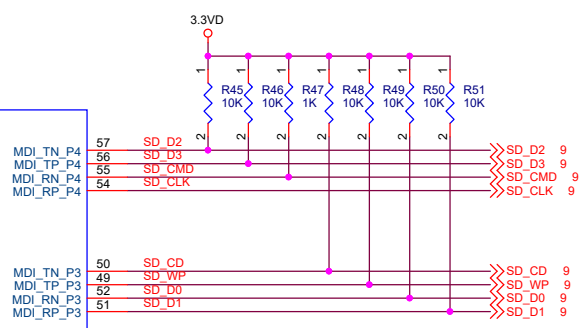
GPIO3  
GPIO2  
GPIO0  
GPIO1  
GPIO37

GPIO44 GPIO35 (KN)

Title		
WIFIAudio A31 MODULE(MT7688AN)		
Size	Document Number	Rev
A4	SPI I2C SDXC GPIO	03

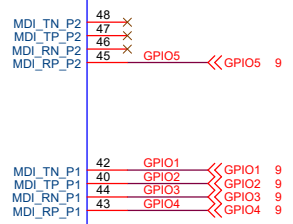


Close to MT7688AN



GPIO29  
GPIO28  
GPIO27  
GPIO26

GPIO23  
GPIO22  
GPIO25  
GPIO24

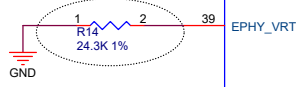


2'b00	2'b01
PWM_CH0	GPIO21 GPIO20 GPIO19 GPIO18

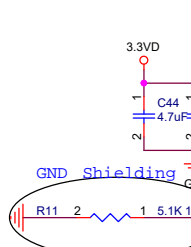
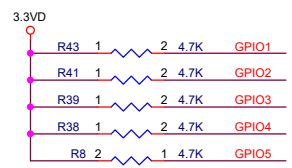
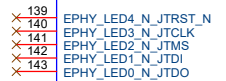


2'b00	2'b01	2'b10	2'b11
SPI5_CLK	GPIO15		PWM_CH1
SPI5_CS	GPIO14		PWM_CH0
SPI5_MOSI	GPIO17		UART_RXD2
SPI5_MISO	GPIO16		UART_TXD2

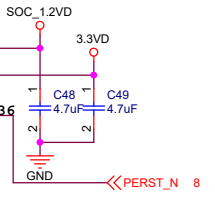
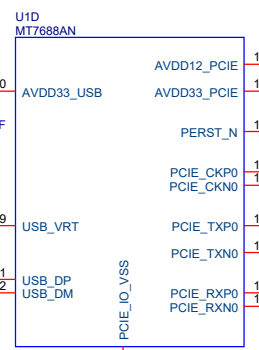
Close to MT7688AN



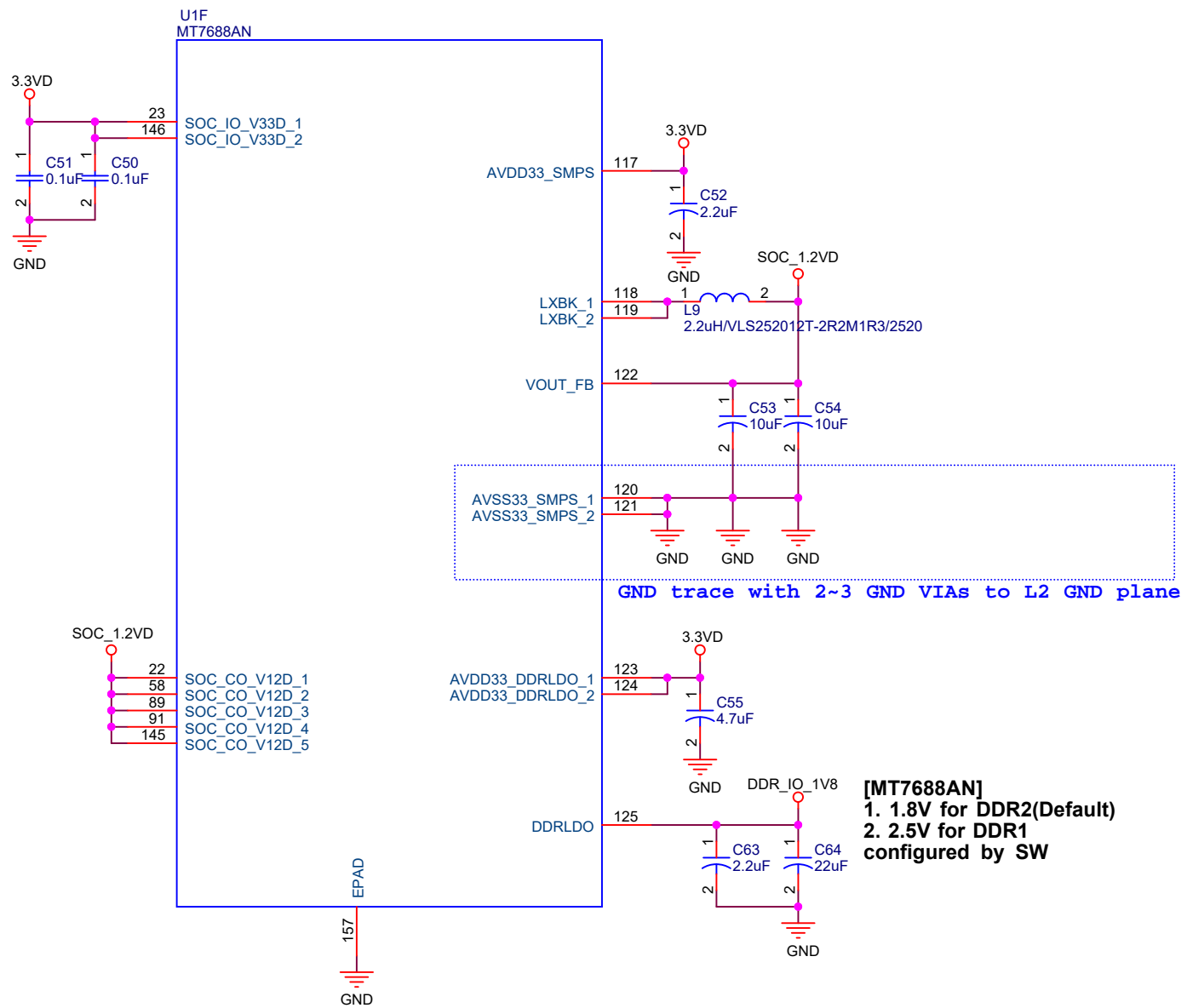
GPIO30 (KN) GPIO39  
GPIO31 (KN) GPIO40  
GPIO32 (KN) GPIO41  
GPIO33 (KN) GPIO42  
GPIO34 (KN) GPIO43



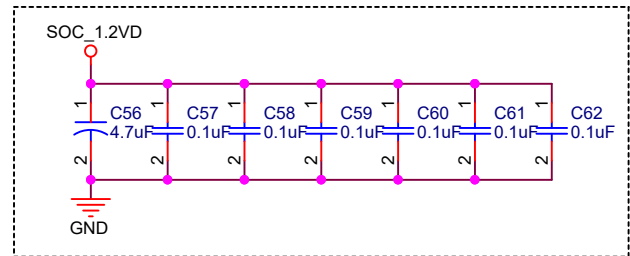
9 USB\_DP  
9 USB\_DM



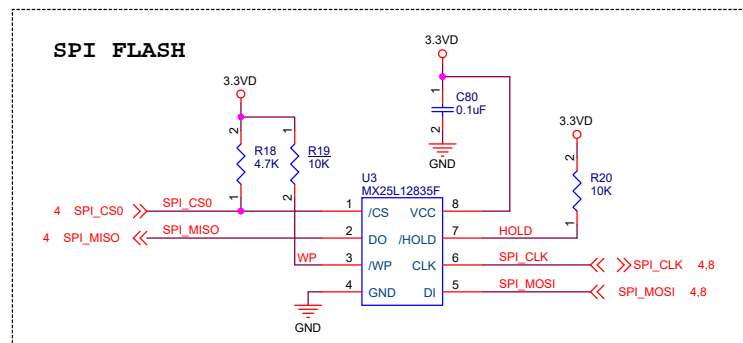
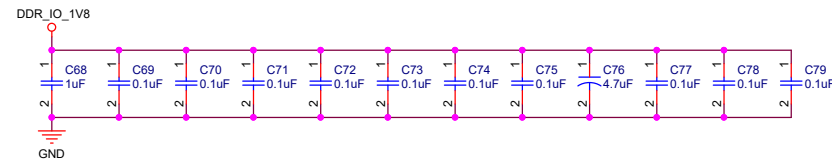
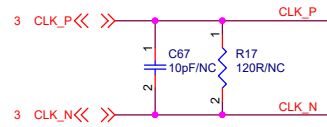
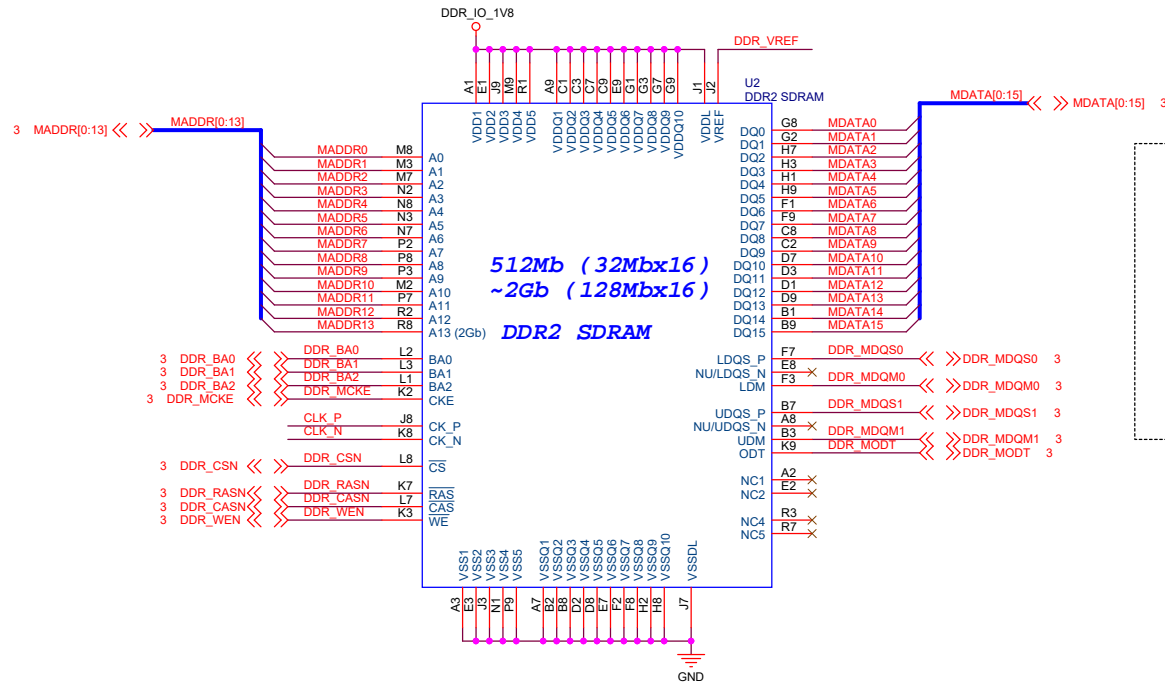
Title		
WiFiAudio A31 MODULE(MT7688AN)		
Size	Document Number	Rev
A3	USB_PCIE_ETH	03



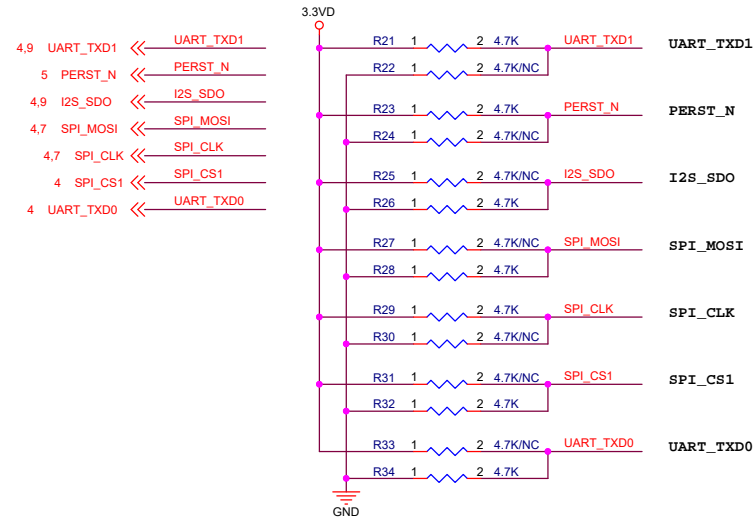
**Close to MT7688AN**



Title		
WIFIAudio A31 MODULE(MT7688AN)		
Size	Document Number	Rev
A4	MT7688AN POWER	03



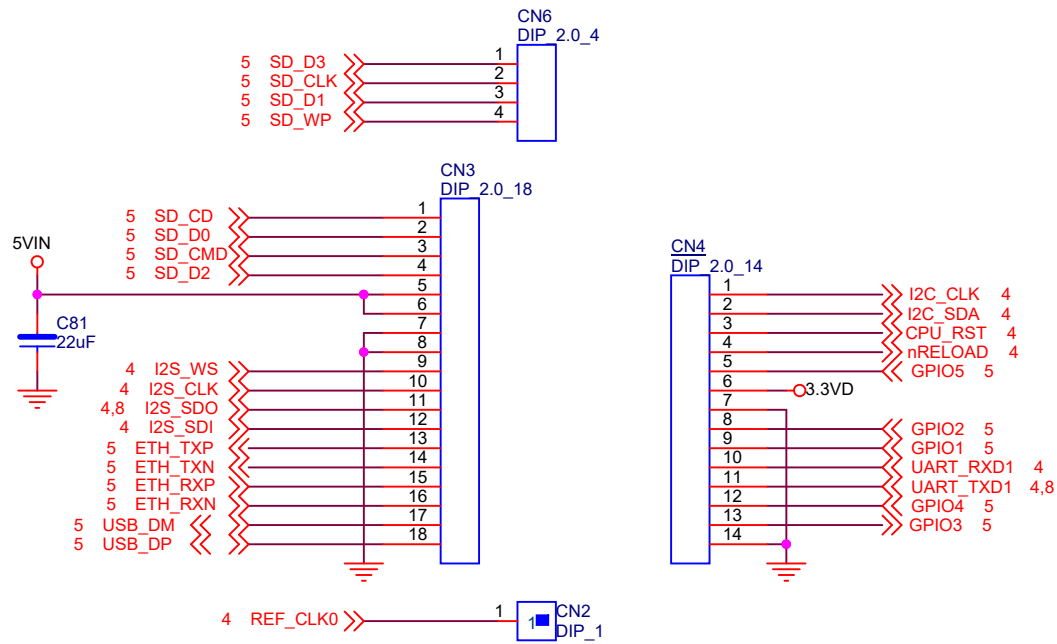
Title		
WiFiAudio A31 MODULE(MT7688AN)		
Size	Document Number	Rev
A3	DDR2 SPI FLASH	03



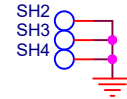
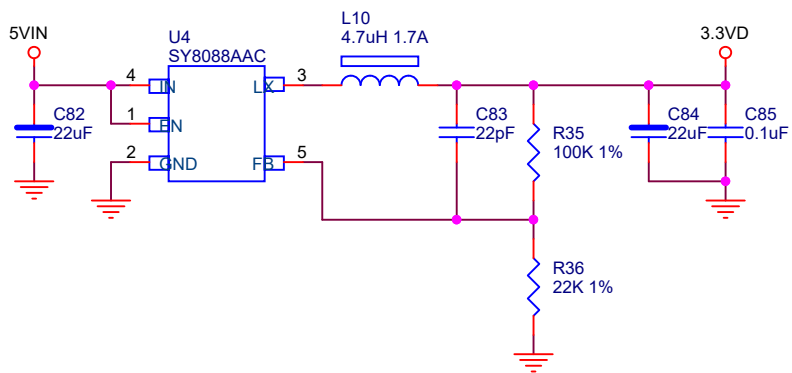
## MT7688AN Bootstrapping Pins Description

Pin Name	Boot Strapping Signal Name	Description
UART_TXD1	DBG_JTAG_MODE	0: JTAG_MODE 1: EPHY_LED (default)
PERST_N	XTAL_FREQ_SEL	0: 25MHz DIP 1: 40MHz SMD
I2S_SDO	DRAM_TYPE	1: DDR1 0: DDR2 [note] This pin is valid for MT7688AN only. It needs to be pull-low for MT7688KN which only supports DDR1.
{SPI_MOSI, SPI_CLK, SPI_CS1 }	CHIP_MODE[2:0]	A vector to set chip function/test/debug modes. In non-test/debug operation, 000: Boot from PLL (boot from SPI 3-Byte Addr) 001: Boot from PLL (boot from SPI 4-Byte Addr) 010: Boot from XTAL (boot from SPI 3-Byte Addr) 011: Boot from XTAL (boot from SPI 4-Byte Addr)
PAD_TXD0	EXT_BGCK	1: TEST_MODE 0: Normal (default)

Title		
WiFiAudio A31 MODULE(MT7688AN)		
Size	Document Number	Rev
A3	BOOTSTRAPPING	03



GPIO5:	PWM_CH0	GPIO18	
GPIO2:	SPIS_CS	GPIO14	PWM_CH0
GPIO1:	SPIS_CLK	GPIO15	PWM_CH1
GPIO4:	SPIS_MISO	GPIO16	UART_TXD2
GPIO3:	SPIS_MOSI	GPIO17	UART_RXD2



Title		
WIFIAudio A31 MODULE(MT7688AN)		
Size	Document Number	Rev
A4	POWER CONNECTOR	03