



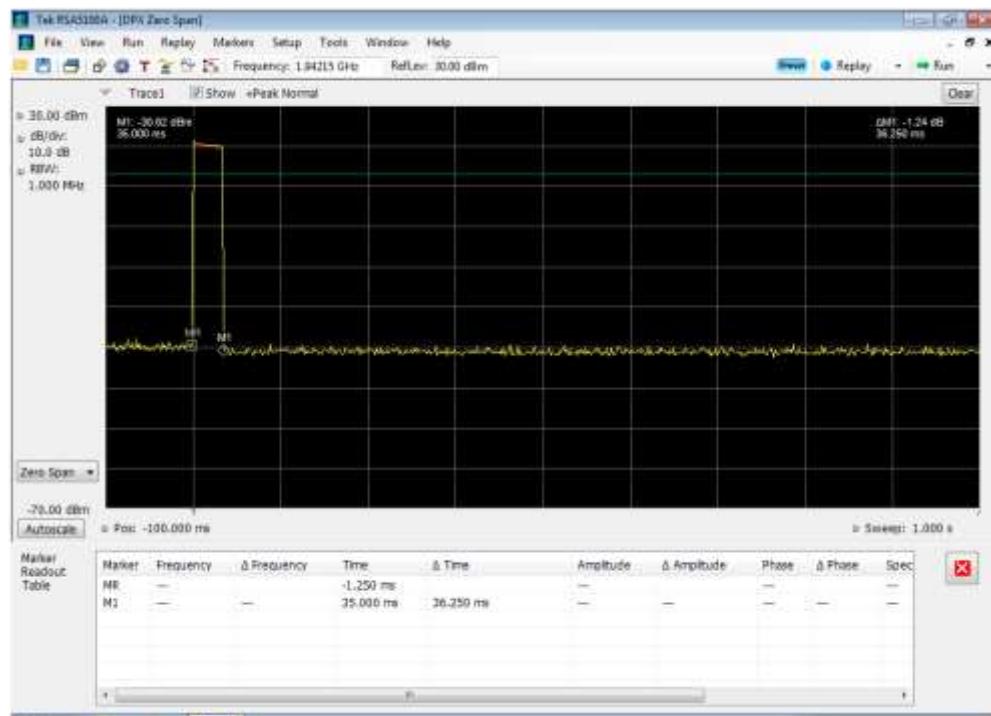
Compliance Testing, LLC
Testing since 1963

Annex I

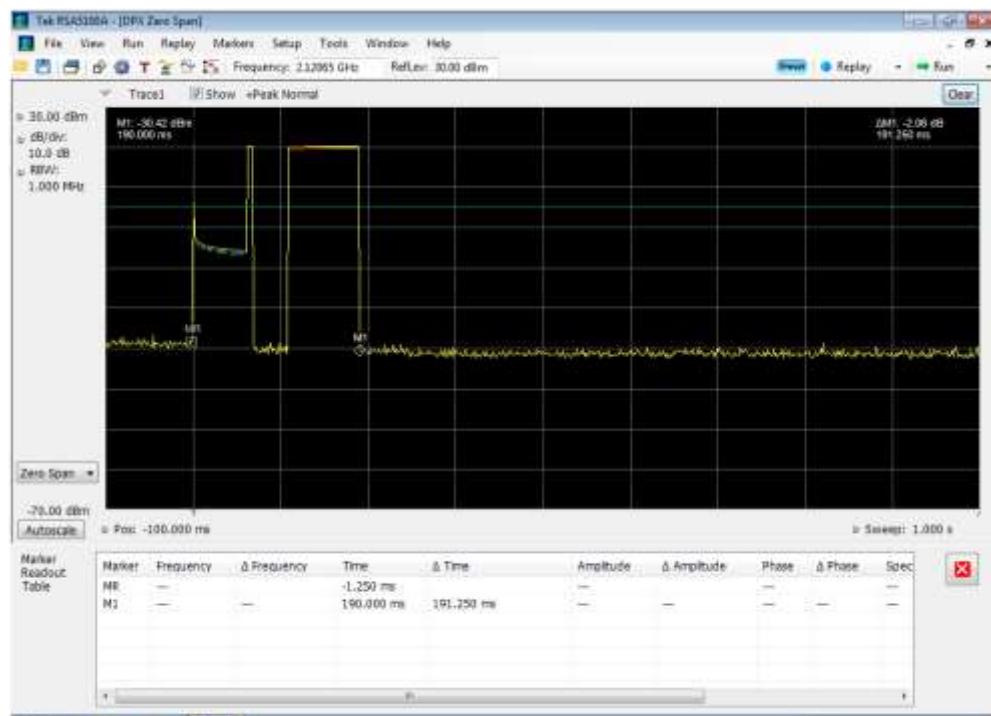
Oscillation Detection



Oscillation Detection Time_Downlink_1930 - 1990 MHz

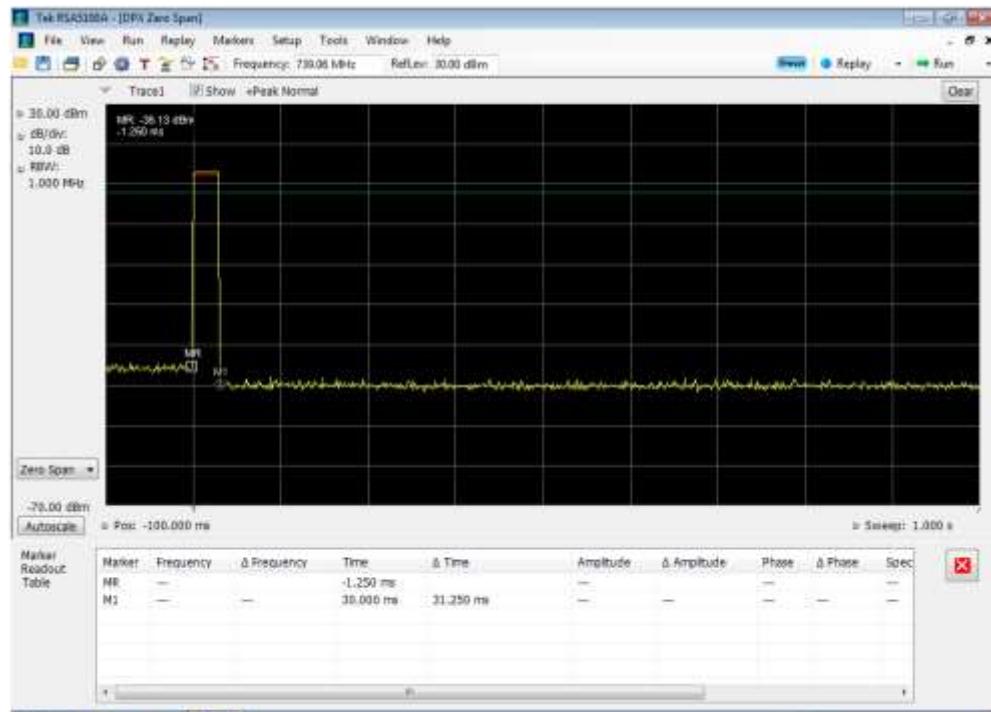


Oscillation Detection Time_Downlink_2110 - 2155 MHz

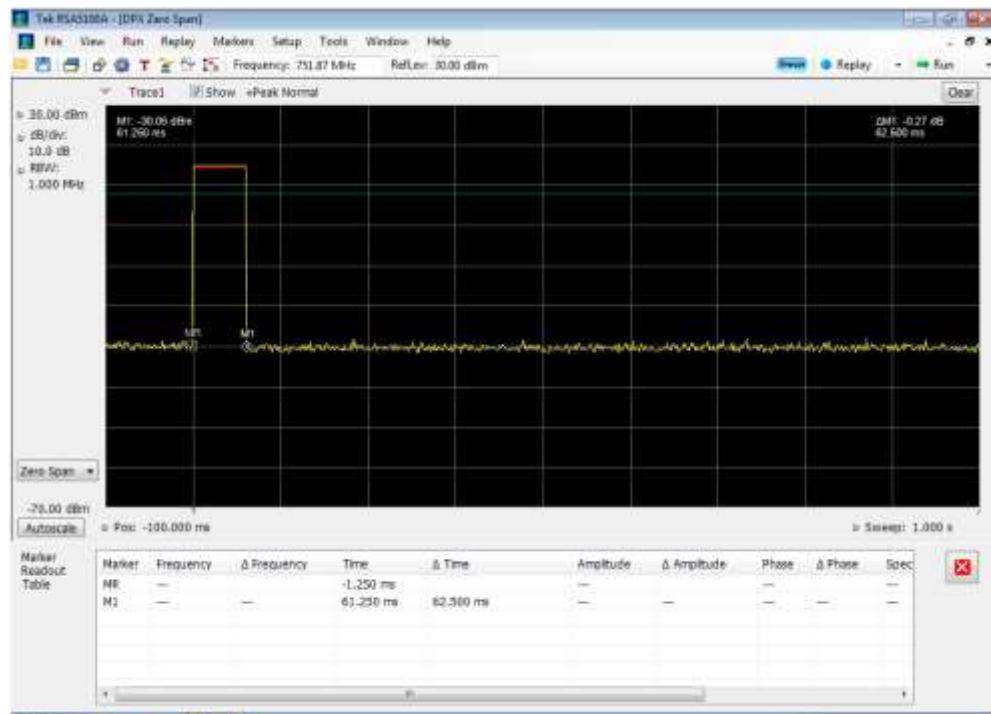




Oscillation Detection Time_Downlink_734 - 746 MHz

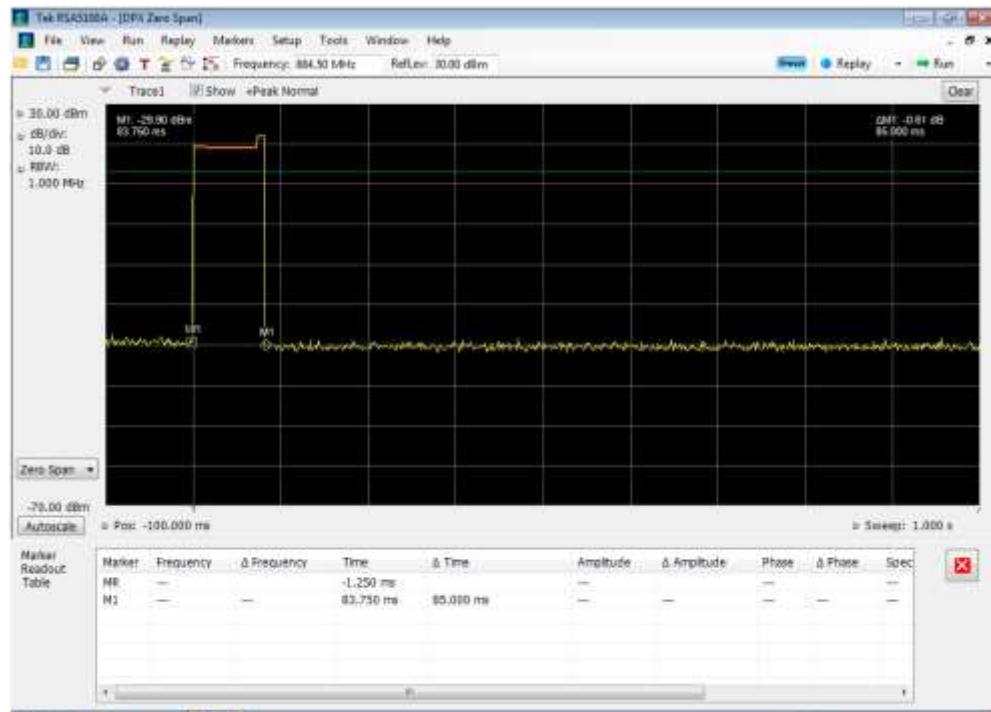


Oscillation Detection Time_Downlink_746 - 757 MHz

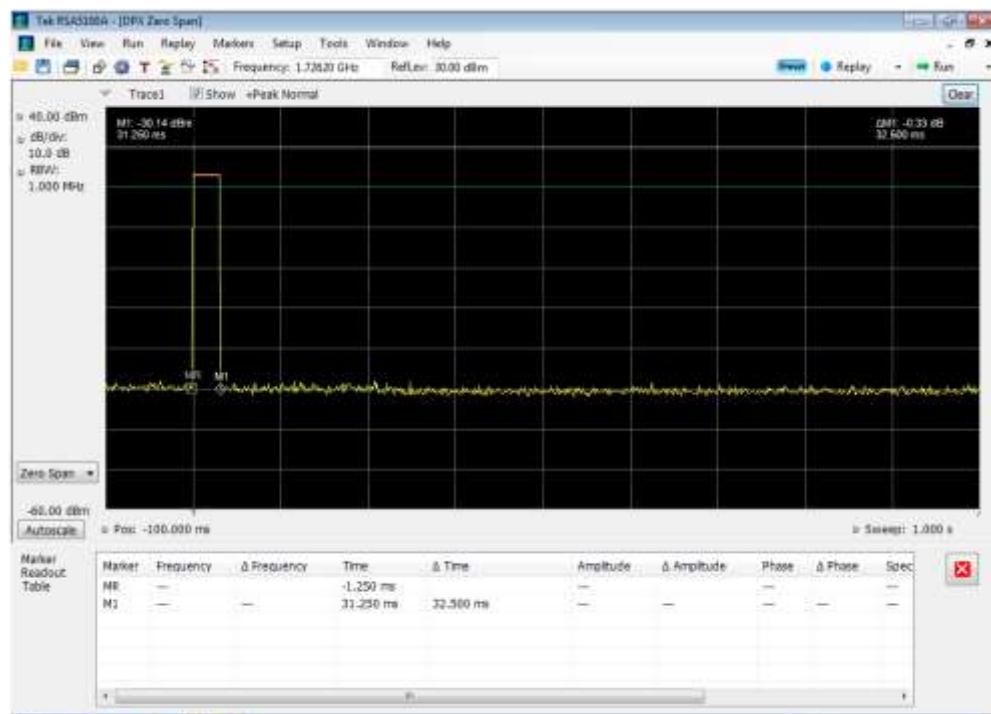




Oscillation Detection Time_Downlink_869 - 894 MHz

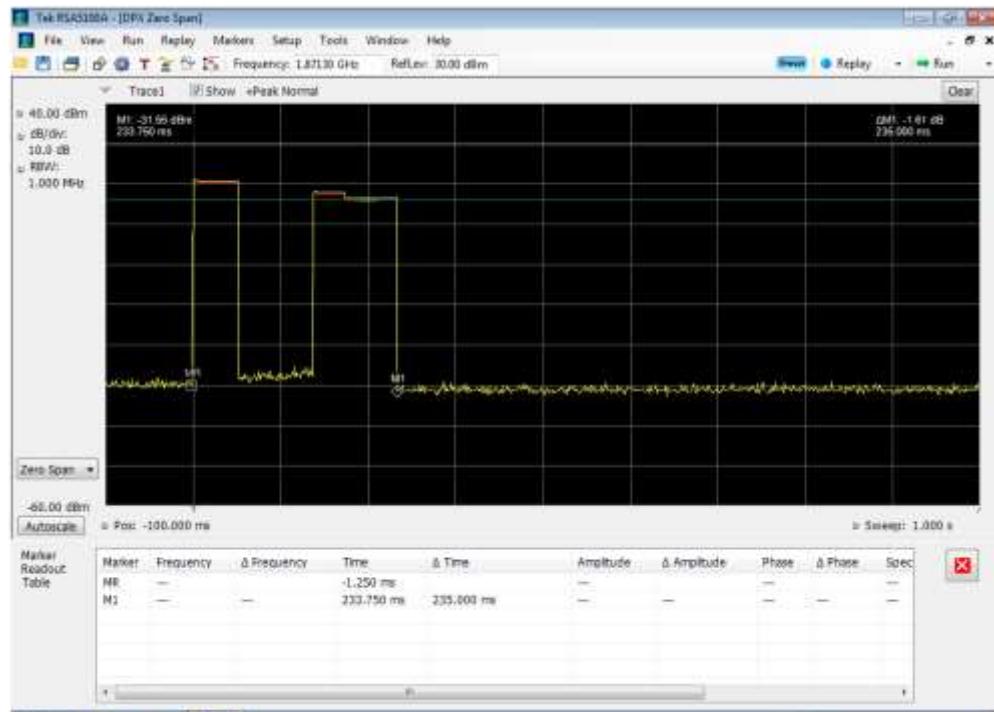


Oscillation Detection Time_Uplink_1710 - 1755 MHz

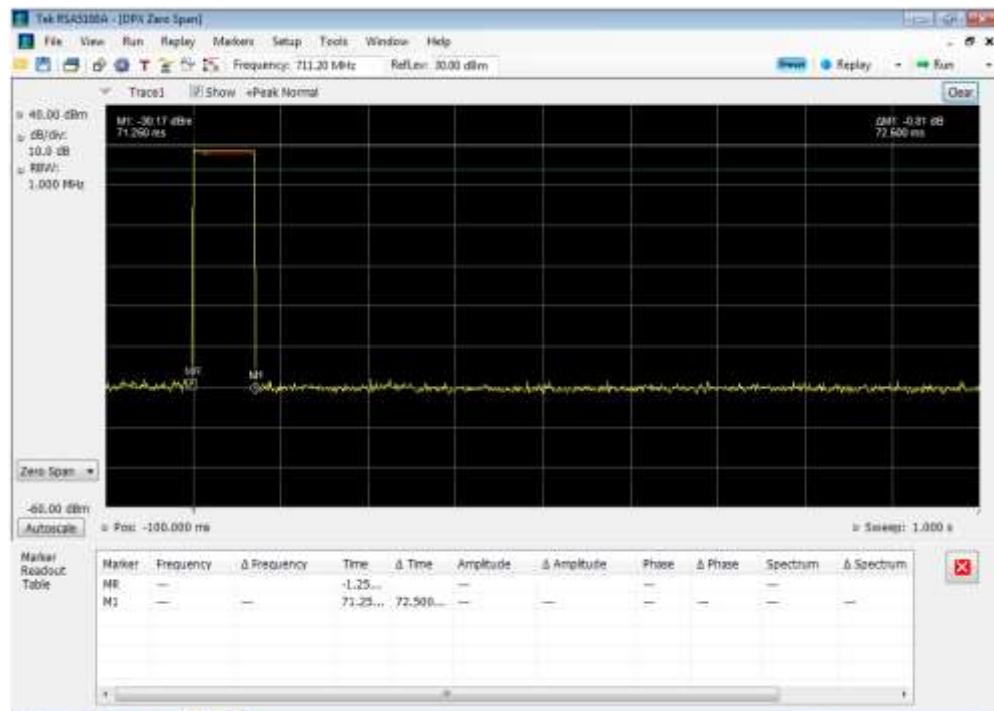




Oscillation Detection Time_Uplink_1850 - 1910 MHz

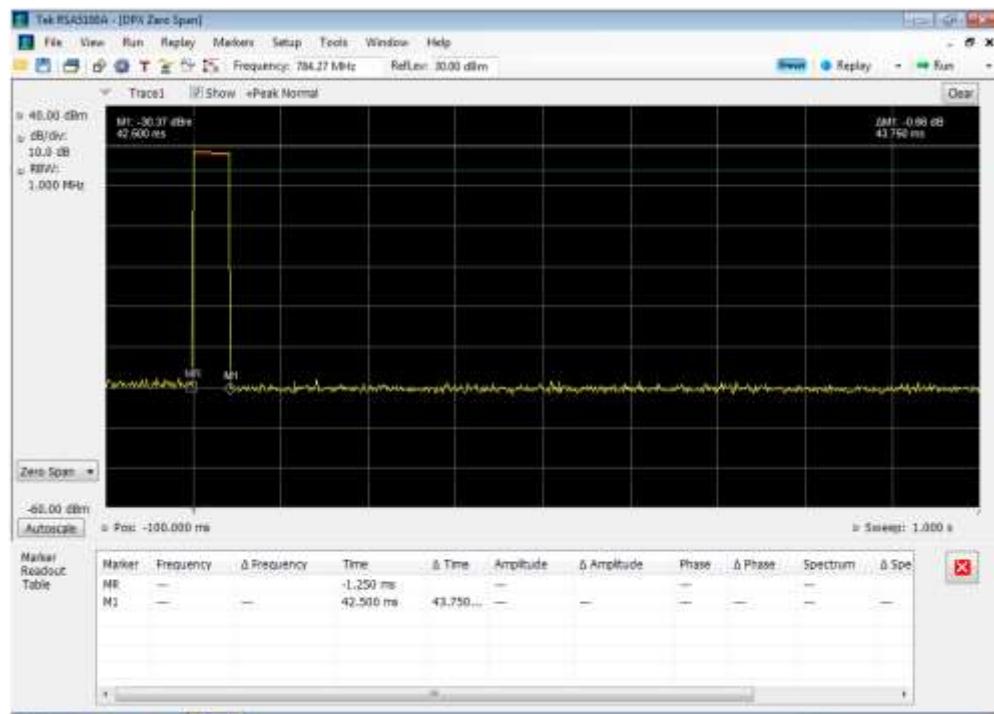


Oscillation Detection Time_Uplink_704 - 716 MHz

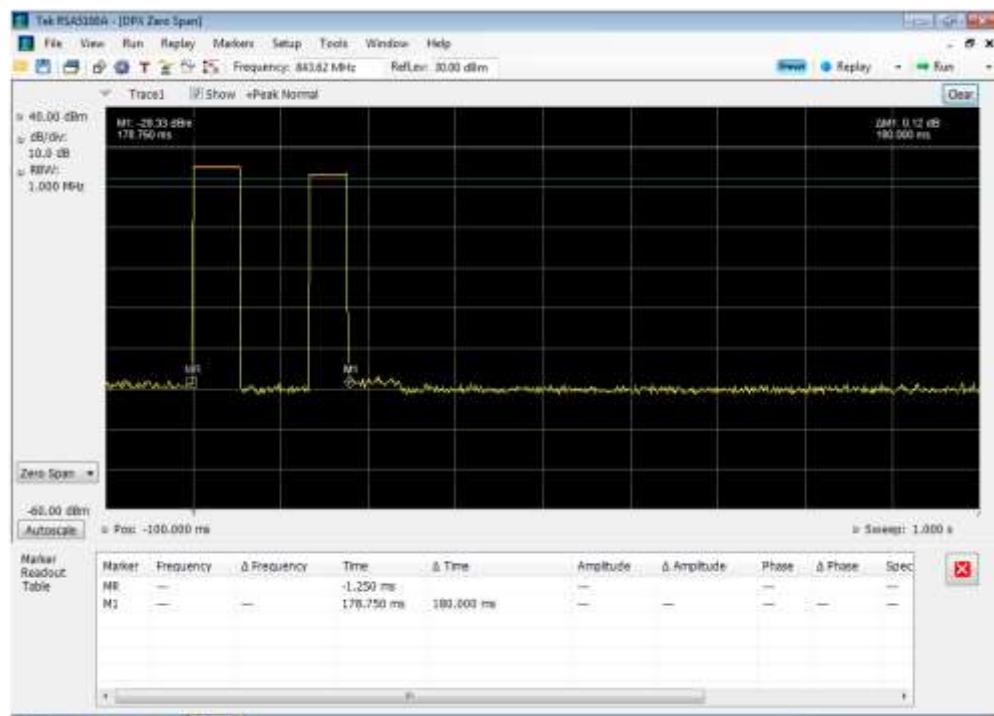




Oscillation Detection Time_Uplink_776 - 787 MHz

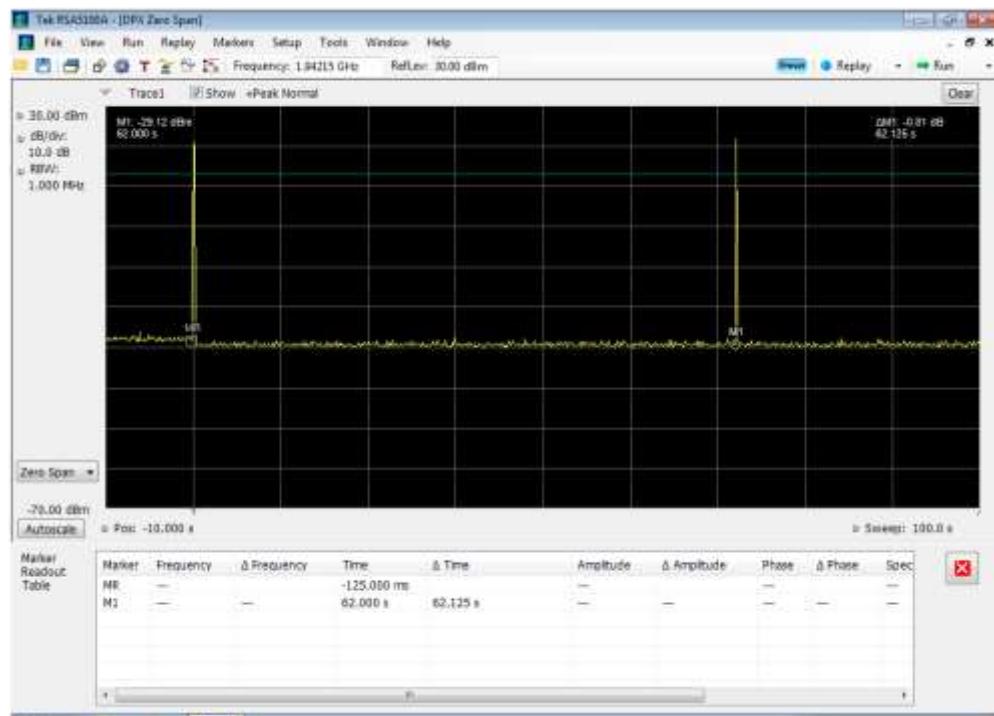


Oscillation Detection Time_Uplink_824 - 849 MHz

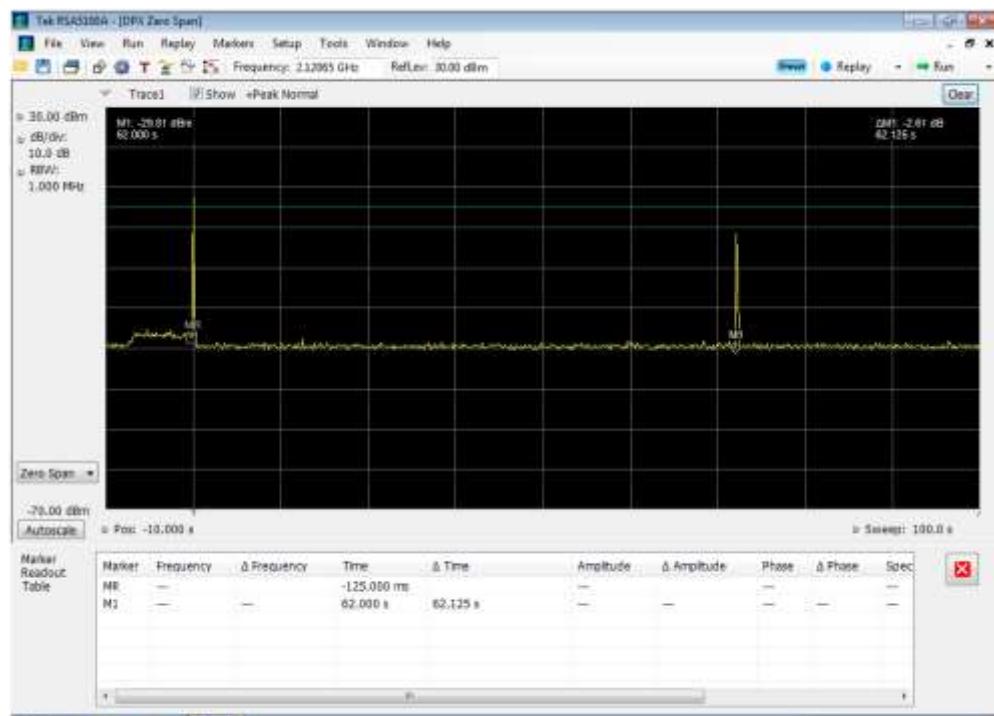




Restart Time_Downlink_1930 - 1990 MHz

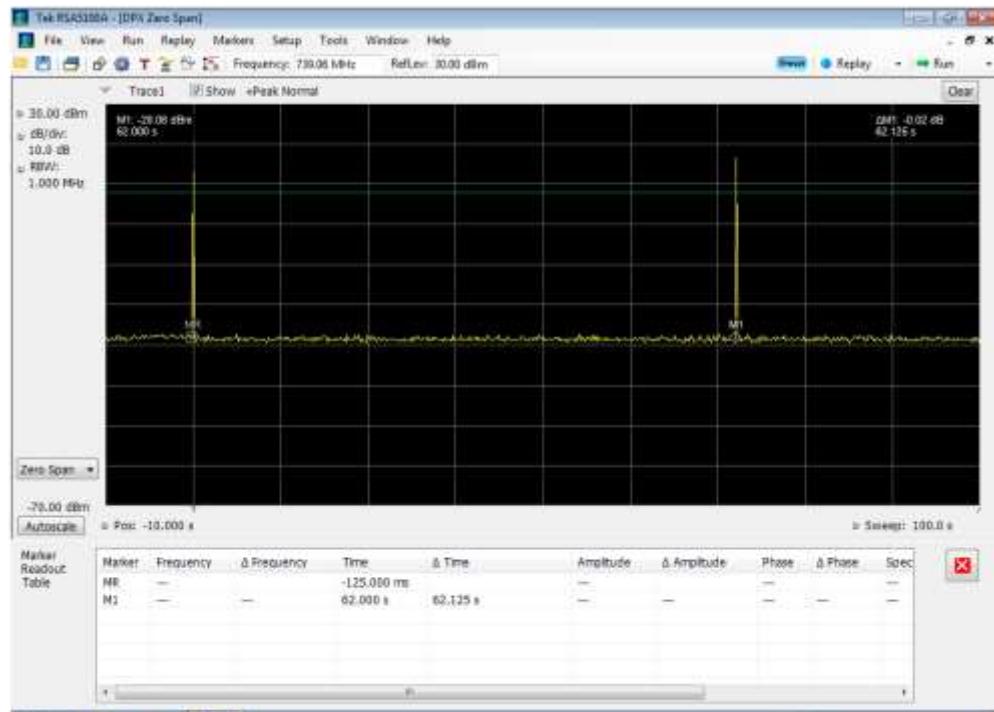


Restart Time_Downlink_2110 - 2155 MHz

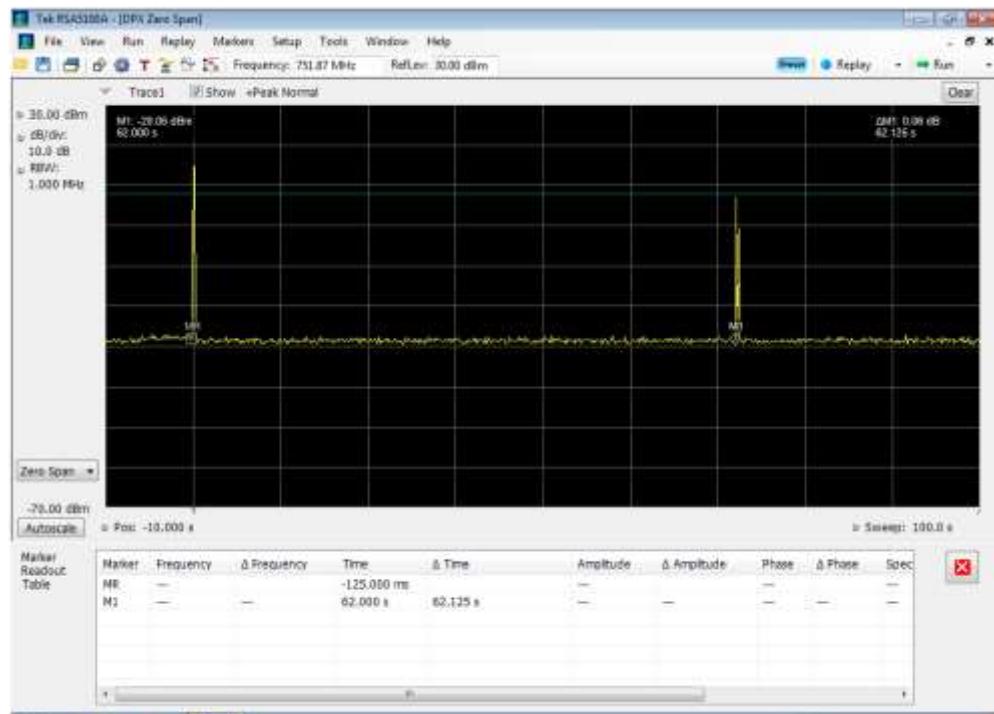




Restart Time_Downlink_734 - 746 MHz

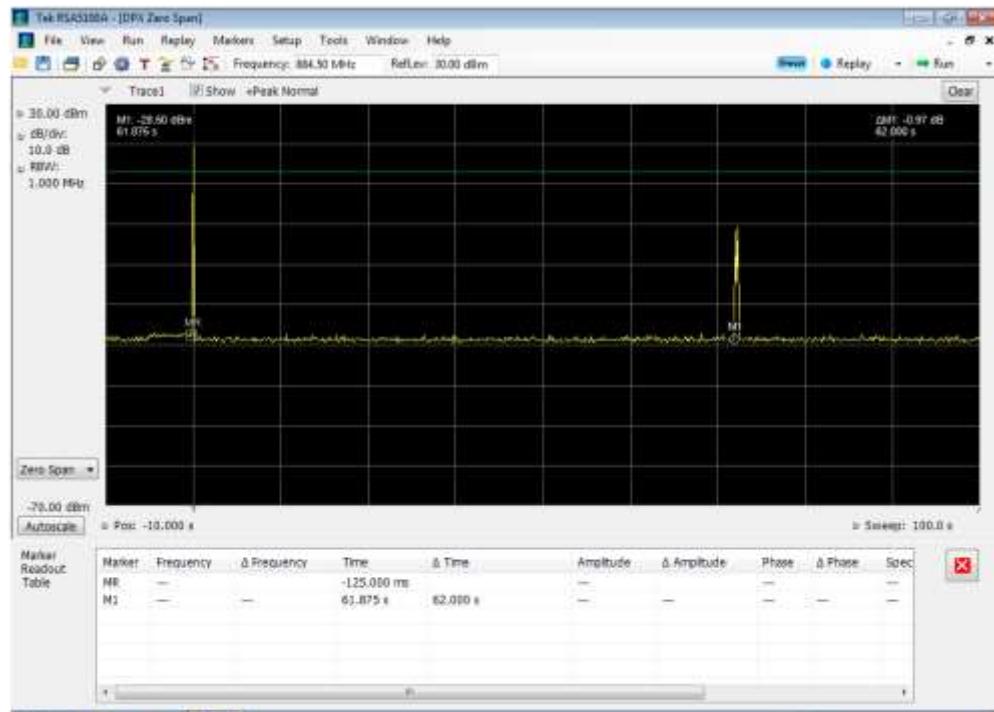


Restart Time_Downlink_746 - 757 MHz

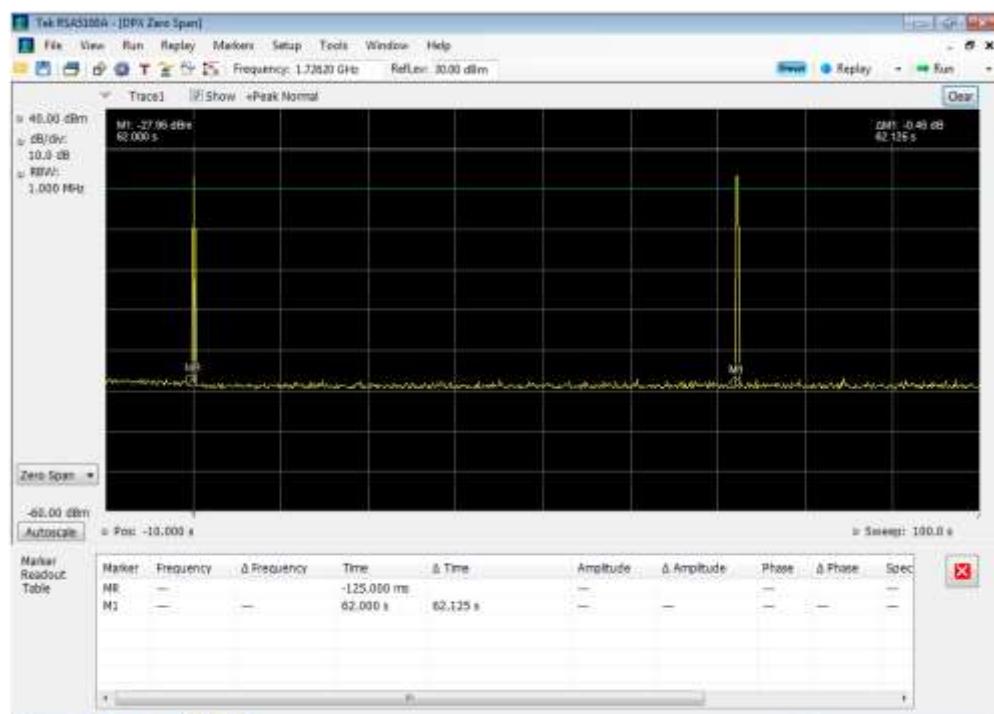




Restart Time_Downlink_869 - 894 MHz

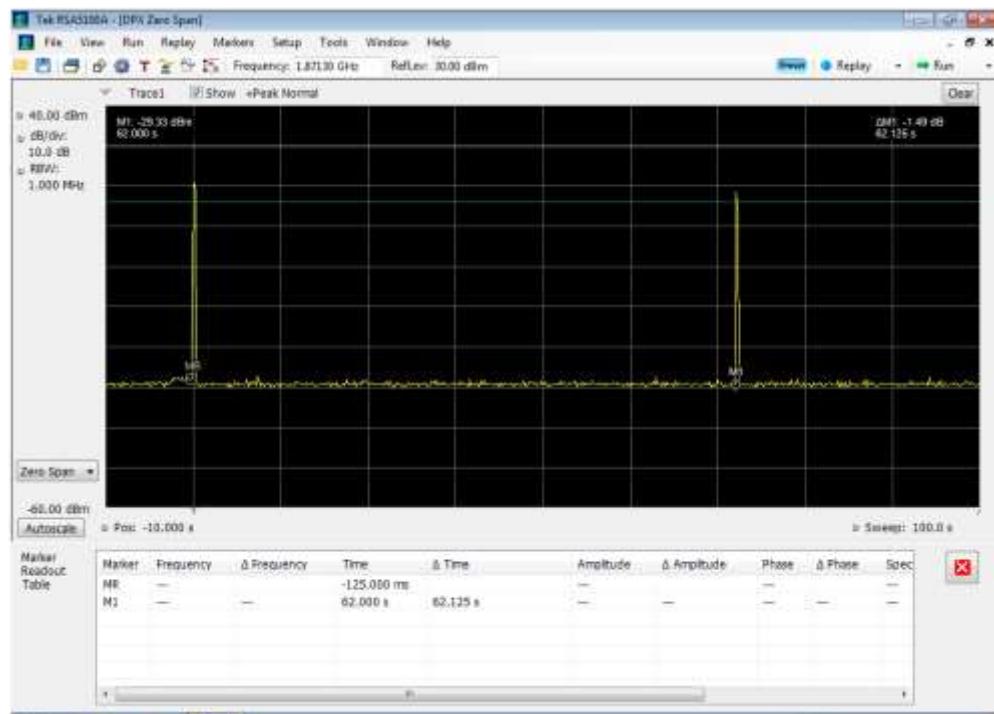


Restart Time_Uplink_1710 - 1755 MHz

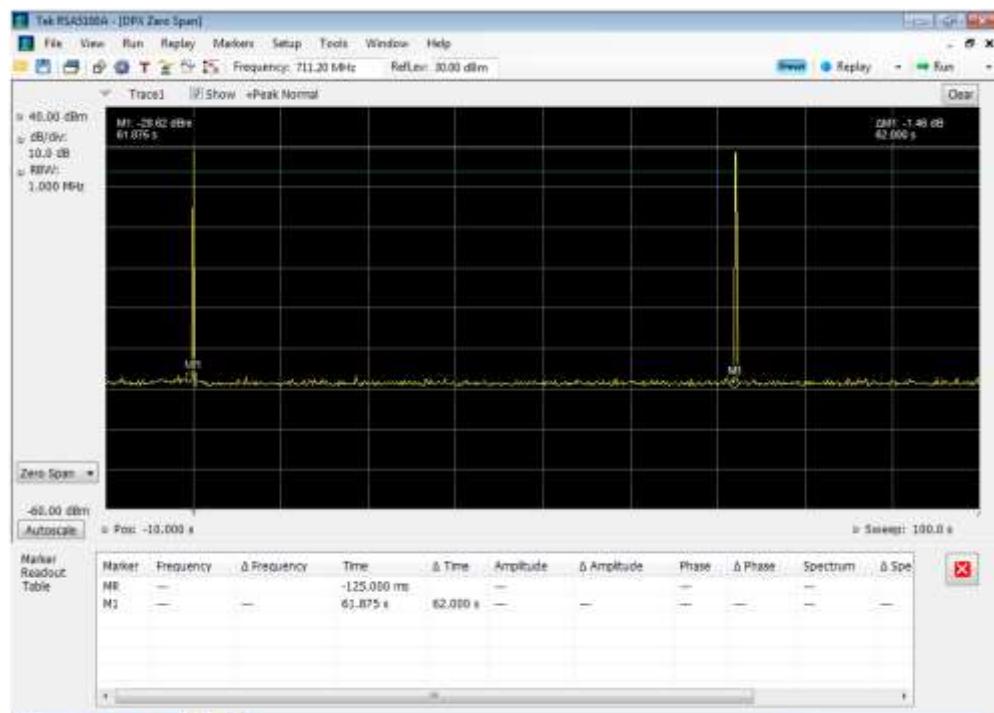




Restart Time_Uplink_1850 - 1910 MHz

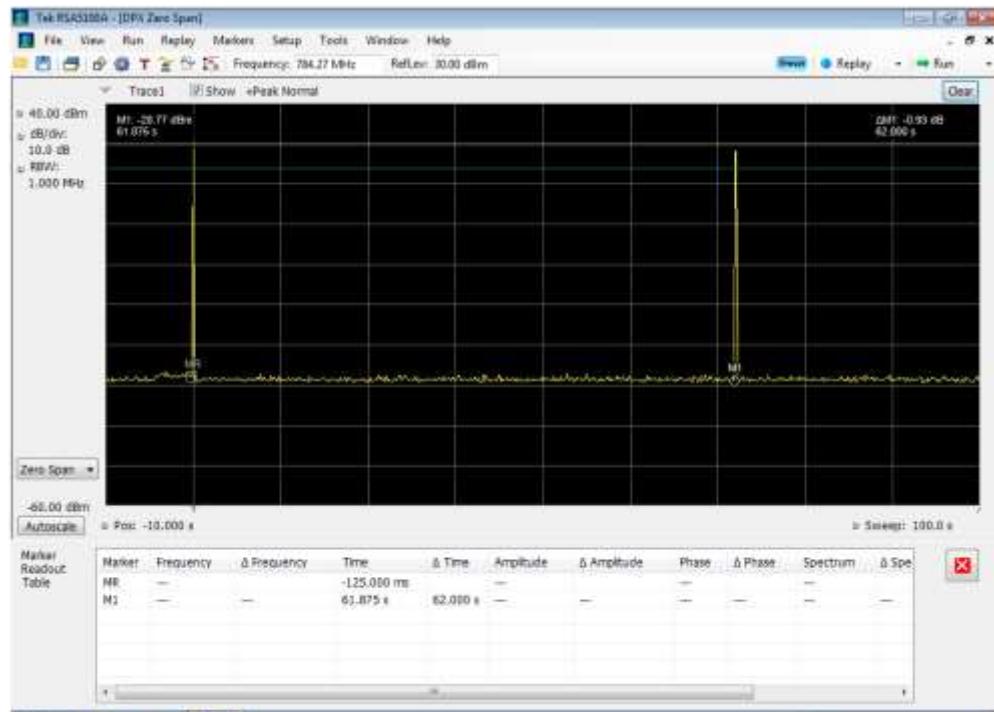


Restart Time_Uplink_704 - 716 MHz

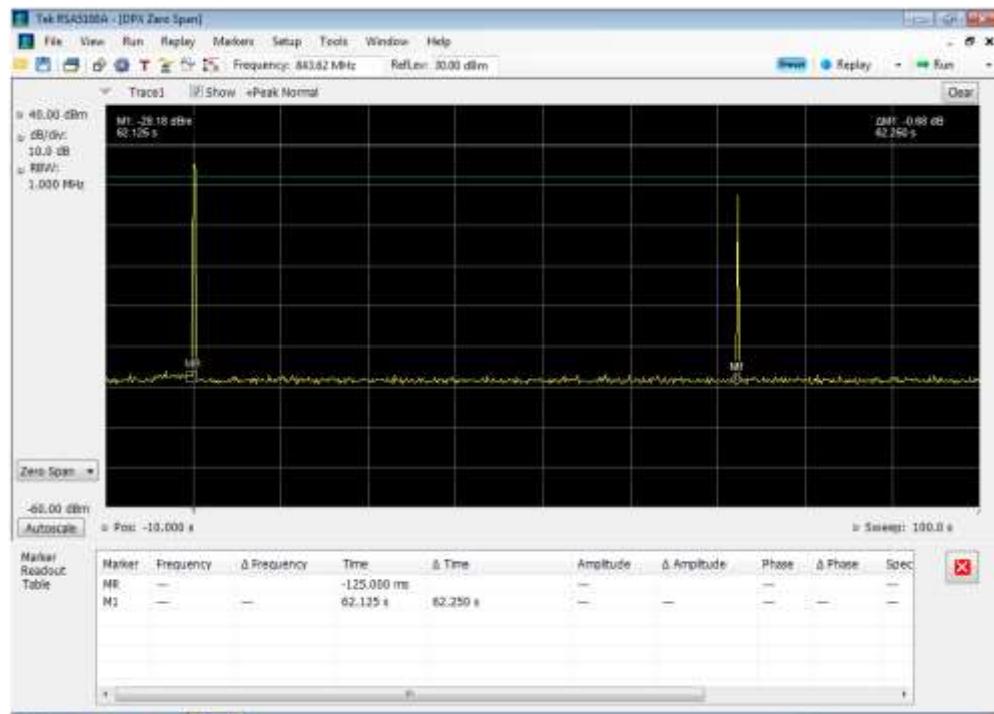




Restart Time_Uplink_776 - 787 MHz

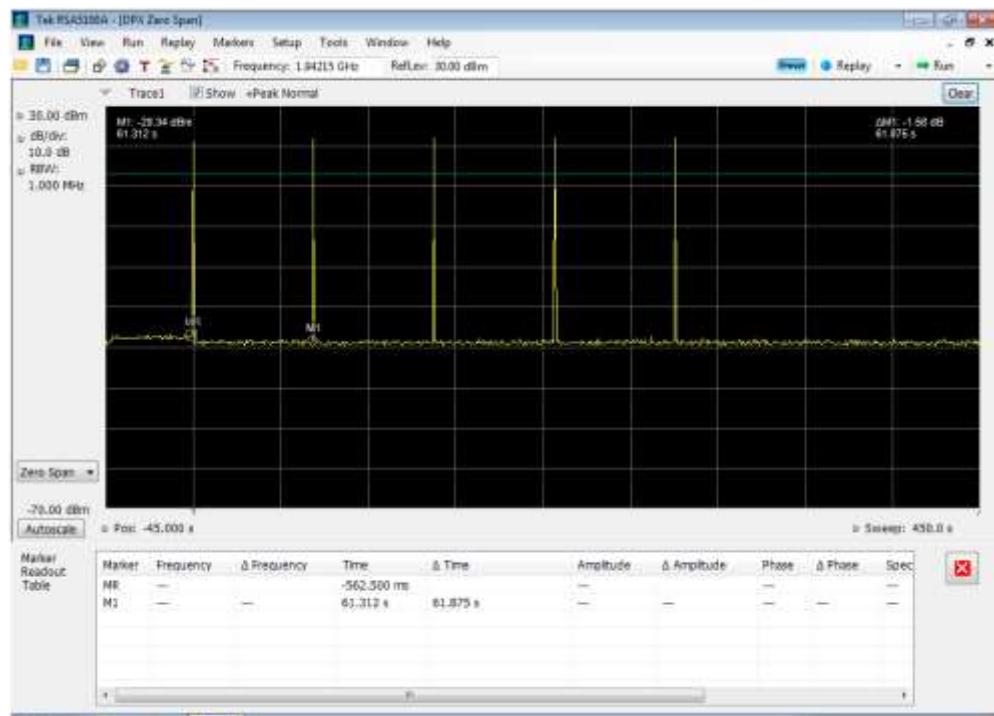


Restart Time_Uplink_824 - 849 MHz

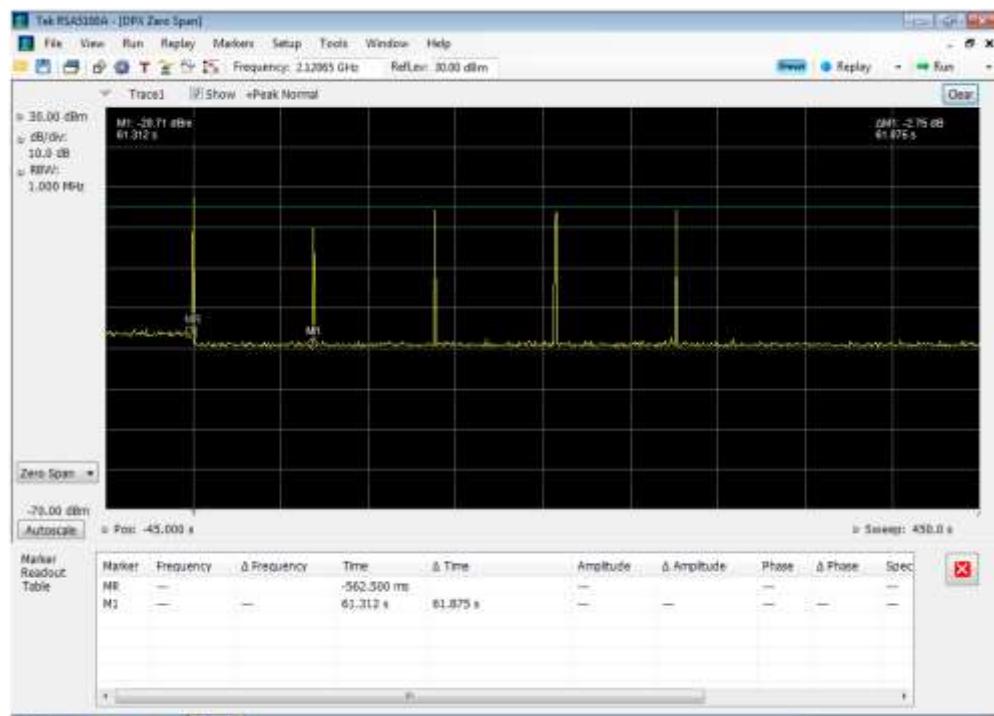




Number of restarts_Downlink_1930 - 1990 MHz

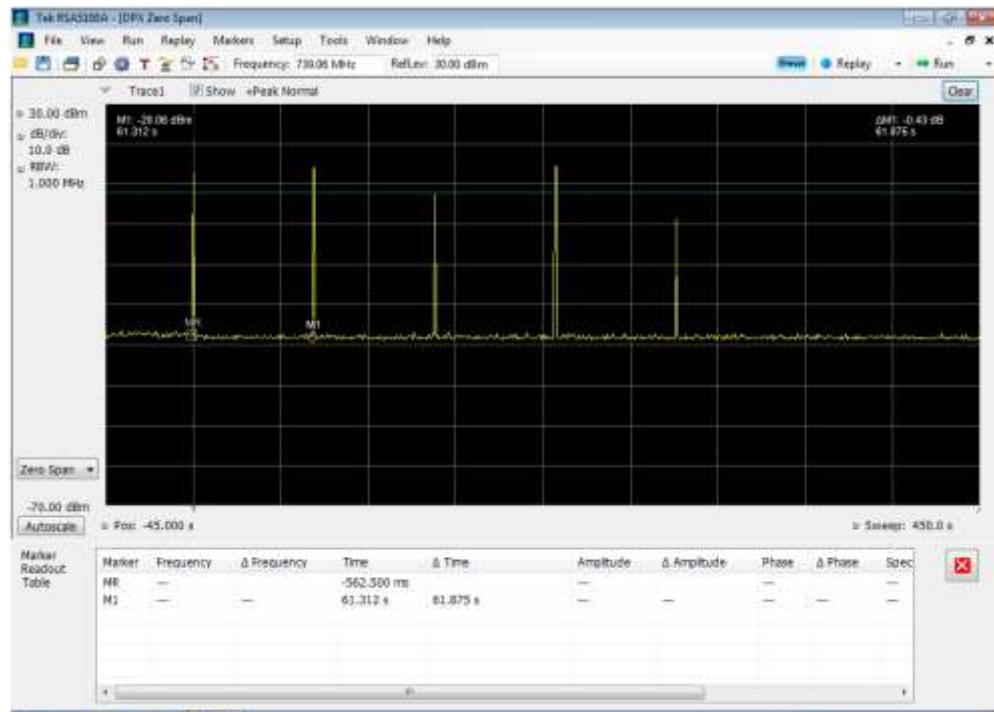


Number of restarts_Downlink_2110 - 2155 MHz

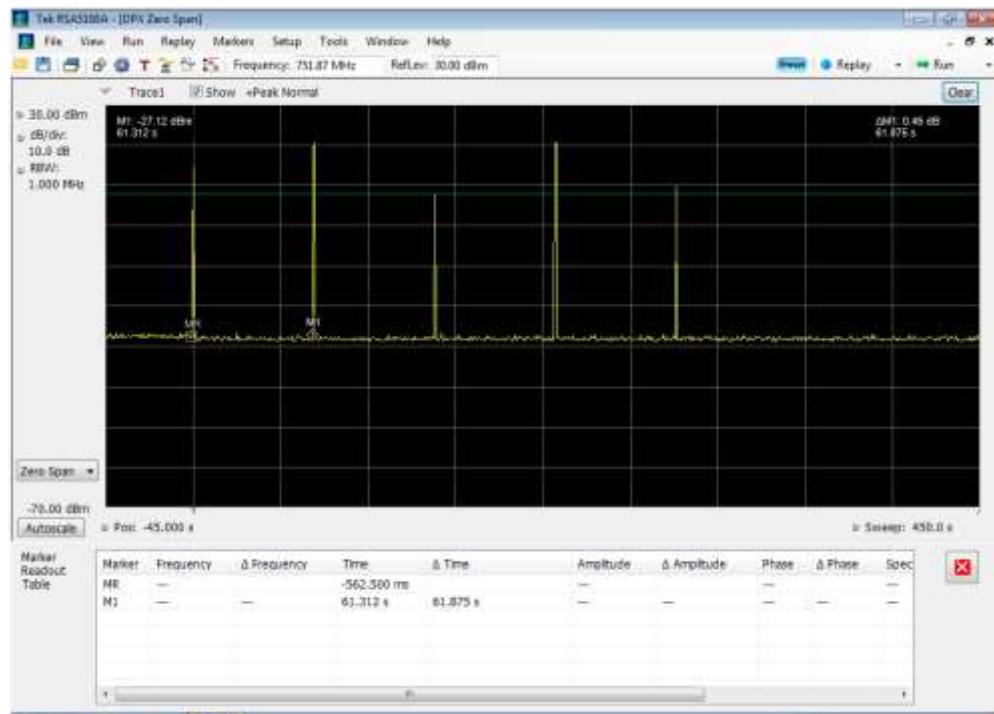




Number of restarts_Downlink_734 - 746 MHz



Number of restarts_Downlink_746 - 757 MHz

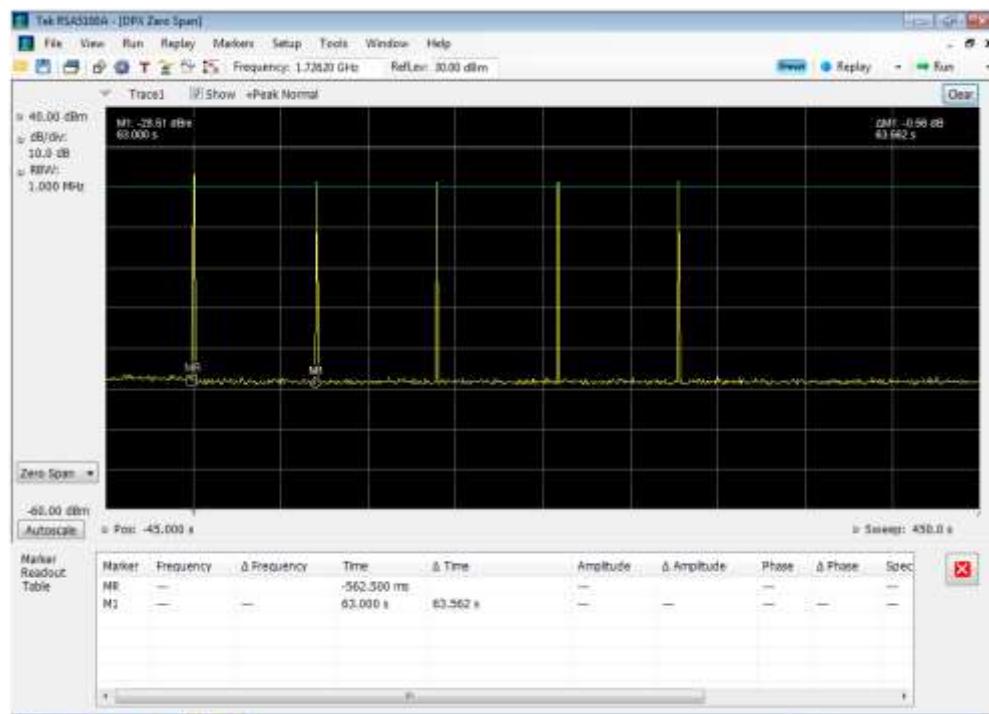




Number of restarts_Downlink_869 - 894 MHz

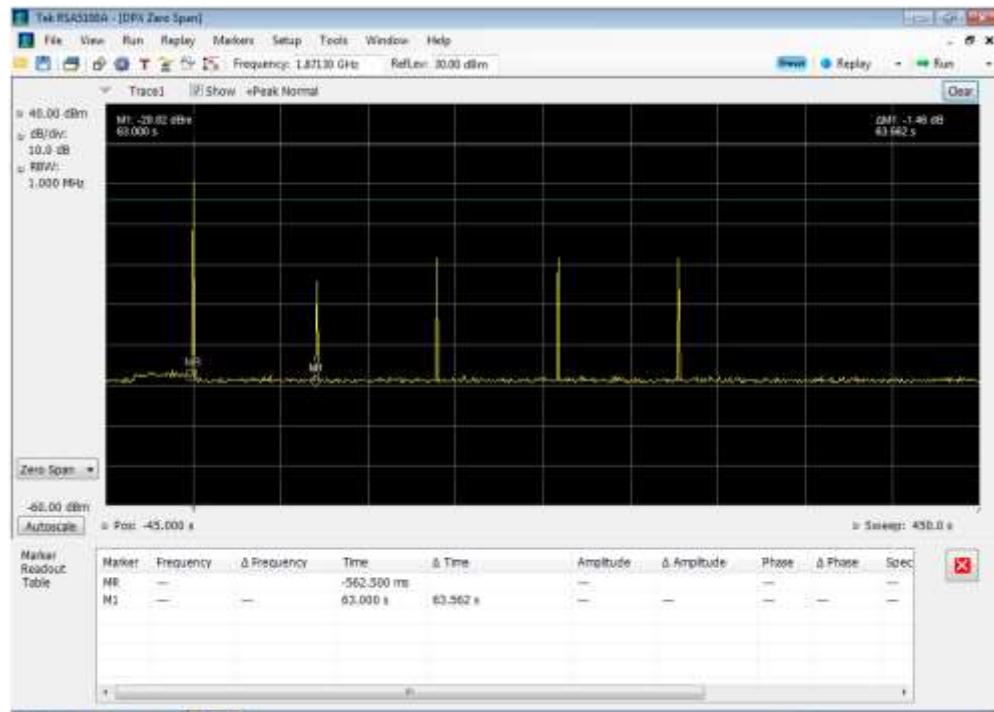


Number of Restarts_Uplink_1710 - 1755 MHz

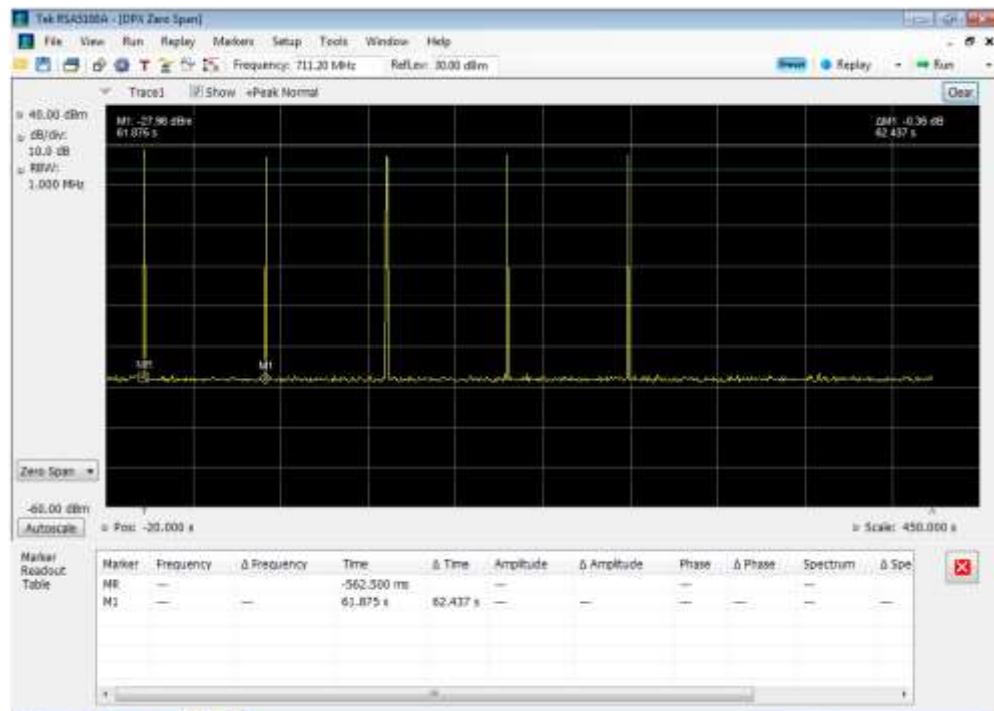




Number of Restarts_Uplink_1850 - 1910 MHz

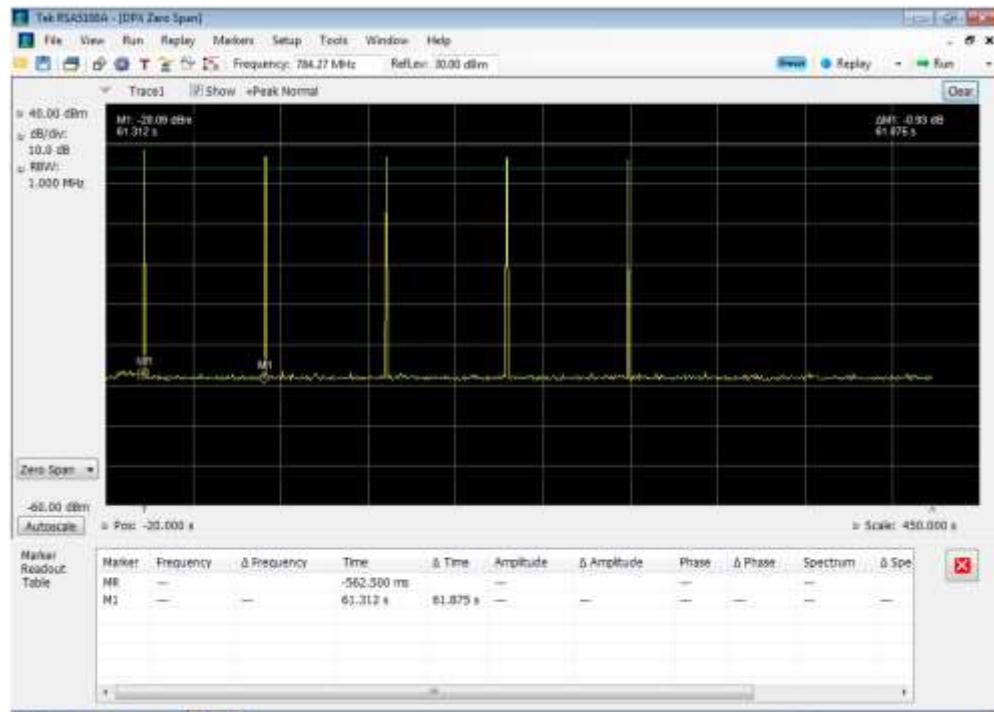


Number of Restarts_Uplink_704 - 716 MHz





Number of Restarts_Uplink_776 - 787 MHz



Number of Restarts_Uplink_824 - 849 MHz

