

TWR-POS-CLRC663

BSX0609-1.a

Société : **NXP**

Suivi par : R.BIENERT

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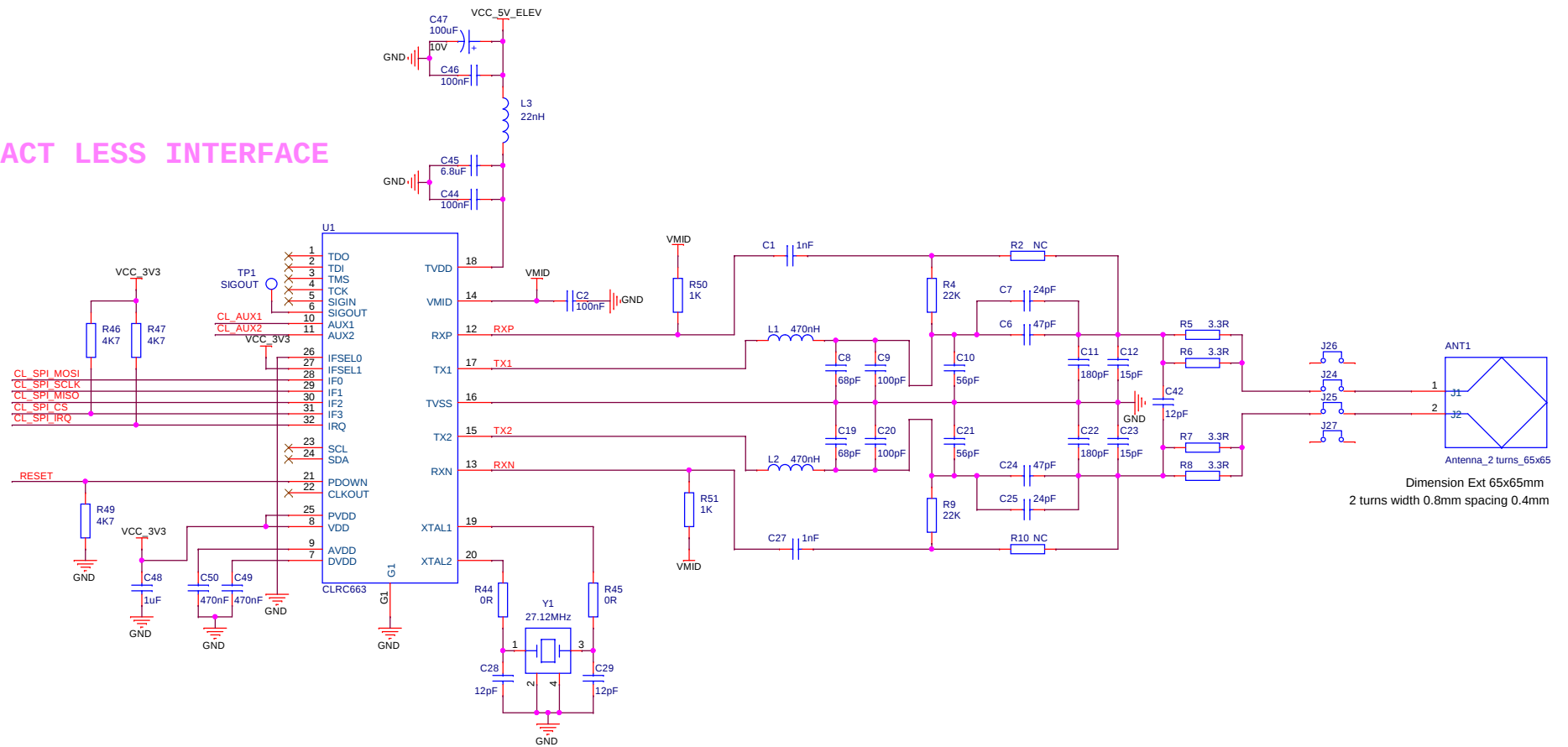
Suivi par : Y.BOUILLET

Tel : 02.31.35.10.09

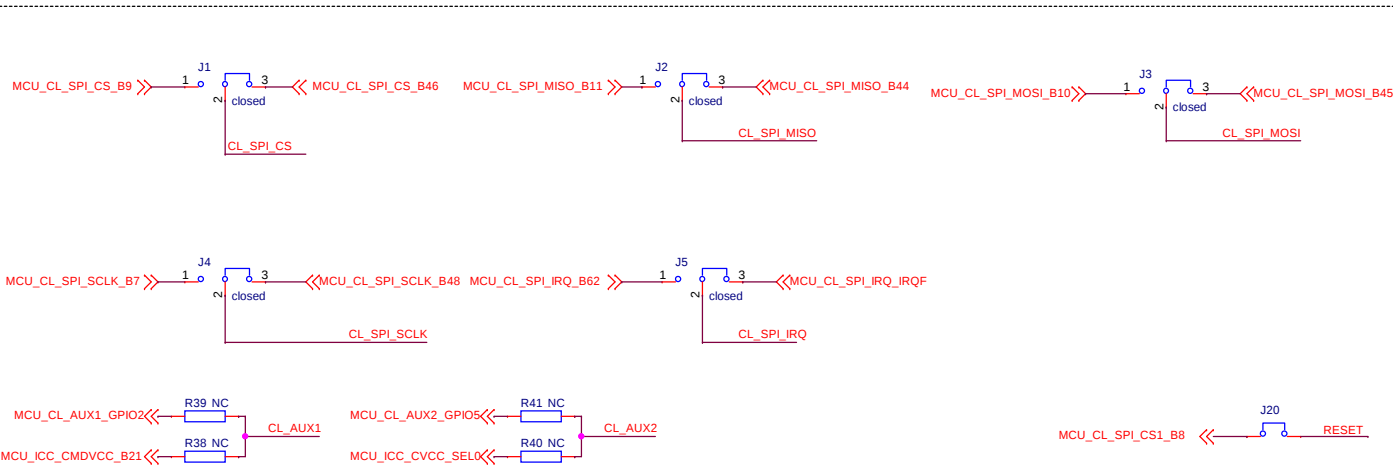
Email : yannick.bouillet@sominex.fr

Réalisé le : lundi 31 juillet 2017

CONTACT LESS INTERFACE



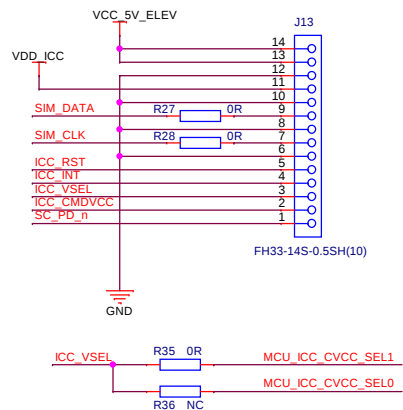
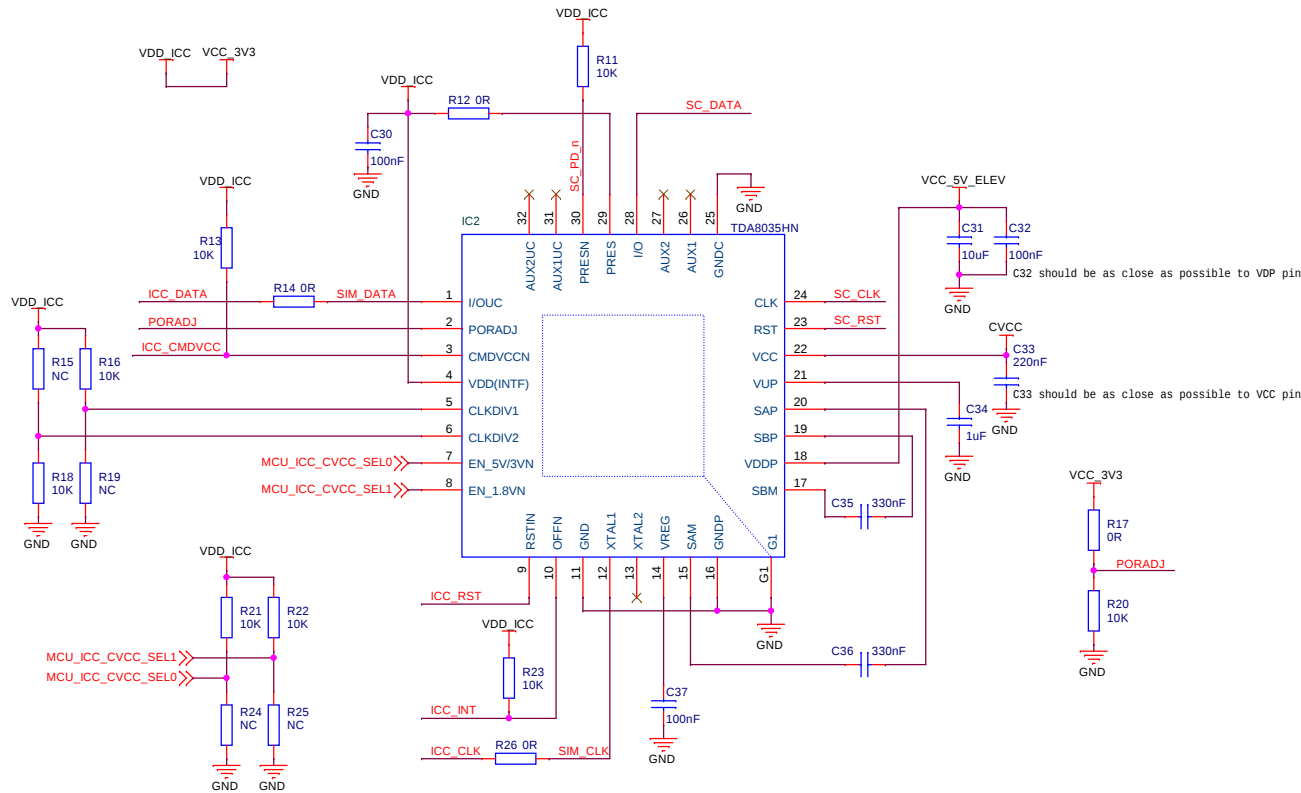
Dimension Ext 65x65mm
2 turns width 0.8mm spacing 0.4mm



JUMPER SETTINGS

		NXP SEMICONDUCTORS 2 Esplanade Anton Philips BP 2000 - COLOMBELLES 14906 CAEN CEDEX 9		 freescale TM semiconductor	
Drawn by : SOMINEX/vrb				Verify by : R.Bienert	
Reference: BSX0609				Projet: TWR-POSCARD	
Size A3	Title: NFC READER				Rev 1.a
Date: Wednesday, August 23, 2017				Sheet 1 of 3	

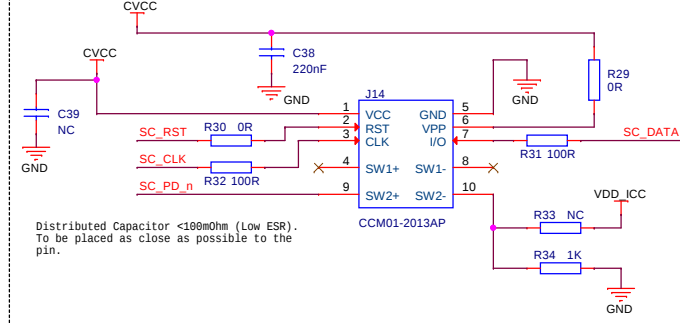
SMART CARD INTERFACE



SMART CARD SOCKET

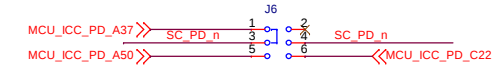
Card Power Supply:

Depending on VSEL lines of the Phy - TDA8035

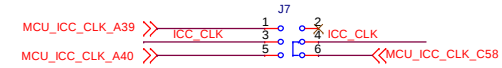


Distributed Capacitor <100mOhm (Low ESR). To be placed as close as possible to the pin.

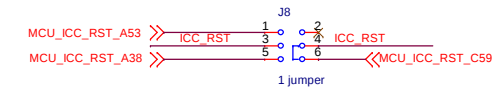
JUMPER SETTING



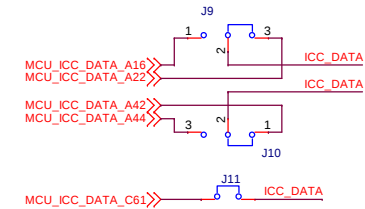
ELEVATOR PD PIN SELECTION



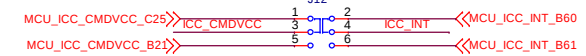
ELEVATOR CLK PIN SELECTION



ELEVATOR RST PIN SELECTION



ELEVATOR DATA PIN SELECTION



Note: Single Connector is used due to space constraint, If space allows we can make it two separate connectors after placement.

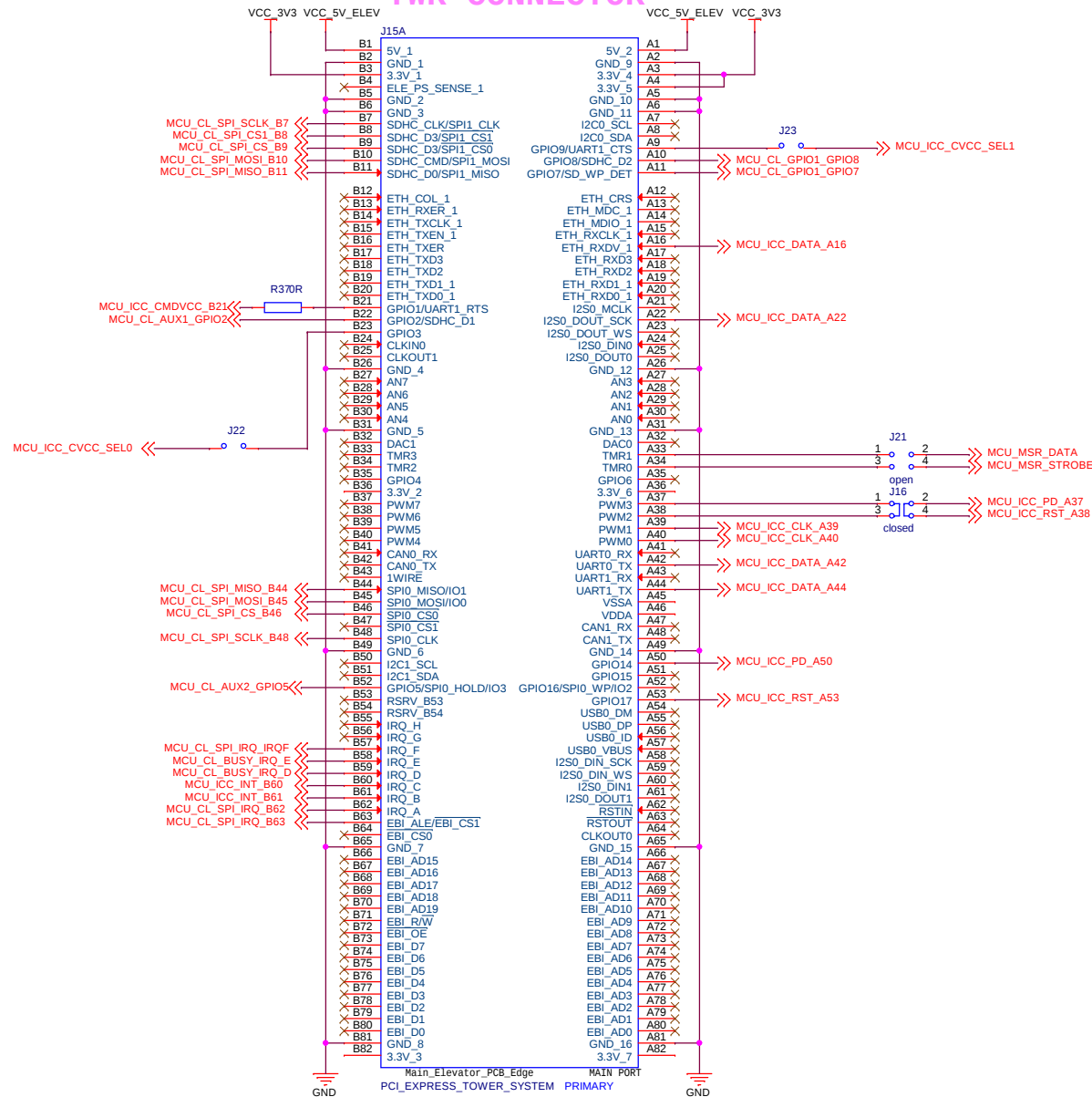
ELEVATOR IRQ PIN SELECTION

Jumper table

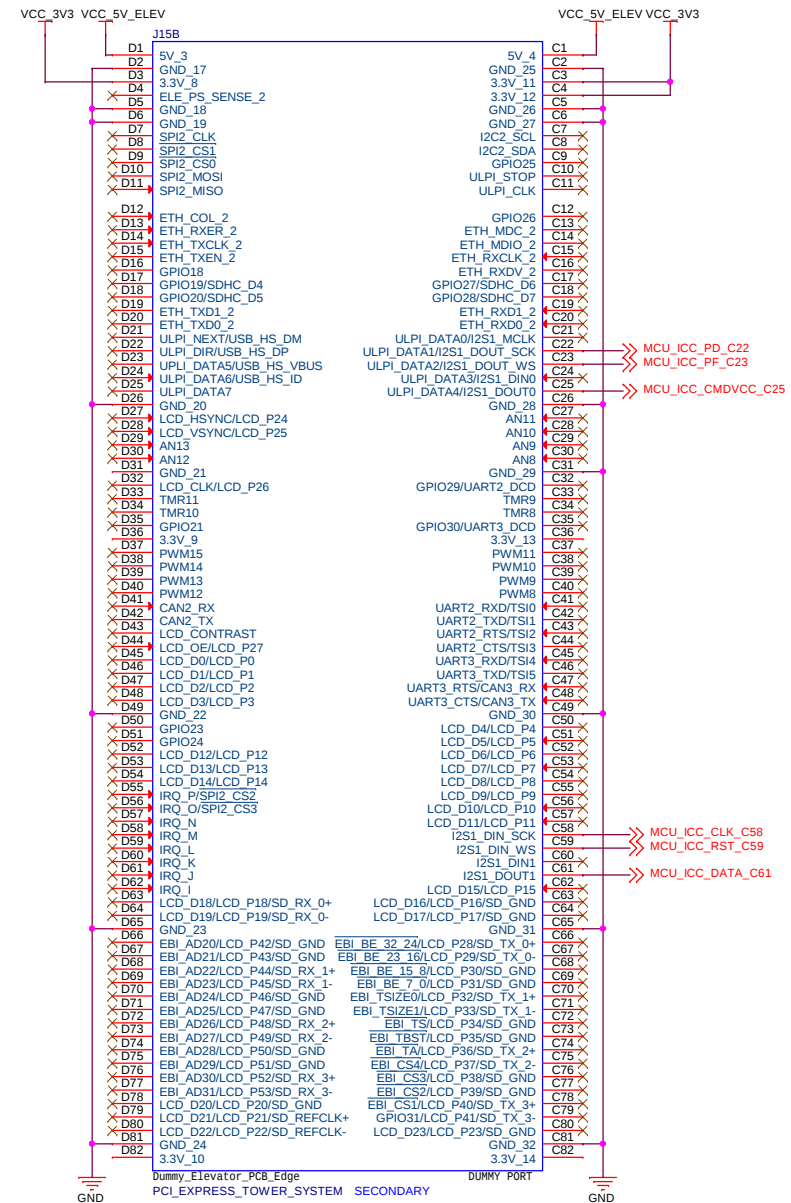
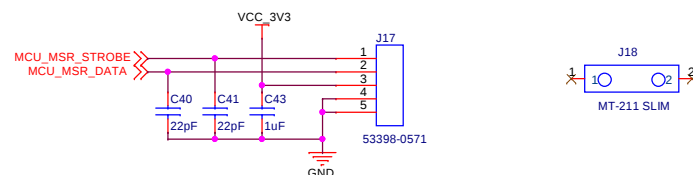
HDR	JUMPER SETTINGS
J1	1-3 or 3-5 or 4-6
J2	1-3 or 3-5 or 4-6
J3	1-3 or 3-5 or 4-6
J7	(1-3 or 3-5) and (2-4 or 4-6)

NXP SEMICONDUCTORS 2 Esplanade Anton Philips BP 2000 - COLOMBELLES 14906 CAEN CEDEX 9		Verity by: R.Bienert	
Drawn by: SOMINEX/yl		Reference: BSX0609	
Size A3		Title: SMART CARD READER	
Date: Wednesday, August 23, 2017		Sheet 2 of 3	

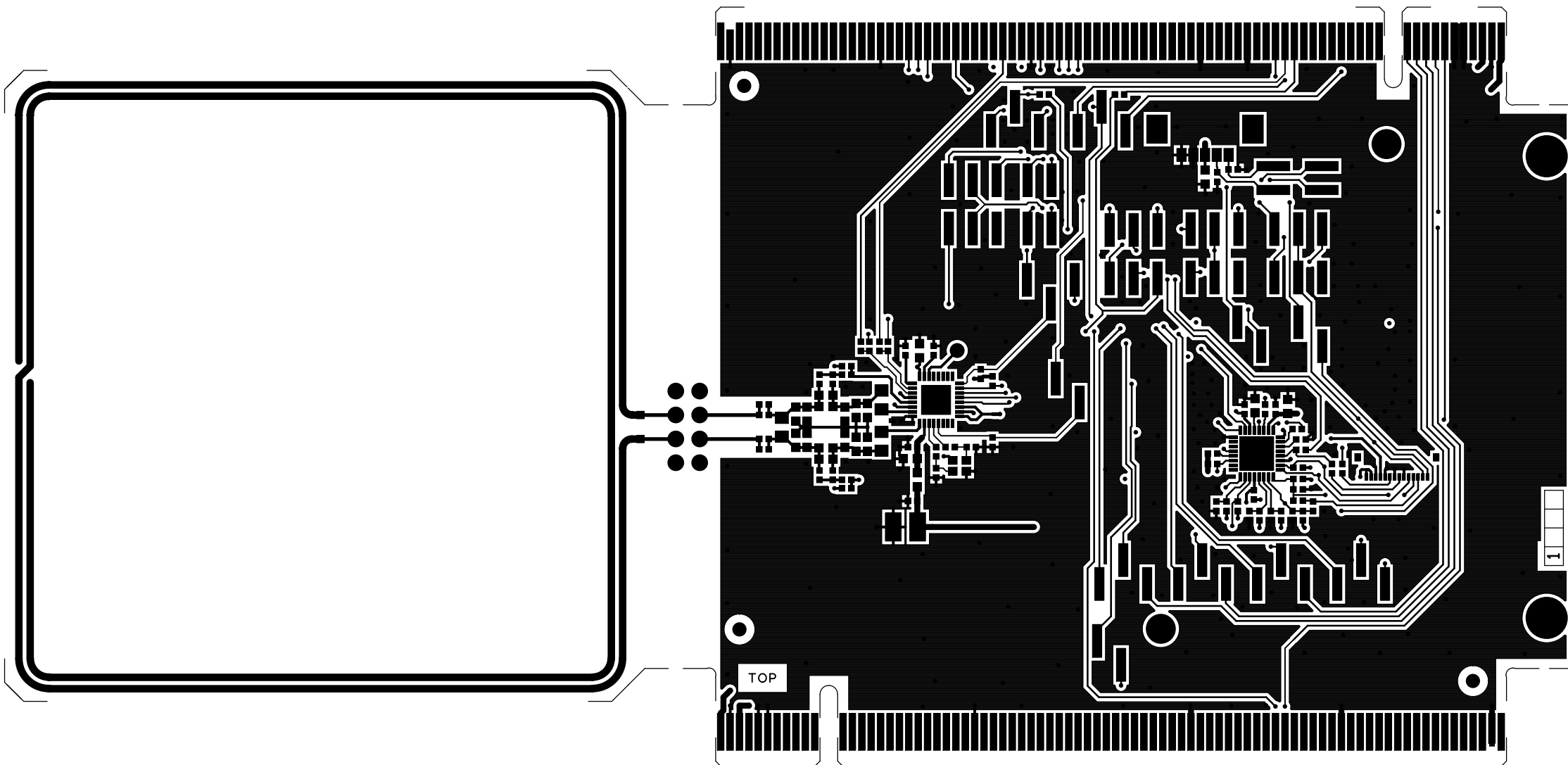
TWR CONNECTOR



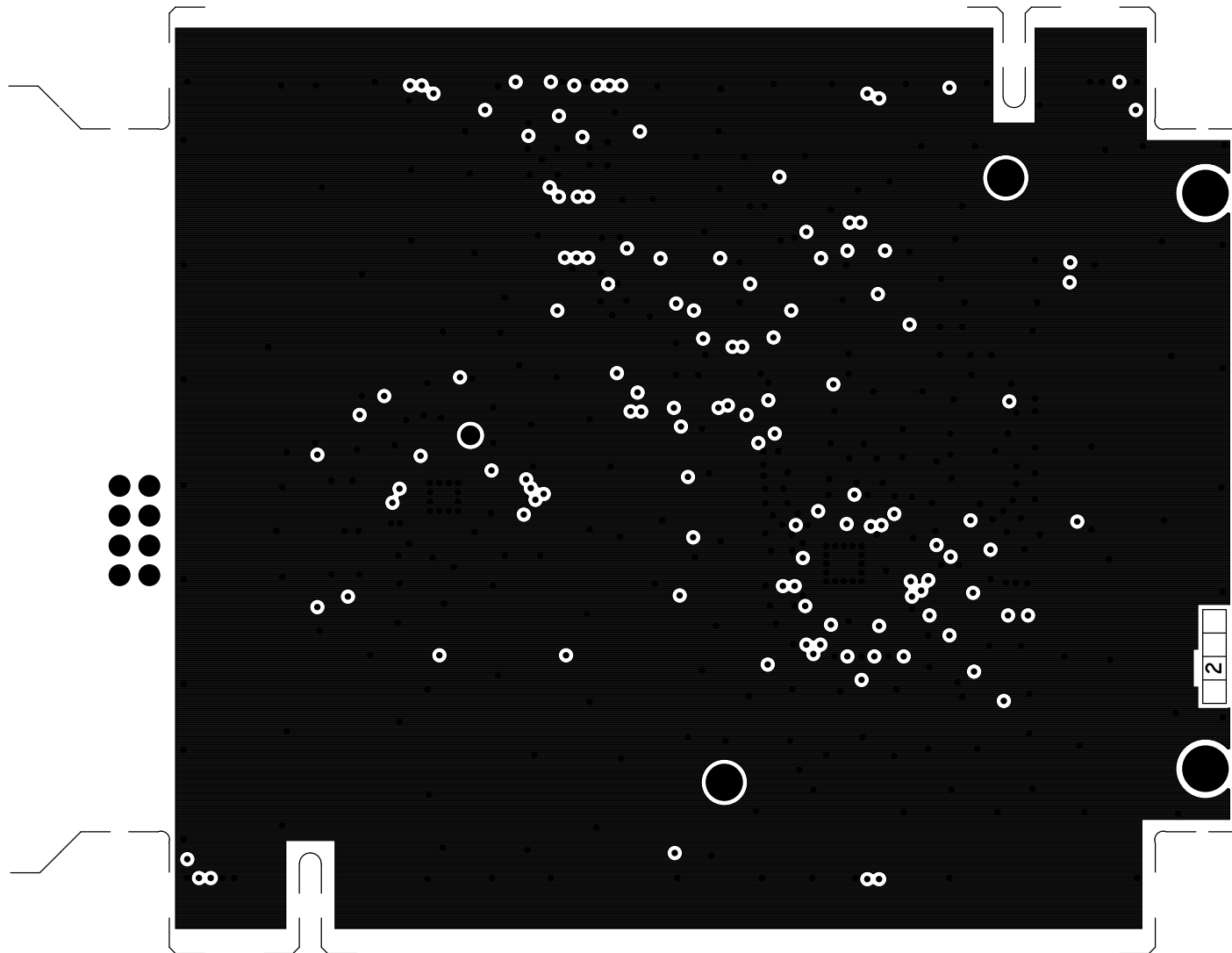
MAGNETIC STRIPE READER INTERFACE



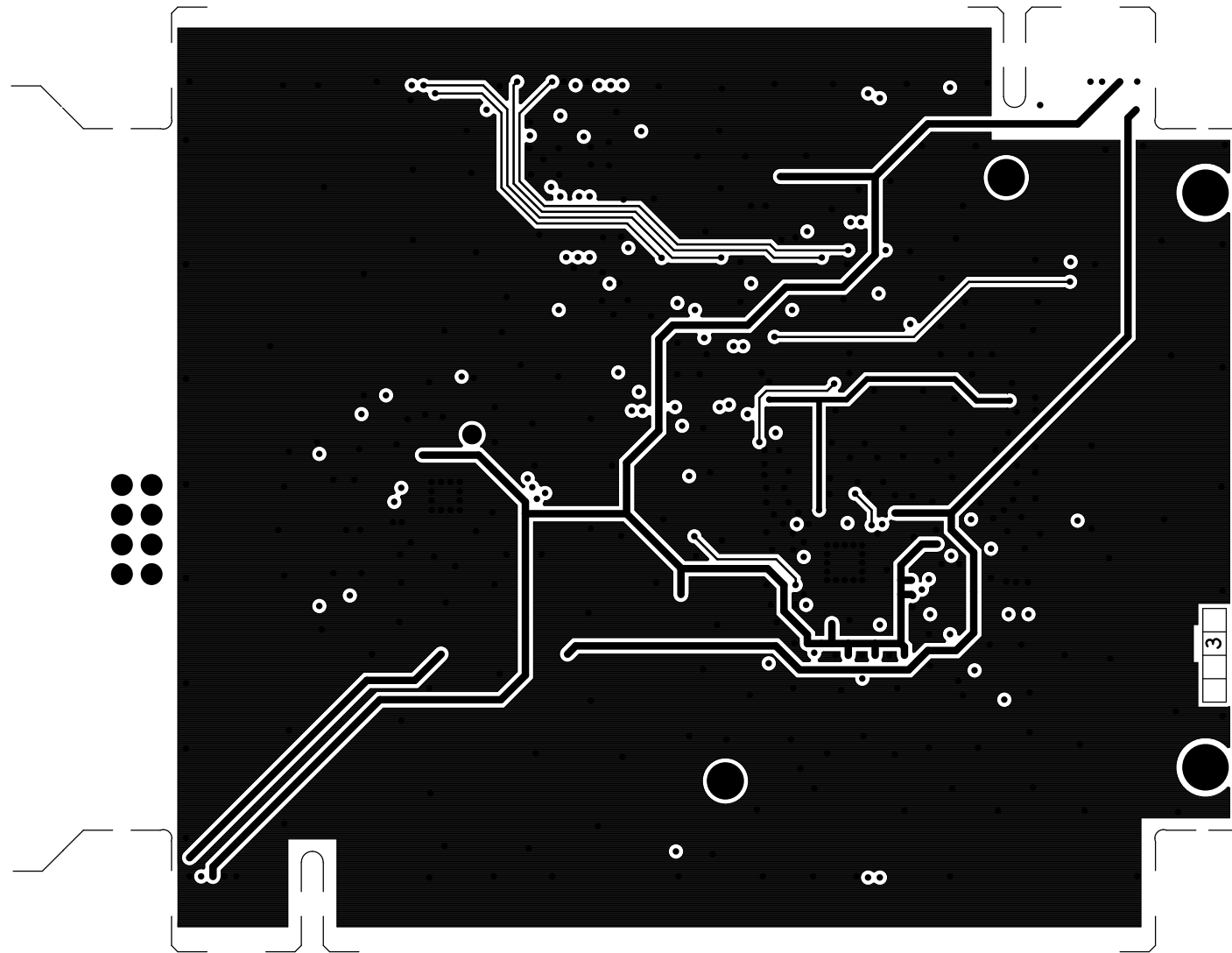
NXP SEMICONDUCTORS 2 Esplanade Anton Philips BP 2000 - COLOMBELLES 14906 CAEN CEDEX 9		Drawn by: SOMINEX/MV Reference: BSX0609 Size A3 Date: Wednesday, August 23, 2017	
Verify by: R.Bienert Projet: TWR-POSCARD Title: TWR CONNECTORS Rev 1.a		Sheet 3 of 3	



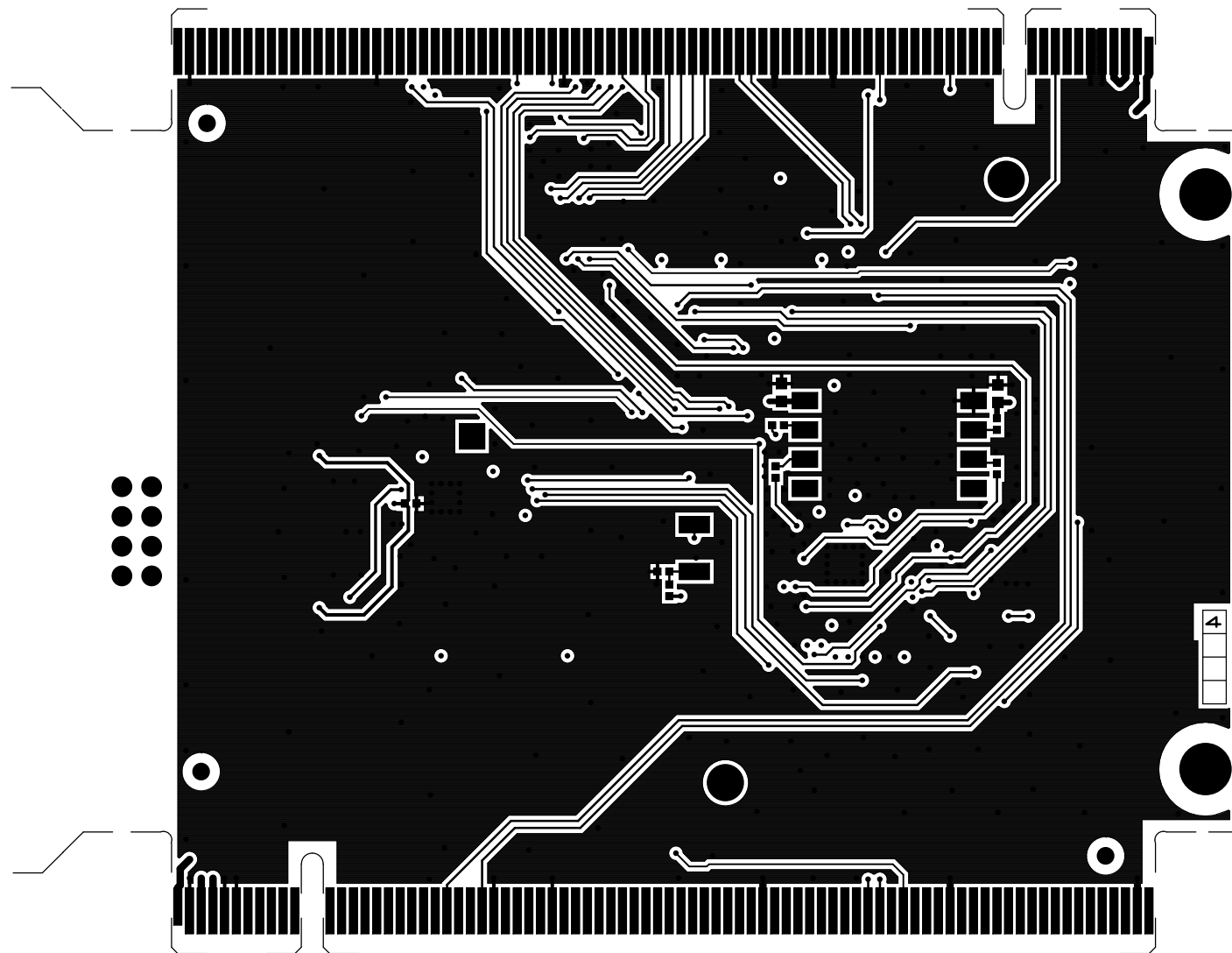
TWR-POS-CLRC663	Drawn by: SOMINEX/ Y.Bouillet		
Engineer: R.Bienert/NXP		BSX0609	
LAYER 1		1	23/08/17



TWR-POS-CLRC663	Drawn by: SOMINEX/ Y.Bouillet		
Engineer: R.Bienert/NXP		BSX0609	
LAYER 2		1	23/08/17



TWR-POS-CLRC663	Drawn by: SOMINEX/ Y.Bouillet		
Engineer: R.Bienert/NXP		BSX0609	
LAYER 3		1	23/08/17



TWR-POS-CLRC663	Drawn by: SOMINEX/ Y.Bouillet		
Engineer: R.Bienert/NXP		BSX0609	
LAYER 4		1	23/08/17



TWR-POS-CLRC663
Rev1

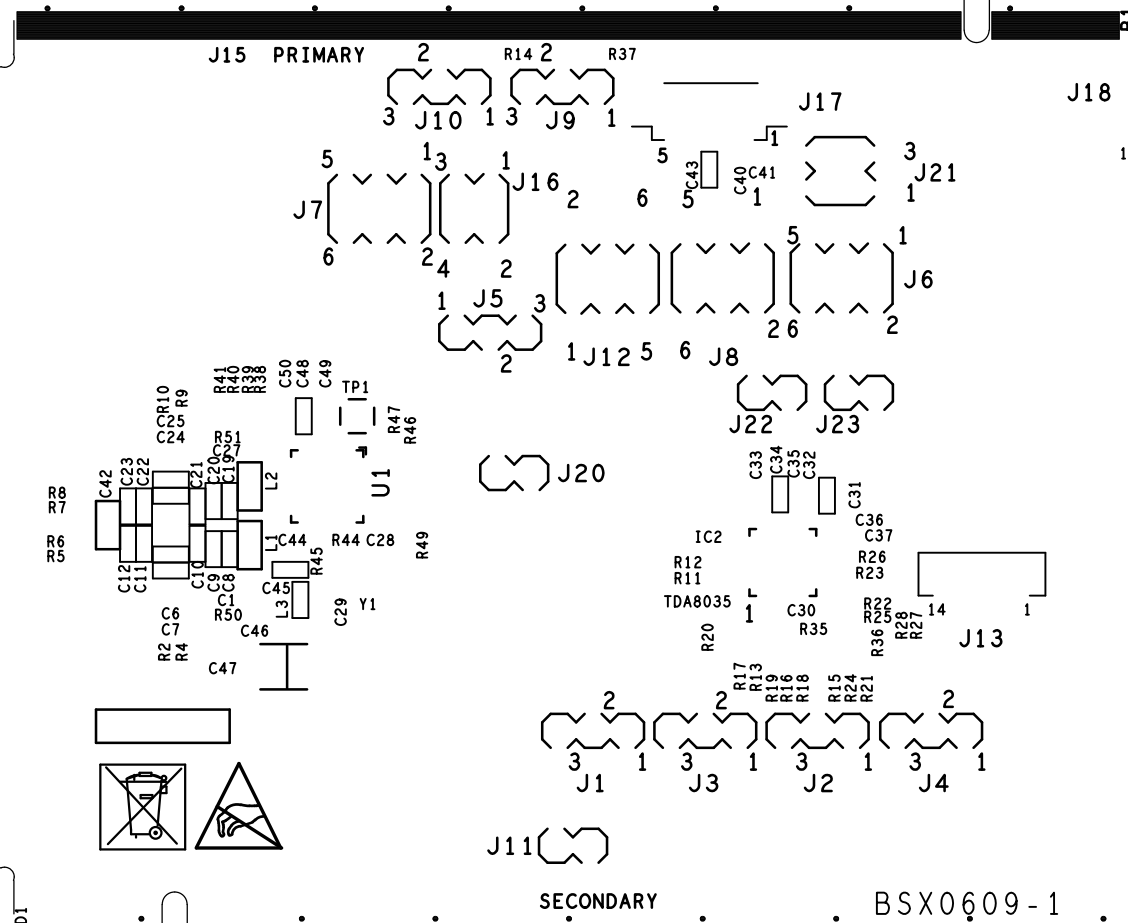
CLEV6630T



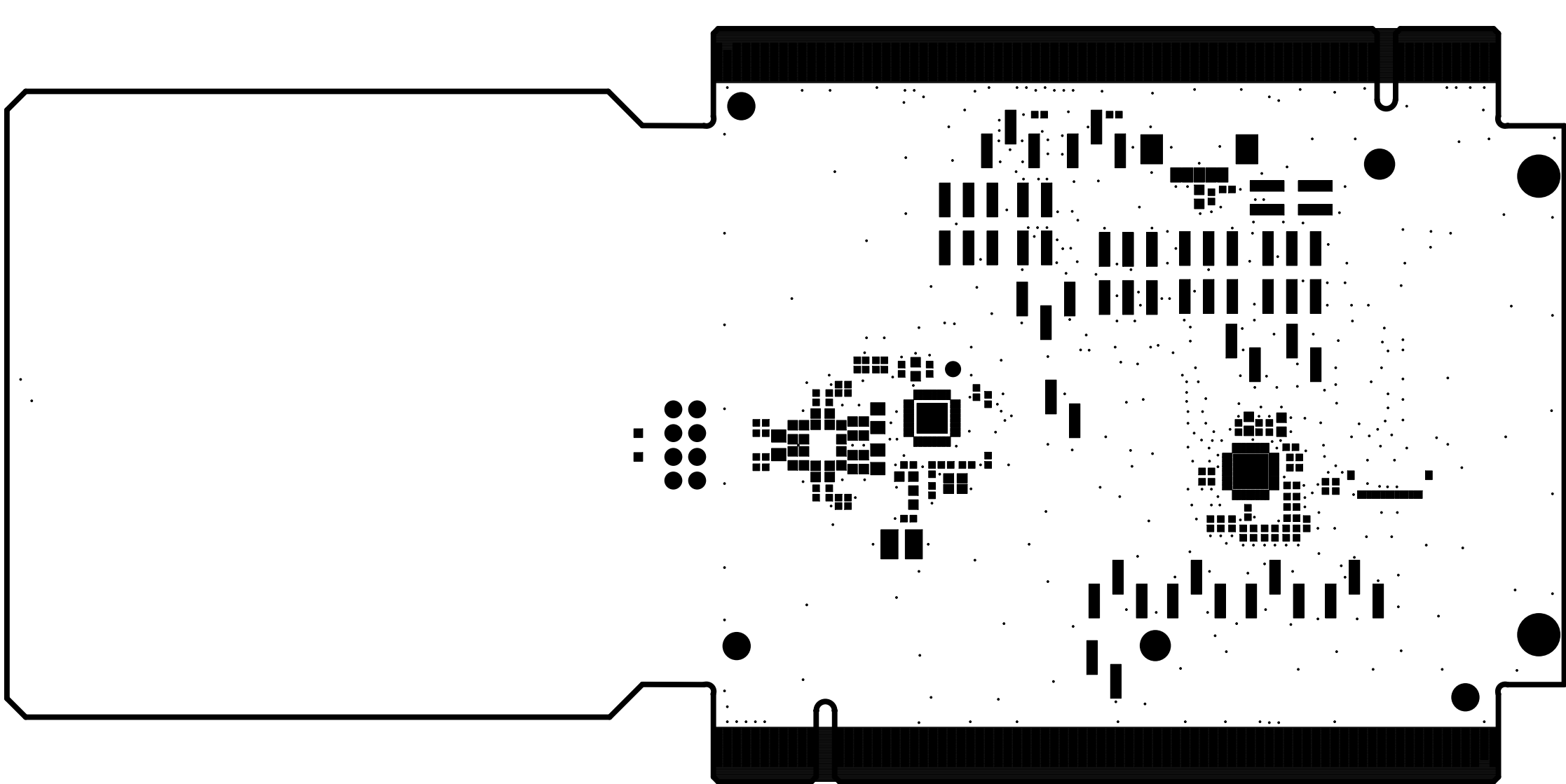
FCC ID: OWRCLEV6630T

J26
J25
J24
J27

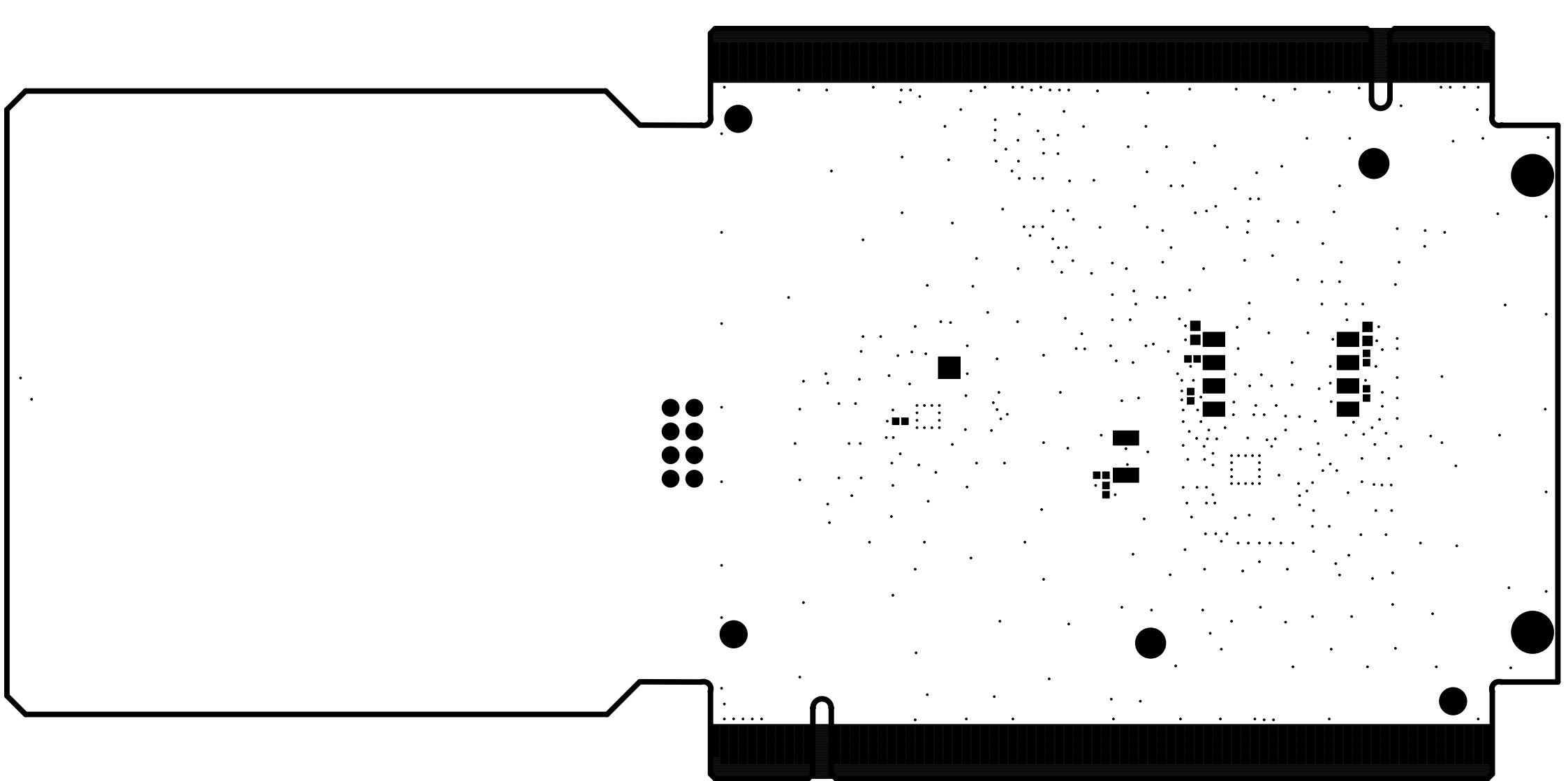
D1



TWR-POS-CLRC663	Drawn by: SOMINEX/ Y.Bouillet		
Engineer: R.Bienert/NXP		BSX0609	
SILKSCREEN TOP		1	23/08/17



TWR-POS-CLRC663	Drawn by: SOMINEX/ Y.Bouillet		
Engineer: R.Bienert/NXP		BSX0609	
SOLDERMASK TOP		1	23/08/17

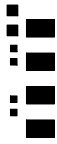


TWR-POS-CLRC663	Drawn by: SOMINEX/ Y.Bouillet		
Engineer: R.Bienert/NXP		BSX0609	
SOLDERMASK BOTTOM		1	23/08/17



TWR-POS-CLRC663	Drawn by: SOMINEX/ Y.Bouillet		
Engineer: R.Bienert/NXP		BSX0609	
PASTEMASK TOP		1	23/08/17

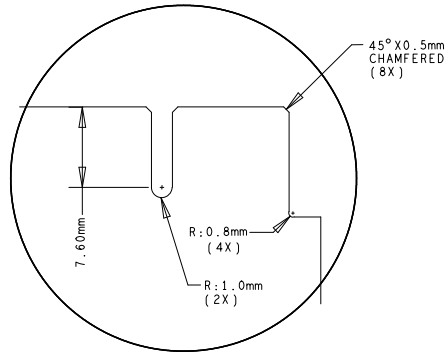
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			Engineer : R.Bienert\NXP		
S3\08\17	1		PASTERMASK BOT		



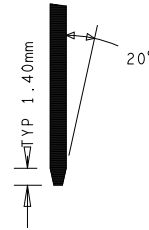
..

DETAIL C

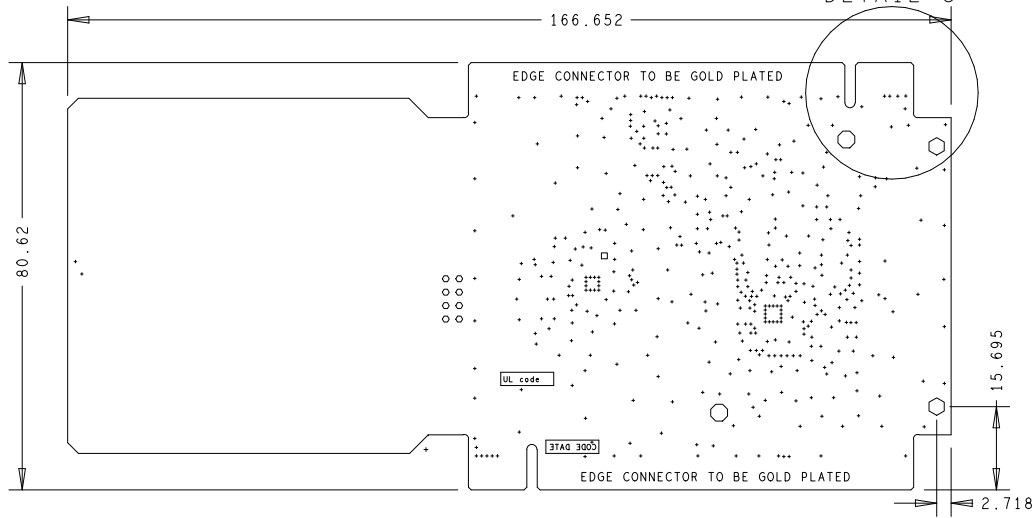
SCALE: NONE



PCI Express Card Edge Connector Bevel Information.
EDGE CONNECTOR TO BE GOLD PLATED



DETAIL C



VIEW FROM PRIMARY SIDE

4 LAYERS STACKUP

LAYER_1	TOP	SOLDER MASK TOP
LAYER_2		BASE COPPER : 35um
LAYER_3		FR4 : 203um : 17.5um
LAYER_4	BOTTOM	BASE COPPER : 17.5um
		FR4 : ***to be defined***
		BASE COPPER : 17.5um
		FR4 : 203um
		BASE COPPER : 35um
		SOLDER MASK BOTTOM

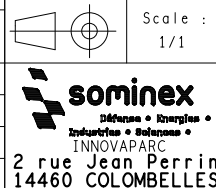
total thickness : 1.6mm

DRILL CHART: TOP to BOTTOM					
ALL UNITS ARE IN MILLIMETERS					
FIGURE	SIZE	TOLERANCE	PLATED	QTY	NOTE
+	0.25	+0.1/-0.05	PLATED	465	VIAS
□	1.1	+0.1/-0.05	PLATED	1	
○	1.3	+0.1/-0.05	PLATED	8	
○	3.3	+0.1/-0.05	PLATED	2	
○	3.2	+0.1/-0.05	NON-PLATED	2	

TOTAL HOLES: 478

Substrate : FR4	PCB thickness : 16/10
Copper thickness : 35u ext + 17.5u int (before plating)	
Nb of layers : 4	Finish : Ni/Au ch + zone Ni/Au Electro
Solder mask : Top+Bot Color: Black	Silkscreen : Top Color: White
Mini track width/mini gap: 0.15/0.15mm	
Electrical test: YES	IPC-A-600 Level: 2
Controlled impedance: No	
PCB COMPATIBLE LEAD FREE PROCESS	

DATE CODE: supplier logo + date allowed on bottom silk or bottom solder mask
UL code: logo UL 94 on solder mask top



TWR-POS-CLRC663 BSX0609

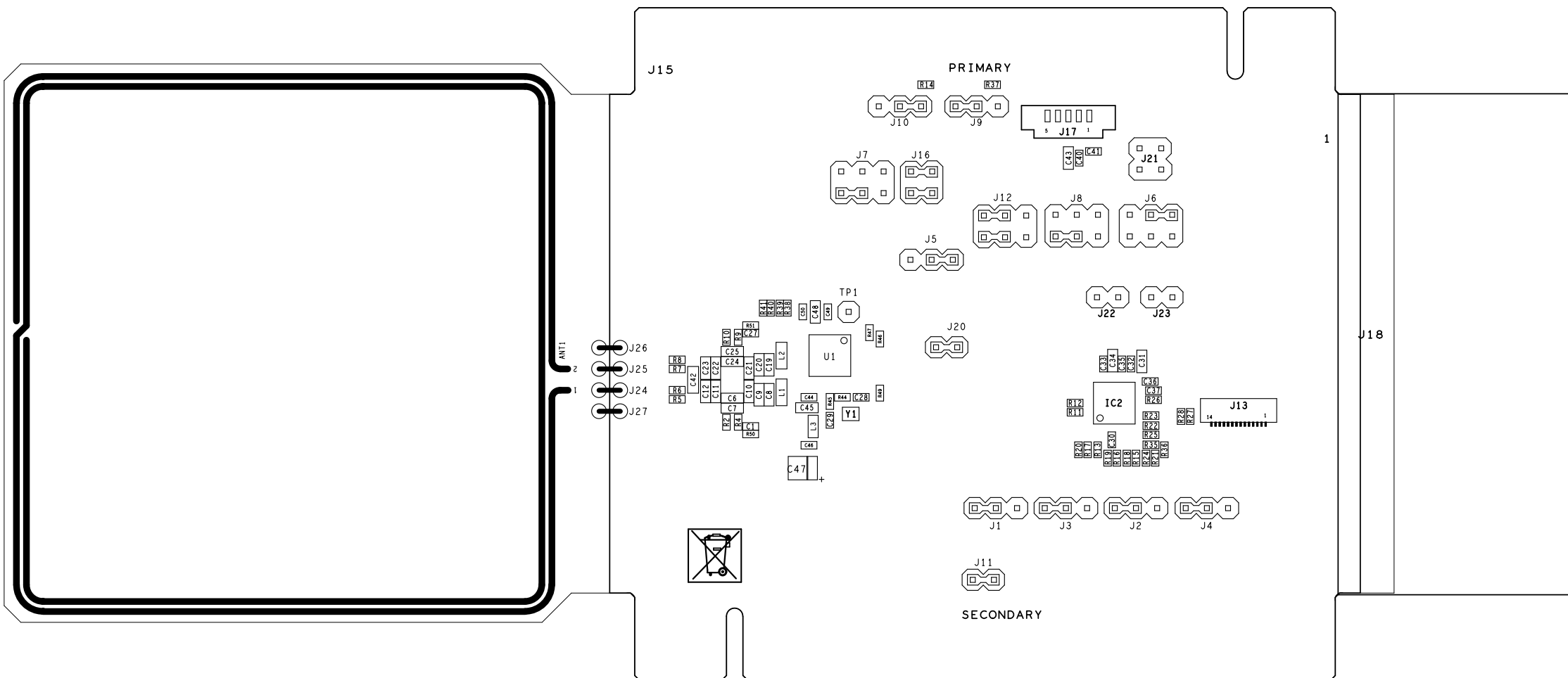
DRILL LAYER

Drawn by : SOMINEX/ Y.Bouillet

Format : A3

Page : 1/1

1 23/08/2017



☐=cavaliers a ajouter suivant plan

TWR-POS-CLRC663	Drawn by: SOMINEX/ Y.Bouillet	BSX0609		
Engineer: R.Bienert/NXP				
ASSEMBLY TOP			1	23/08/17

