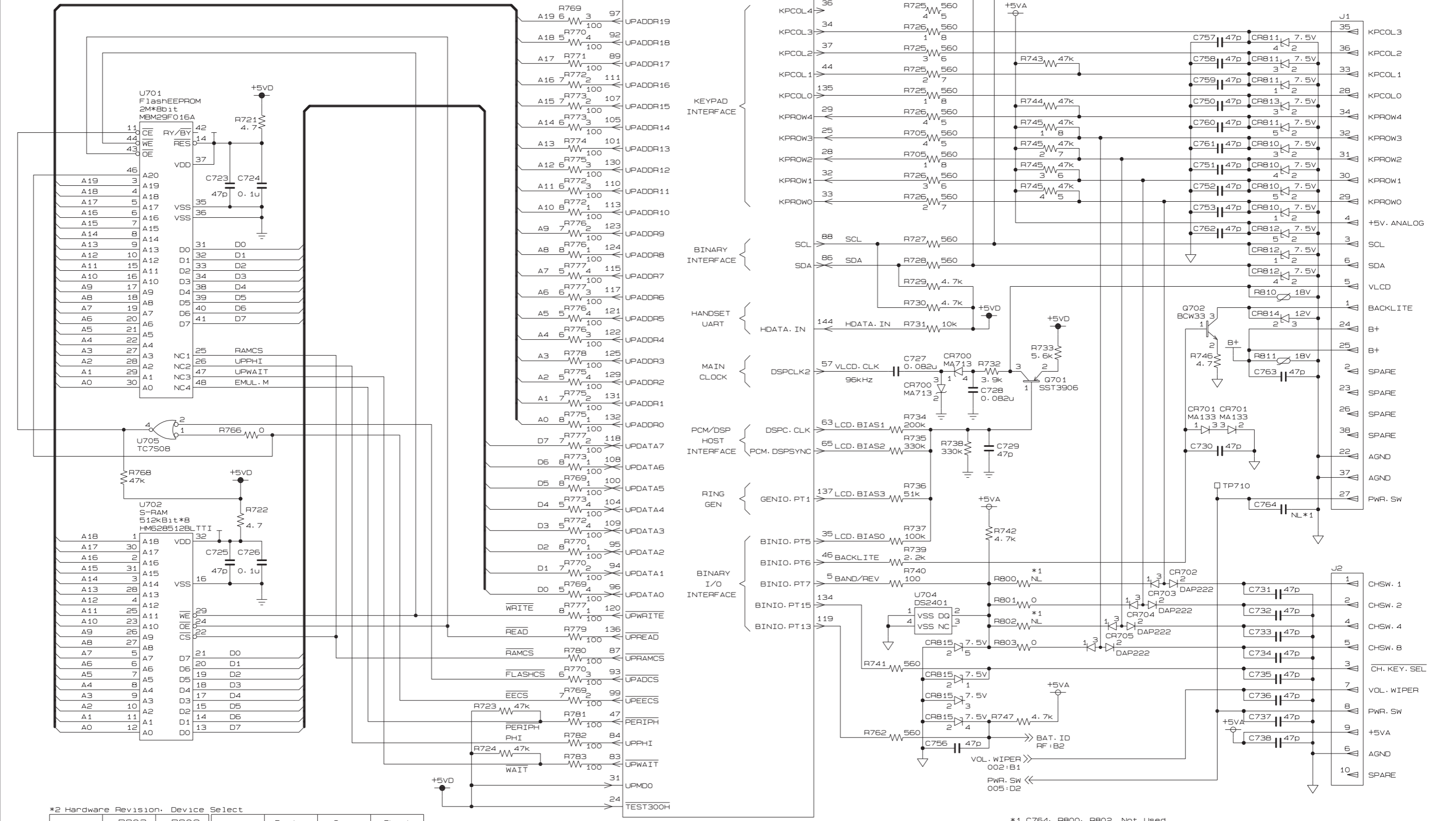


REVISIONS	SYM	DATE	POSITION	DESCRIPTION	REVISED	CHECKED	APPROVED
1	X 3	01.07.09		Rev. Up	H. Wakaizumi	Y. Ikeda	H. Kawaguchi
2	X 7	01.10.01		Constant change	M. Sato	K. Sugimoto	N. Kimura
3	X 0	01.10.15		Rev. up	M. Sato	K. Sugimoto	N. Kimura
4	X 0	01.12.22		Rev. up	M. Sato	K. Sugimoto	N. Kimura
5	X						



\*2 Hardware Revision: Device Select

	R803	R802	Device	Open	Short
RA0	Short	Short	R800	DSP	VC5416
RA1	Short	Open	R801	PLL	SA7025
RA2	Open	Short			CX72301
RA3	Open	Open			

DRAWN H. Wakaizumi DSGN OR ENGR Y. Ikeda	DATE 01.05.30 DATE 01.05.30	CHECKED O. Baba APPROVED H. Kawaguchi	DATE 01.05.30 DATE 01.05.30	DATE 01.05.30	UNIT mm	MODEL H/H	EQPT. NAME J700P(C5416) 800MHz
KOKUSAIDENKI ENGINEERING CO., LTD			SCALE	TITLE MAIN LOGIC SCHEMATIC	PARTS CODE	DWG. No. G3UD06903	Rev. 4