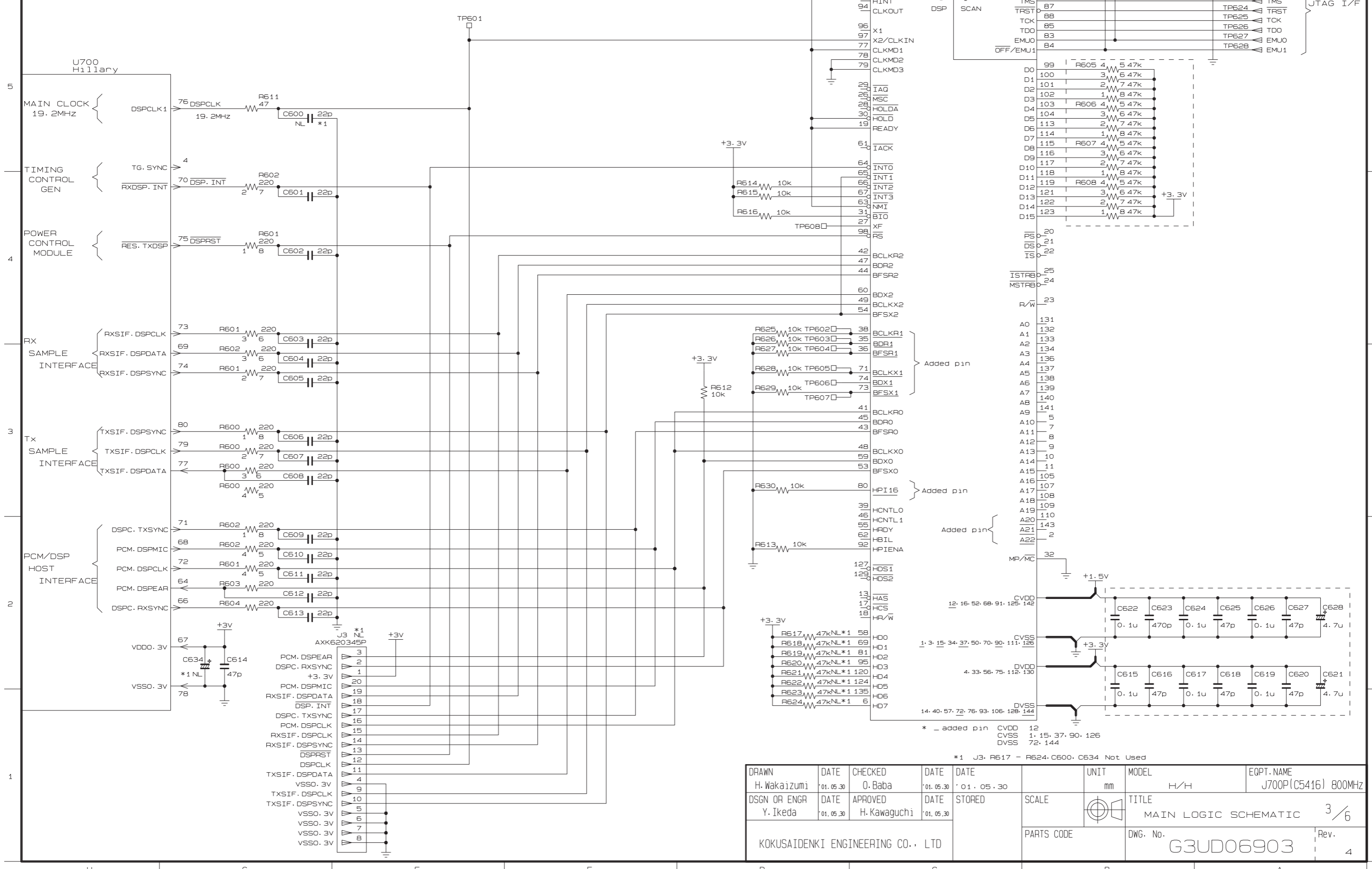


REVISIONS	SYM	DATE	POSITION	DESCRIPTION	REVISED	CHECKED	APPROVED
1	X33	'01.07.09		Rev. Up	H. Wakaizumi	Y. Ikeda	H. Kawaguchi
2	X4	'01.10.01		Modification by DSP modify	M. Sato	K. Sugimoto	N. Kimura
3	X4	'01.10.15		Part addition. Capacitor exchange	M. Sato	K. Sugimoto	N. Kimura
4	X0	'01.12.22		Rev. up	M. Sato	K. Sugimoto	N. Kimura
5	X						



DRAWN H. Wakaizumi	DATE '01.05.30	CHECKED O. Baba	DATE '01.05.30	DATE '01.05.30	UNIT mm	MODEL H/H	EQPT. NAME J700P(C5416) 800MHz
DSGN OR ENGR Y. Ikeda	DATE '01.05.30	APPROVED H. Kawaguchi	DATE '01.05.30	STORED	SCALE	TITLE MAIN LOGIC SCHEMATIC	3/6
KOKUSAI DENKI ENGINEERING CO., LTD					PARTS CODE	DWG. No. G3UD06903	Rev. 4

* - added pin CVDD 12
CVSS 1. 15. 37. 90. 126
DVSS 72. 144

*1 J3, R617 - R624, C600, C634 Not Used