

Model:	Lightning
Test Date:	Feb 01, 2007

CONFIGURATION (Full Rate, All Up Bits)		CONDUCTED POWER (dBm)			
		CELL Ch 383		PCS Ch 600	
		Ave	Peak	Ave	Peak
SO2 Loopback	RC1	28.99	24.17	27.28	22.80
	RC3	28.43	24.11	26.98	22.79
SO3 Speech	RC1	28.92	24.14	27.26	22.82
	RC3	28.53	24.12	26.68	22.77