FCC TYPE ACCEPTANCE REPORT

LU1000AL

1000 WATT UHF TRANSLATOR

AXCERA, LLC

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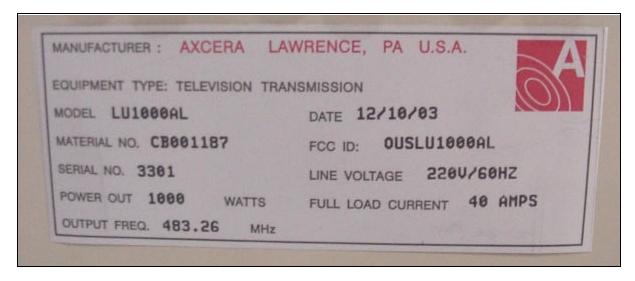
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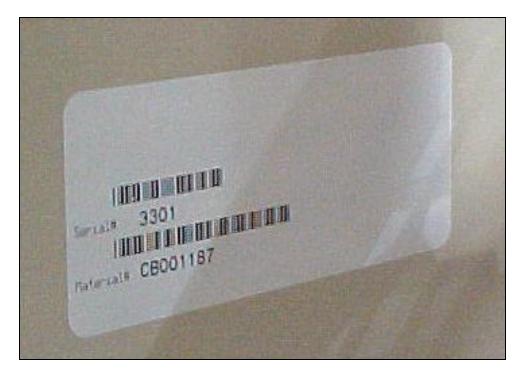
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1. ID LABEL/LOCATION INFO

1.1 Front Panel FCC Label



1.2 Rear Panel Manufacturer's Label



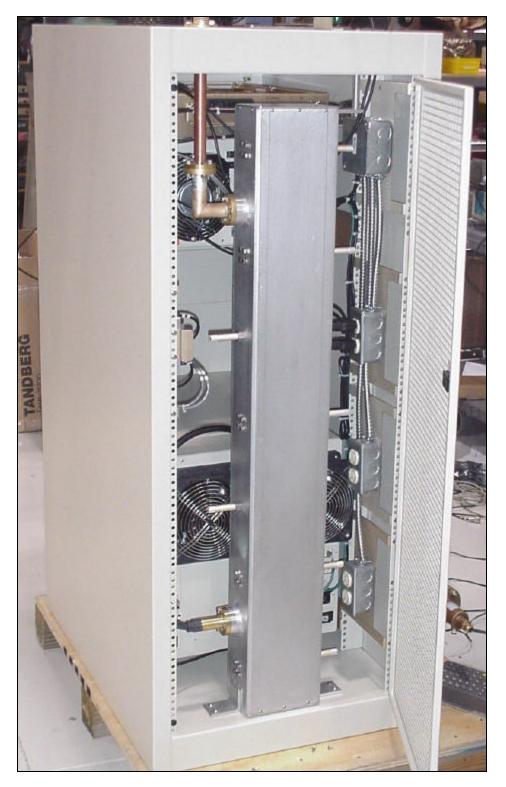


1.3 Front view, complete Axcera-LU1000AL Translator with FCC Label





1.4 Rear view, complete Axcera-LU1000AL Translator





1.5 Rear view, Axcera-LU1000AL Translator with Manufacturer's Label.





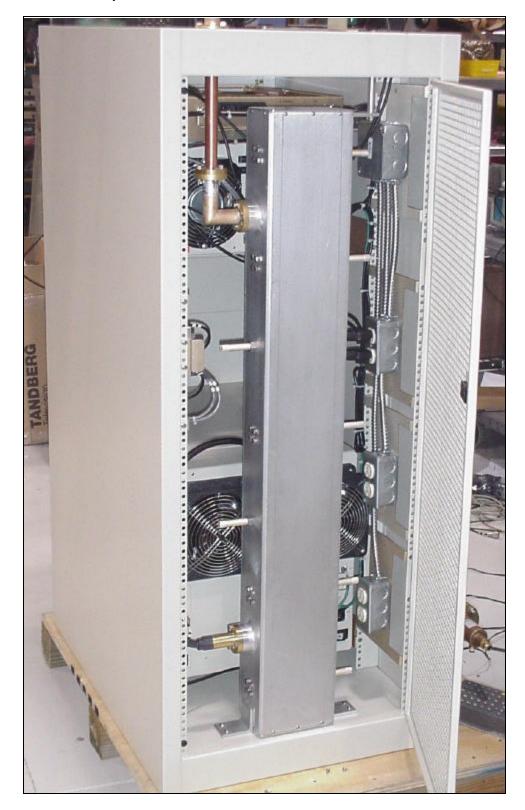
2. EXTERNAL PHOTOS

2.1 Front view, complete LU1000AL Translator





2.2 Rear view, complete Axcera-LU1000AL Translator





3. SCHEMATICS

The schematics for the Axcera-LU1000AL system are provided as separate PDF files.



4. TEST REPORT

4.1 RF Power Measurements

Figure 4-1 shows the test equipment setup for the RF power measurements.

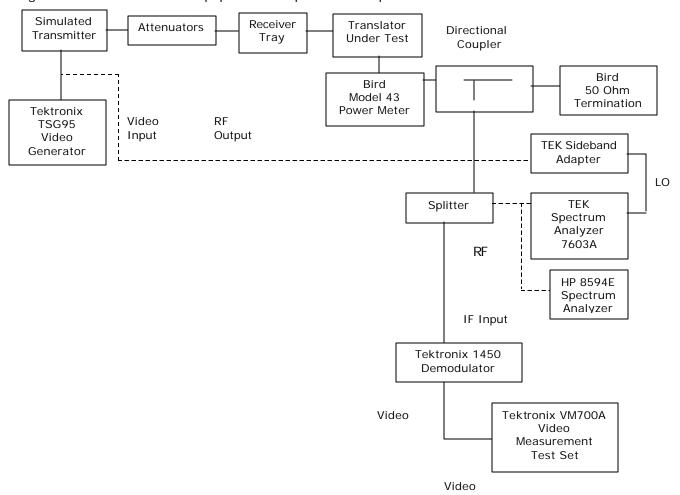


Figure 4-1. Test Equipment Setup for RF Power Measurements

The video modulation was adjusted for 25% with sync and blanking video signals and the aural power was turned off. The power was then adjusted to obtain 600 watts of average visual RF output (1000 watts peak sync at the output connector). This level was used to establish a reference on the spectrum analyzer.

Next, the aural power was turned on and adjusted to 10 dB below the visual power. The following operating parameters were recorded:

Peak Visual Power /100%=1000 Watts
Aural Power/100%=100 Watts continuous wave (CW)
Reflected Power: <2%



4.2 Modulation Characteristics

4.2.1 Video Modulation

The test setup shown in Figure 4-1 was used to adjust the video signal to obtain a white picture level using the Tektronix 1450 Demodulator chopper function, the modulation was accurately measured to be 87.5% while maintaining a depth of modulation at blanking.

Next, the video was adjusted for modulated staircase and the differential phase and gain were measured and recorded as follows:

Differential phase: +/-0.5°
Differential gain: 4.7%

Figure 4-2 shows the substantially linear transfer characteristics of the transmitter as a demodulated video waveform.

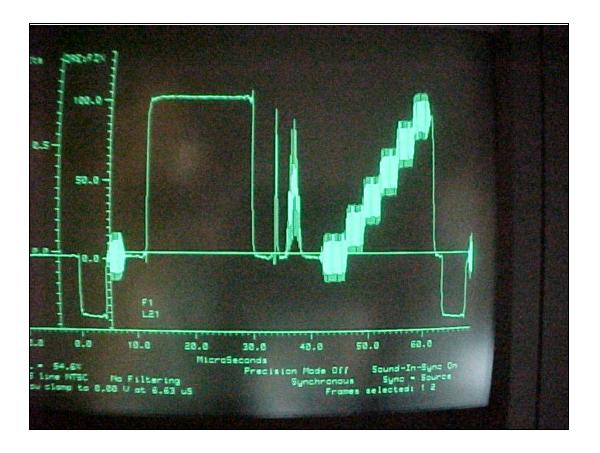


Figure 4-2. Substantially Linear Transfer Characteristics of the Transmitter as a Demodulated Video Waveform



4.2.2 Video Envelope Delay

Not Applicable - Unit under test is a translator.

4.2.3 Video Noise

Not Applicable - Unit under test is a translator.

4.2.4 Video Frequency Response

The test setup shown in Figure 4-1 was used to record the detected video frequency response; the results are shown in Table 4-2 and Figure 4-3.

Note: For this test, the video signal was adjusted to provide a 50% average picture level and the video sweep signal level was set to cover the range from black to white picture.



VIDEO FREQUENCY	RELATIVE RESPONSE
200 kHz	+2.2 dB
500 kHz	0 dB
750 kHz	-0.1 dB
1.0 MHz	-0.3 dB
1.25 MHz	-0.2 dB
1.5 MHz	-0.4 dB
2.0 MHz	0 dB
2.5 MHz	+0.1 dB
3.0 MHz	-0.3 dB
3.5 MHz	-0.2 dB
3.58 MHz	+0.1 dB
4.0 MHz	-0.1 dB
4.18 MHz	-0.2 dB
4.5 MHz	-25 dB
4.75 MHz	-38 dB
5.0 MHz	-41 dB

Table 4-2. Detected Video Frequency Response

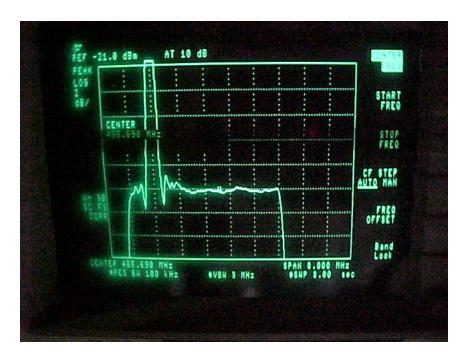


Figure 4-3. Graph of Detected Video Frequency Response

4.2.5 RF Sideband Response

The RF sideband response was recorded using the test setup shown in Figure 4-1. With the output power set to 1000 watts peak of sync, the Tektronix TSG95 video generator was adjusted to provide a cable sweep test signal and the RF sideband response was recorded. Data from this test is provided in Table 4-3.

Photographs of the spectrum analyzer results are shown in Figure 4-4.



Table 4-3. RF Sideband Response

OUTPUT FREQUENCY	VIDEO FREQUENCY	RESPONSES
(MHz)		
483.26	Carrier	
483.46	+200 KHz	0 (reference)
483.51	+250	9
483.76	+500	-4.0
484.01	+750	-4.1
484.26	+1000	-4.4
485.26	+2000	-4.6
486.26	+3000	-4.1
487.26	+4000	-4.2
487.76	+4500	-24.0
488.01	+4750	-34.0
488.26	+5000	-52.0
483.01	- 250	-1.0
482.76	-500	-1.9
482.51	- 750	-4.4
482.26	- 1000	-10.0
482.01	-1250	-34.0
481.76	- 1500	-36.0
481.51	- 1750	-38.0
481.26	-2000	-41.0
480.26	- 3000	-44.0
479.68	- 3580	-46.0
479.26	- 4000	-54.0
478.26	- 5000	-54.0
489.01	+5750	-59.0

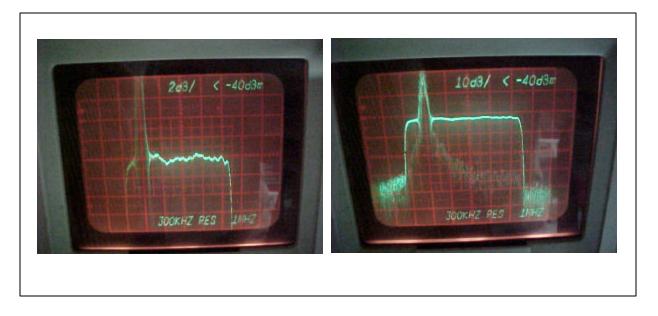


Figure 4-4. Spectrum Analyzer Results

4.2.6 Audio Modulation

Not Applicable - Unit under test is a translator.



4.2.7 AM and FM Noise

• Not Applicable - Unit under test is a translator.

4.3 Occupied Bandwidth

Using the test setup in Figure 4-6, with the transmitter operating at maximum power, photographs of the translator-occupied bandwidth spectrum were taken and are shown in Figures 4-7 and 4-8.

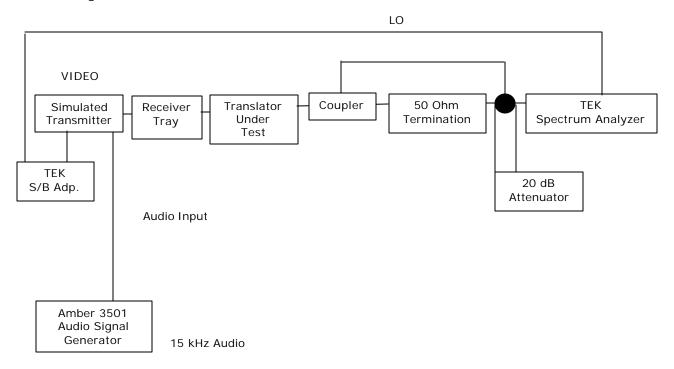


Figure 4-6. Test Setup for Occupied Bandwidth Spectrum Analysis

Note: Using the test procedure shown in Figure 4-1, the visual modulation was adjusted to 87.5% at white with the modulated staircase waveform and aural deviation adjusted to ± 25 kHz (100%) with the 400 Hz audio tone.



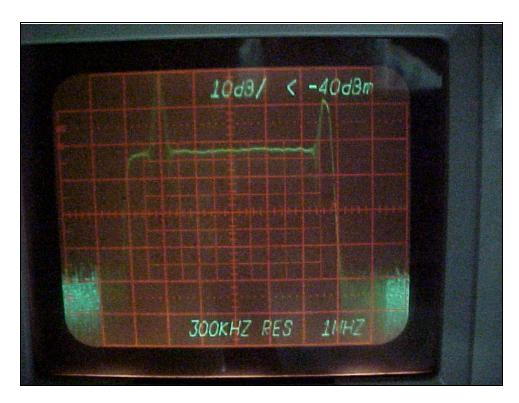


Figure 4-7. Channel-Occupied Bandwidth

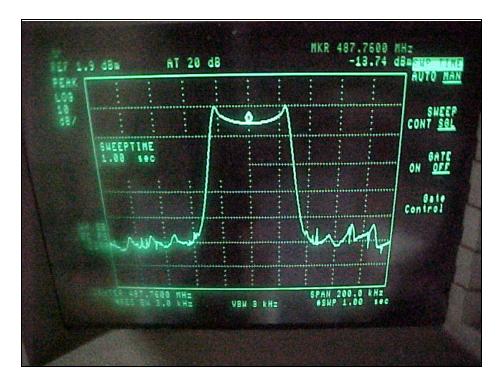


Figure 4-8. Aural Carrier-Occupied Bandwidth



4.4 Conducted Spurious Emissions

Using the test setup shown in section 4.1, the spectrum outside of the specified channel was observed and the data was recorded on all products above the 70 dB noise floor of the spectrum analyzer. This data is shown in Table 4-6 and is presented as graphs in Figures 4-9 and 4-10.

Table 4-6. Products Above the 70 dB Noise Floor of the Spectrum Analyzer
--

FREQUENCY (MHz)	SOURCE	PEAK LEVEL OBSERVED (dB)
574.76	Image Visual Carrier	None observed
570.76	Image Aural Carrier	None observed
529.01	Local Oscillator	None observed
474.26	-9 MHz Product	None observed
478.76	-4.5 MHz Product	-69
487.76	Aural Carrier	-10
483.26	Visual Carrier	0
490.46	+7.2 MHz Product	-66
494.06	+10.8 MHz Product	None observed
492.26	+9 MHz Product	- 70
491.34	+8.08 MHz Product	-62
488.68	+5.42 MHz Product	-61
479.68	-3.58 MHz Product	None observed
975.51	Second Harmonic - Aural Carrier	None observed
966.51	Second Harmonic - Visual Carrier	None observed
1463.26	Third Harmonic - Aural Carrier	None observed
1449.76	Third Harmonic - Visual Carrier	None observed

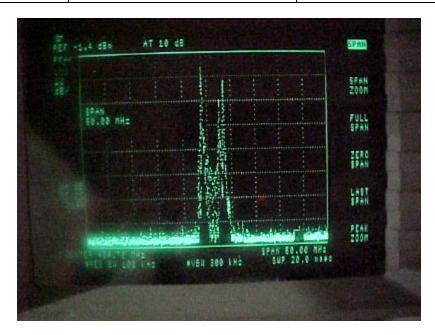


Figure 4-9. Products Above the 70 dB Noise Floor of the Spectrum Analyzer (1 of 2)



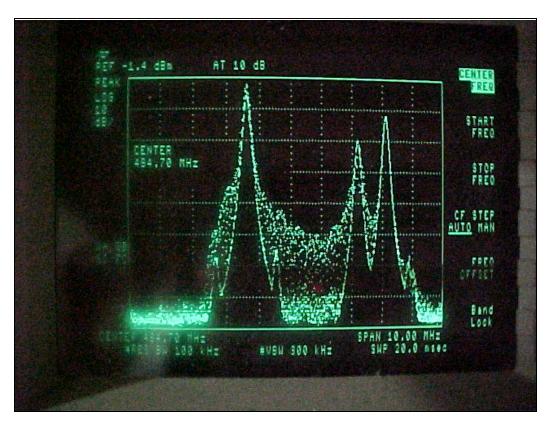


Figure 4-10. Products Above the 70 dB Noise Floor of the Spectrum Analyzer (2 of 2)

4.5 Radiated Emissions

Using the test setup shown in Figure 4-11, with the transmitter operating at full power, the spectrum analyzer was moved 20 meters from the transmitter and connected to a dipole antenna cut to 483 MHz. This antenna was oriented to maximize the received level and the data was recorded. The antenna was then cut to the local oscillator frequency and the second and third harmonic frequencies of the transmitter, and all of the signals received, were maximized by antenna orientation and their absolute levels were recorded.

With these various antennas, and with an adjustable length dipole for 40 to 1,500 MHz, the frequency spectrum from 40 MHz to 2,100 MHz was observed. The only measurable levels observed were at 483.26 and 487.76 MHz. These levels are shown below in Table 4-8 and an analysis of the relative field and strength are provided in the following paragraphs.

Table 4-8. Measurable Levels Observed in Frequency Spectrum

FREQUENCY	MEASURED LEVEL (INTO 50 W)
483.26	- 41 dBm
487.76	- 48 dBm



The spectrum analyzer had a maximum sensitivity of -110 dBm during these tests.

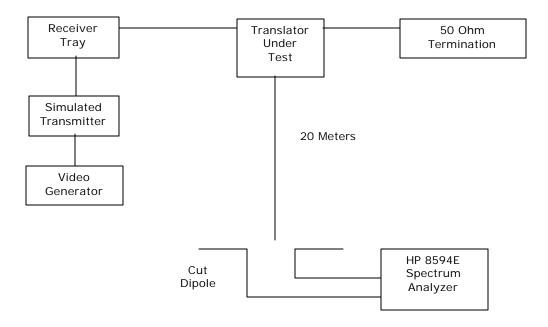


Figure 4-11. Test Setup for Measuring Radiated Emissions

Three levels were compared to the following reference level.

If all of the power of the translator was radiated by an isotropic radiator, the power density at 20 meters would be:

$$P = Pt/4\pi R^2 = 1000/4\pi \bullet (20)^2 = .2 \text{ w/m}^2$$

Using a dipole-transmitting antenna increases this by 1.64 to:

$$1.64 \text{ x} .20 = .33 \text{ w/m}^2$$

If a dipole-receive antenna of area 1.64 x $\lambda^2/4\pi$ is used to receive the signal, the received level would be:

$$330 \text{ mw} = +25.2 \text{ dBm}$$

The receive levels at -41dBm and -48 dBm were therefore at -66.2 dB and -73.2 dB relative to this level.

The receive levels were therefore at the relative levels shown in Table 4-9.

Table 4-9. Receive Levels

FREQUENCY	(REF = +25 dBm) RELATIVE MEASURED LEVEL
483.26	-66.2 dB
487.76	-73.2 dB



With the receive dipole cut to 483.26 MHz, the cabinet radiation was also checked, within very close proximity to the trays of the translator, and the received level that was recorded, and shown below, did not exceed a power density above of -1 dBm:

 $Pr/A = 0.8 \text{ mw/cm}^2$

This level is far less than the current or proposed standard for safe radiation levels.

4.6 Frequency Stability

The output carrier frequencies are determined by the crystal oscillator/multipliers in the exciter and receiver tray.

The Receiver's oscillator was placed in a temperature-controlled chamber and the temperature was varied from -30° C to $+50^{\circ}$ C. The chamber was slowly heated and the frequency was measured at 10° C increments up to $+50^{\circ}$ C. The oscillator was allowed to stabilize at each temperature before measurements were recorded.

Tables 4-10, 4-11, and 4-12 provide data on the individual oscillator frequencies; a calculation of the output frequency error indicates that the output frequency of the transmitter is well within the FCC tolerance for this service.

OVEN CONTROLLED **EXCITER** TEMP (°C) CRYSTAL OSCILLATOR (MHz) -30 81.124932 -20 81.124968 - 10 81.124969 0 81.124975 10 81.124986 20 81.125000 30 81.125031 81.125057 40 50 81.125070

Table 4-10. Oscillator Data

DUAL OVEN TOTAL OUTPUT TEMP (°C) **OSCILLATOR ERROR** ERROR (kHz) D¹ KHz -30 -0.068 -0.544 -20 -0.032 -0.256 -0.248 - 10 -0.031 -0.025 -0.200 0 -0.112 10 -0.014 20 0.0 0.0 +0.24830 0.031 40 0.057 +0.456

Table 4-11. Calculated Frequency Errors

Table 4-12. Frequency Stability Versus Line Voltage

0.070

+0.560

LINE VOLTAGE	OSCILLATOR (MHz)	TOTAL OUTPUT ERROR kHz
95V	81.125000	0.00
115V	81.125000	0.00
135V	81.125000	0.00

The LU1000AL is designed to operate using an external 10 MHz precise reference oscillator. The frequency stability of this external reference determines the frequency stability of the translator.

The frequency determining variables of the transmitter may be defined as follows:

 F_{LO} = Desired local oscillator frequency

 F_{IF} = Desired IF oscillator frequency

50

F_R = Desired external reference oscillator frequency

 F_{RF} = Desired RF output frequency

 E_{LO} = Local oscillator frequency offset error

 $E_{IF} = IF$ oscillator frequency offset error

 E_R = External reference oscillator frequency offset error

 $E_{RF} = RF$ output frequency error

The PLL circuitry maintains a constant ratio between the external reference frequency and the output frequency of the oscillator. This ratio is defined below for both the LO and IF oscillators.

$$G_{LO} = F_{LO} / F_R$$

 $G_{IF} = F_{IF} / F_R$

Any change in the external 10 MHz reference will effect a corresponding change in the output frequency such that the above ratios are maintained.

$$G_{LO} = (F_{LO} + E_{LO}) / (F_R + E_R) = F_{LO} / F_R$$

 $G_{IF} = (F_{IF} + E_{IF}) / (F_R + E_R) = F_{IF} / F_R$



Solving for the change in output frequency yields:

$$E_{LO} = E_R * (F_{LO} / F_R) = E_R * G_{LO}$$

$$E_{IF} = E_R * (F_{IF} / F_R) = E_R * G_{IF}$$

The desired RF carrier frequency is equal to the LO frequency minus the IF frequency:

$$F_{RF} = F_{LO} - F_{IF}$$

The actual RF frequency, including any error introduced by the external reference, may be defined as follows:

$$\begin{aligned} F_{RF} + E_{RF} &= (F_{LO} + E_{LO}) - (F_{IF} + E_{IF}) \\ F_{RF} + E_{RF} &= (F_{LO} + F_{IF}) - (E_{LO} - E_{IF}) \\ F_{RF} + E_{RF} &= F_{RF} + (E_{LO} - E_{IF}) \end{aligned}$$

Calculating for the error of the carrier yields:

$$\begin{split} E_{RF} &= (E_{LO} - E_{IF}) \\ E_{RF} &= E_R * G_{LO} - E_R * G_{IF} \\ E_{RF} &= E_R (G_{LO} - G_{IF}) \\ E_{RF} &= E_R / F_R * (F_{LO} - F_{IF}) \\ E_{RF} &= E_R / F_R * F_{RF} \end{split}$$

Therefore, the error of the RF carrier is a function of the external 10 MHz reference error.

The maximum RF frequency error for this service is +/- 1.0 KHz. The highest channel frequency for this service (CH. 69 = 801.25 MHz) represents the worst case condition. With these values, the maximum allowable reference error ($E_{R(max)}$) can be calculated.

$$E_{R(max)} = 12.48 \text{ Hz}$$

The required reference oscillator stability may be calculated as follows:

```
Stability = E_{R(max)} / F_R
Stability = 12.48 Hz/10 x 10<sup>6</sup> Hz = 1.248 x 10<sup>6</sup>
```

Therefore, the RF frequency error of the LU1000AL will not exceed +/- 1.0 KHz when operated with a precise reference oscillator with a stability equal to or better than 1.248 x 10^6 .

Commercially available GPS precise reference oscillators, such as the TRACK Systems 8821, which has a frequency stability of 1 x 10⁻⁹ over a temperature range of 0 to 50 degrees C and a line voltage/frequency range from 85 to 265 VRMS/48 to 440Hz (See included TRACK Systems 8821 Specifications), insures a frequency stability within tolerance specified in the Rules and Regulations for this service.



4.7 Automatic Level Control Test

Using the test setup of Figure 4-1, the RF input level to the receiver tray was varied with Telsonic 5070-04 and 5010-02 step attenuators. The output level was monitored on the spectrum analyzer and recorded.

Table 4-13. Automatic Level Control Results

RF Input (dBm)	RF Output (dBm)
-62	Shut down
	(below receiver threshold)
-60	+59.5
-58	+59.5
-56	+59.8
-46	+60.0
	(1000 Watts)
- 36	+60.1
-26	+60.2



4.8 Test Equipment

The test equipment that was used to analyze the Axcera LU1000AL system is listed in Table 4-14.

Table 4-14. Test Equipment

MODEL	MANUFACTURER	DESCRIPTION	SERIAL#
TSG95	Tektronix	Video Generator	10346
1450-1	Tektronix	Demodulator	10028
43	Bird	Power Meter	20060
1000E	Bird	1000W Power Meter UHF Element	N/A
8892-300	Bird	50 Ω Termination	2867
5253B	Hewlett-Packard	Frequency Counter	716-18295
VM-700A	Tektronix	Delay and Test Set	10911
ZFM-15	Mini-Circuits	Mixer	N/A
3501	Amber	Distortion Analyzer	20014
1405	Tektronix	Sideband Adapter	20159
7603A	Tektronix	Spectrum Analyzer	20019
8594E	Hewlett-Packard	Spectrum Analyzer	10118
5010-02	Telsonic	Step Attenuator	N/A
5070-04	Telsonic	Step Attenuator	N/A
1265-1300	Axcera	Exciter	N/A
1265-1100	Axcera	Receiver	N/A



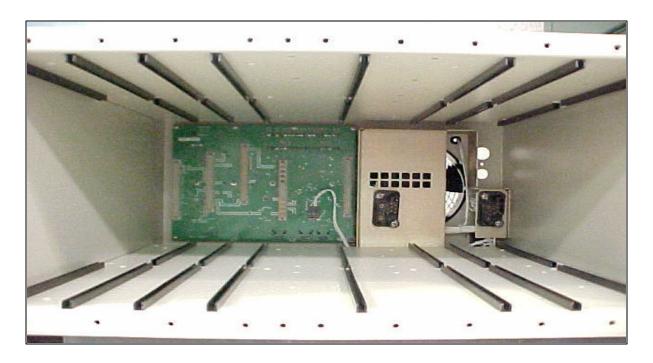
5. USER'S MANUAL

The user's manual for the Axcera-LU1000AL system is provided as separate PDF files.



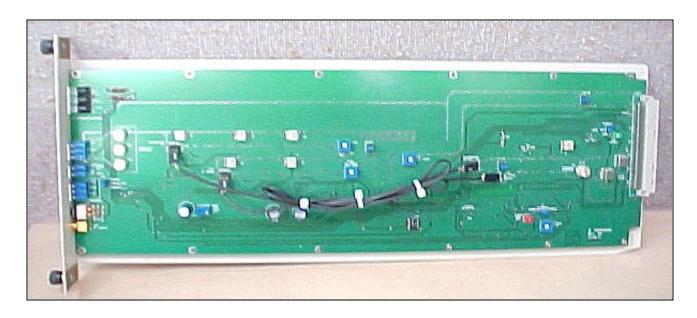
6. INTERNAL PHOTOS

6.1 Front view - Chassis Assembly (Exciter)

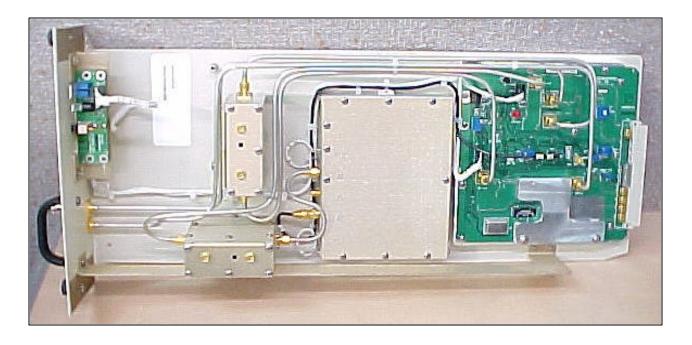




6.2 Side view - IF Processor Assembly (Exciter)



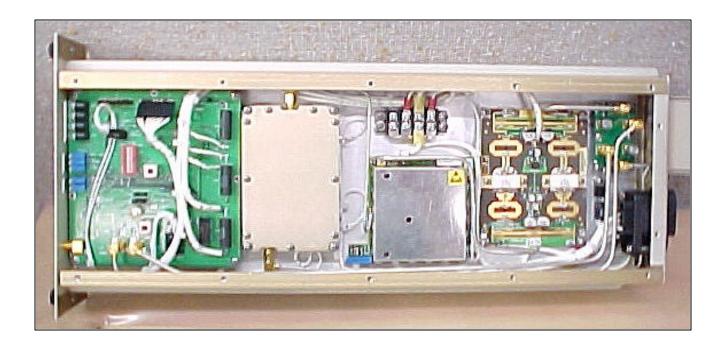
6.3 Side view - L.O. Upconverter Assembly (Exciter)



6.4 Side view - Control / Power Supply Assembly (Exciter)



6.5 Side view - Power Amplifier Assembly (Exciter)

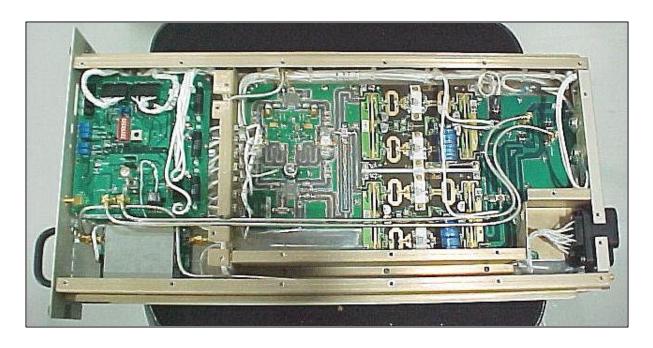




6.6 Front view - Chassis Assembly (External Power Amplifier)



6.7 Side view - Power Amplifier Assembly (External Power Amplifier)

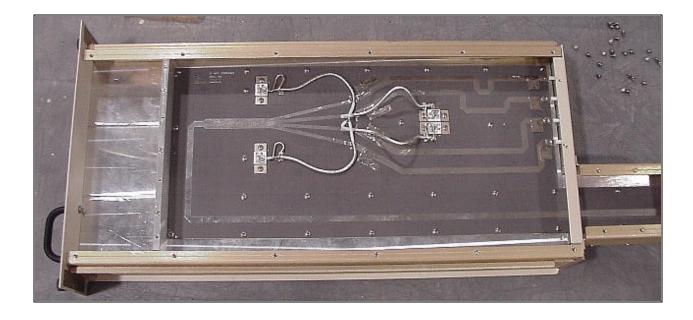




6.8 Side view – Power Supply Assembly (External Power Amplifier)



6.9 Side view – 4-Way Combiner Assembly (External Power Amplifier)





6.10 Top view, Receiver Tray

