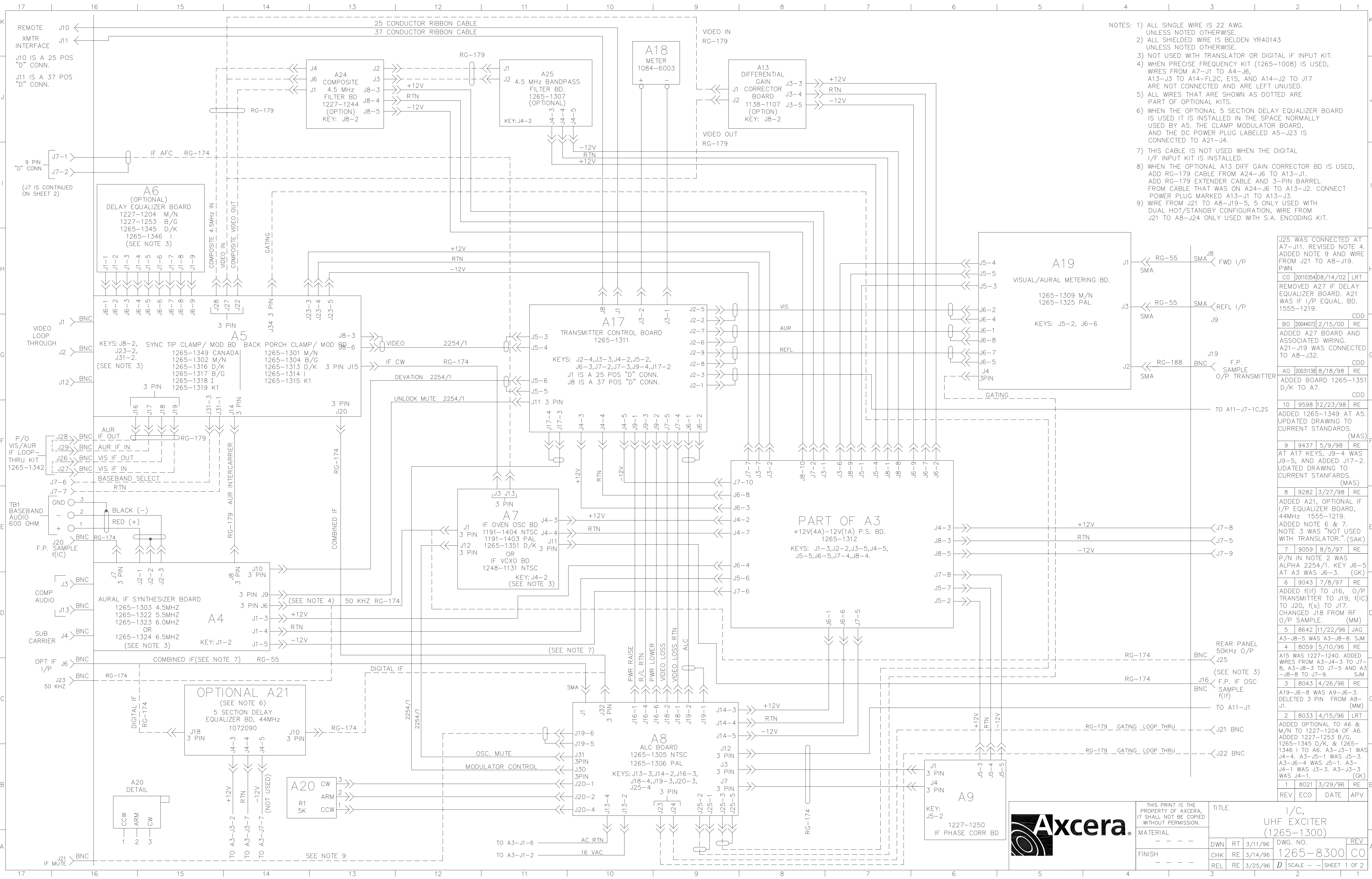




- NOTES: 1) ALL SINGLE WIRE IS 22 AWG. UNLESS NOTED OTHERWISE.
2) ALL SHIELDED WIRE IS BELDEN YR40143 UNLESS NOTED OTHERWISE.
3) NOT USED WITH TRANSLATOR OR DIGITAL IF INPUT KIT.
4) WHEN PRECISE FREQUENCY KIT (1265-1008) IS USED, WIRES FROM A7-J1 TO A4-J6, A13-J3 TO A14-FL2C, E1S, AND A14-J2 TO J17 ARE NOT CONNECTED AND ARE LEFT UNUSED.
5) ALL WIRES THAT ARE SHOWN AS DOTTED ARE PART OF OPTIONAL KITS.
6) WHEN THE OPTIONAL 5 SECTION DELAY EQUALIZER BOARD IS USED IT IS INSTALLED IN THE SPACE NORMALLY USED BY A5. THE CLAMP MODULATOR BOARD, AND THE DC POWER PLUG LABELED A5-J23 IS CONNECTED TO A21-J4.
7) THIS CABLE IS NOT USED WHEN THE DIGITAL I/F INPUT KIT IS INSTALLED.
8) WHEN THE OPTIONAL A13 DIFF GAIN CORRECTOR BD IS USED, ADD RG-179 CABLE FROM A24-J6 TO A13-J1. ADD RG-179 EXTENDER CABLE AND 3-PIN BARREL FROM CABLE THAT WAS ON A24-J6 TO A13-J2. CONNECT POWER PLUG MARKED A13-J1 TO A13-J3.
9) WIRE FROM J21 TO A8-J19-5, 5 ONLY USED WITH DUAL HOT/STANDBY CONFIGURATION. WIRE FROM J21 TO A8-J24 ONLY USED WITH S.A. ENCODING KIT.

J25 WAS CONNECTED AT A7-J11. REVISED NOTE 4. ADDED NOTE 9 AND WIRE FROM J21 TO A8-J19. PWN			
CO	20110354	08/14/02	LRT
REMOVED A27 IF DELAY EQUALIZER BOARD. A21 WAS IF I/P EQUAL. BD. 1555-1219.			
CDD			
BO	2004072	2/15/00	RE
ADDED A27 BOARD AND ASSOCIATED WIRING. A21-J19 WAS CONNECTED TO A8-J32.			
CDD			
AO	20031138	8/18/98	RE
ADDED BOARD 1265-1351 D/K TO A7.			
CDD			
10	9598	12/23/98	RE
ADDED 1265-1349 AT A5. UPDATED DRAWING TO CURRENT STANDARDS.			
(MAS)			
9	9437	5/9/98	RE
AT A17 KEYS, J9-4 WAS J9-5, AND ADDED J17-2. UDATED DRAWING TO CURRENT STANFARDS.			
(MAS)			
8	9282	3/27/98	RE
ADDED A21, OPTIONAL IF I/P EQUALIZER BOARD, 44MHZ 1555-1219. ADDED NOTE 6 & 7. NOTE 3 WAS "NOT USED WITH TRANSLATOR.". (SAK)			
7	9059	8/5/97	RE
P/N IN NOTE 2 WAS ALPHA 2254/1. KEY J6-5 AT A3 WAS J6-3. (GK)			
6	9043	7/8/97	RE
ADDED f(i) TO J16. O/P TRANSMITTER TO J19. f(i) TO J20, f(s) TO J17. CHANGED J18 FROM RF O/P SAMPLE. (MM)			
5	8642	11/22/96	JAG
A3-J8-5 WAS A3-J8-8. SUM			
4	8059	5/10/96	RE
A15 WAS 1227-1240. ADDED WIRES FROM A3-J4-3 TO J7-8, A3-J8-3 TO J7-5 AND A3-J8-8 TO J7-9. SUM			
3	8043	4/26/96	RE
A19-J6-8 WAS A9-J6-3. DELETED 3 PIN FROM A8-J1. (MM)			
2	8033	4/15/96	LRT
ADDED OPTIONAL TO A6 & M/N TO 1227-1204 OF A6. ADDED 1227-1253 B/G, 1265-1345 D/K, & 1265-1346 I TO A6. A3-J3-1 WAS J4-4. A3-J5-1 WAS J5-3. A3-J6-4 WAS J5-1. A3-J4-1 WAS J3-3. A3-J3-3 WAS J4-1. (GK)			
1	8021	3/29/96	RE
REV	ECO	DATE	APV



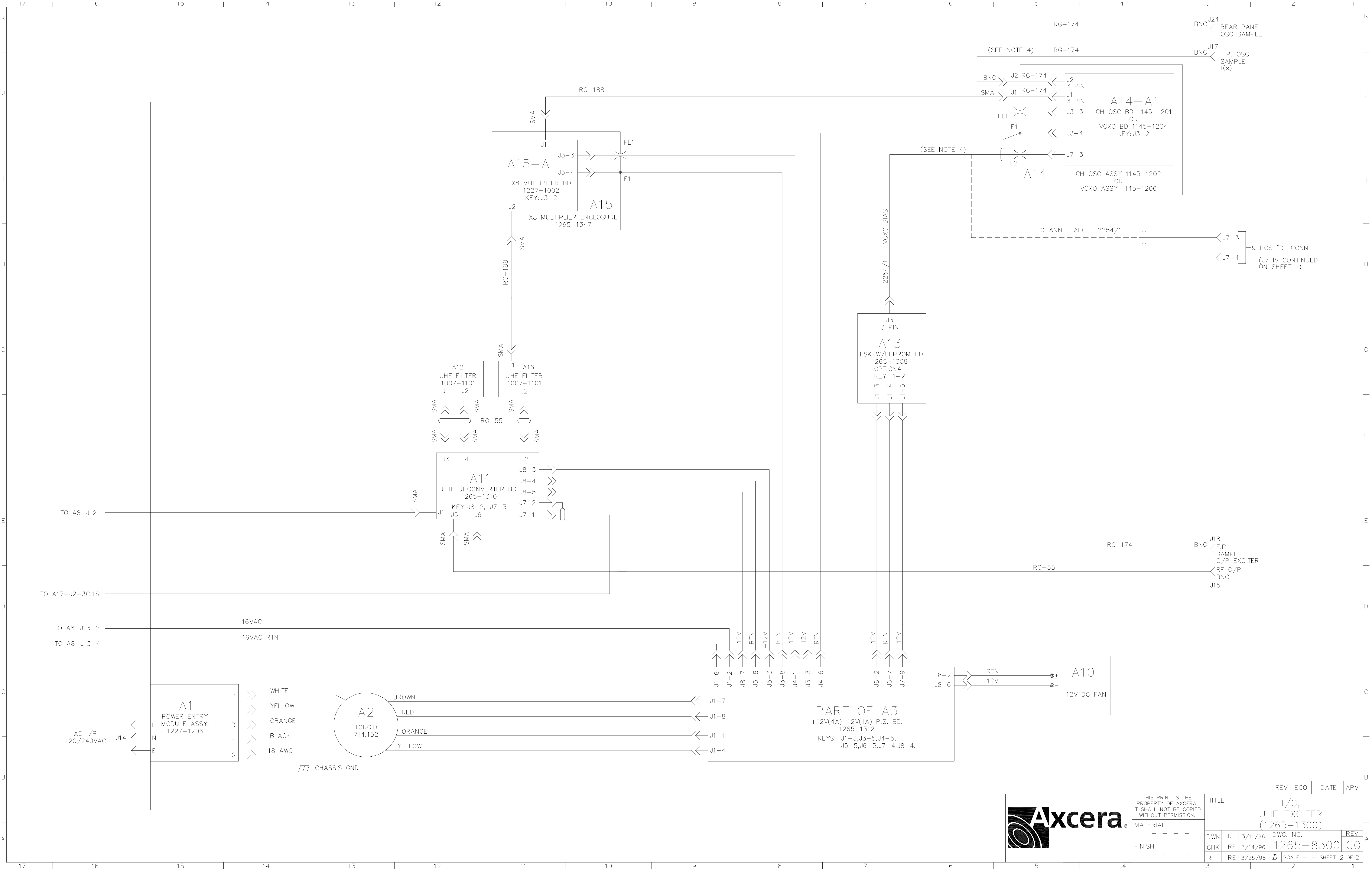
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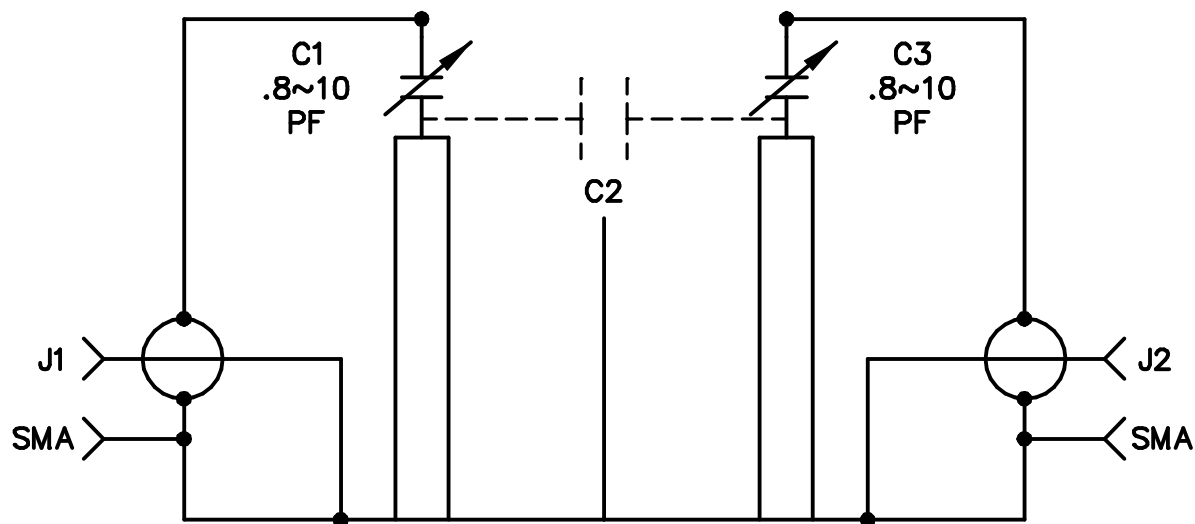
MATERIAL - - - -

FINISH - - - -

TITLE I/C, UHF EXCITER (1265-1300)

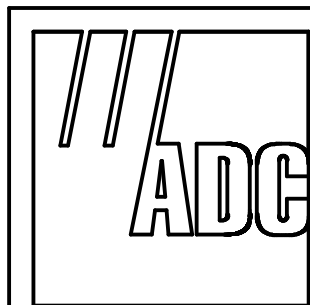
DWN	RT	3/11/96	DWG. NO.	REV
CHK	RE	3/14/96	1265-8300	C0
REL	RE	3/25/96	D	SCALE - - SHEET 1 OF 2





NOTE: C2 IS STRAY CAPACITANCE
BETWEEN LINE SECTIONS.

REV	ECO	DATE	APV
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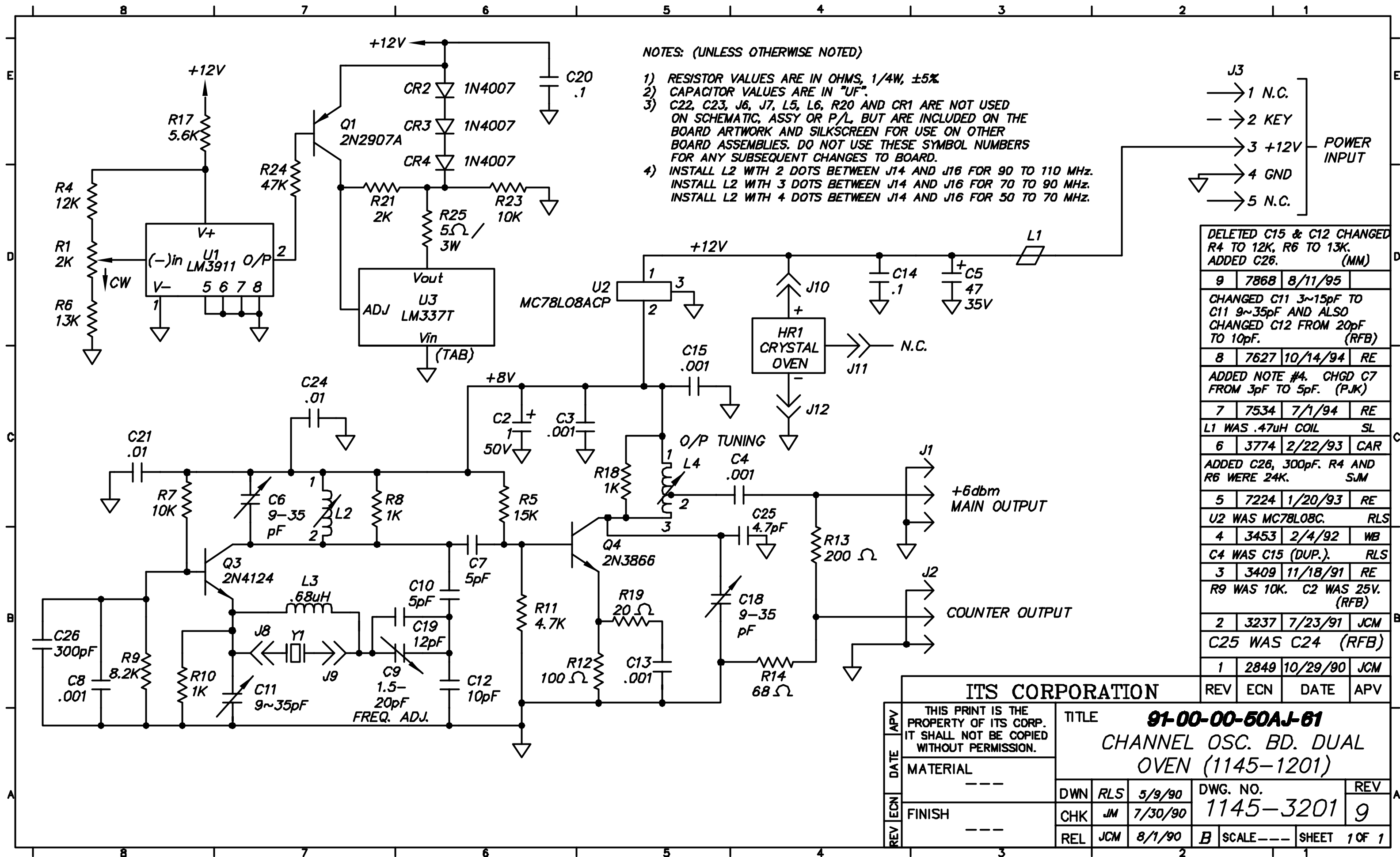
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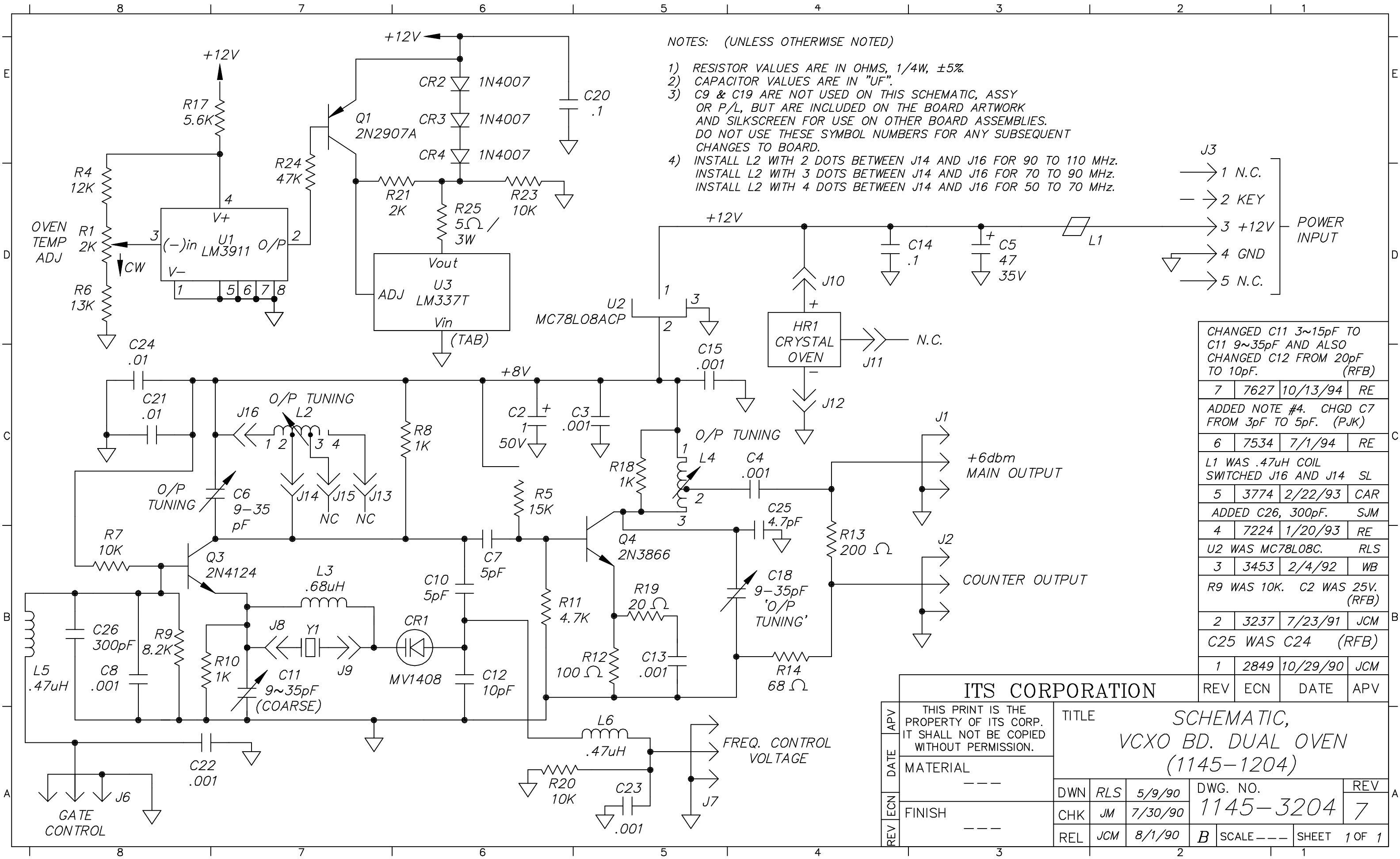
MATERIAL

FINISH

TITLE **91-00-00-50Z-61**
UHF FILTER

DWN	DD	9/13/83	DWG. NO.	REV
CHK	RWZ	9/13/83	1007-3101	0
REL	RWZ	7/20/90	A SCALE ---	SHEET 1 OF 1





- NOTES: (UNLESS OTHERWISE NOTED)
- 1) RESISTOR VALUES ARE IN OHMS, 1/4W, $\pm 5\%$.
 - 2) CAPACITOR VALUES ARE IN "UF".
 - 3) C9 & C19 ARE NOT USED ON THIS SCHEMATIC, ASSY OR P/L, BUT ARE INCLUDED ON THE BOARD ARTWORK AND SILKSCREEN FOR USE ON OTHER BOARD ASSEMBLIES. DO NOT USE THESE SYMBOL NUMBERS FOR ANY SUBSEQUENT CHANGES TO BOARD.
 - 4) INSTALL L2 WITH 2 DOTS BETWEEN J14 AND J16 FOR 90 TO 110 MHz.
INSTALL L2 WITH 3 DOTS BETWEEN J14 AND J16 FOR 70 TO 90 MHz.
INSTALL L2 WITH 4 DOTS BETWEEN J14 AND J16 FOR 50 TO 70 MHz.

CHANGED C11 3~15pF TO
C11 9~35pF AND ALSO
CHANGED C12 FROM 20pF
TO 10pF. (RFB)

7	7627	10/13/94	RE
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ADDED NOTE #4. CHGD C7
FROM 3pF TO 5pF. (PJK)

6	7534	7/1/94	RE
---	------	--------	----

L1 WAS .47uH COIL
SWITCHED J16 AND J14 SL

5	3774	2/22/93	CAR
---	------	---------	-----

ADDED C26, 300pF. SJM

4	7224	1/20/93	RE
---	------	---------	----

U2 WAS MC78L08C. RLS

3	3453	2/4/92	WB
---	------	--------	----

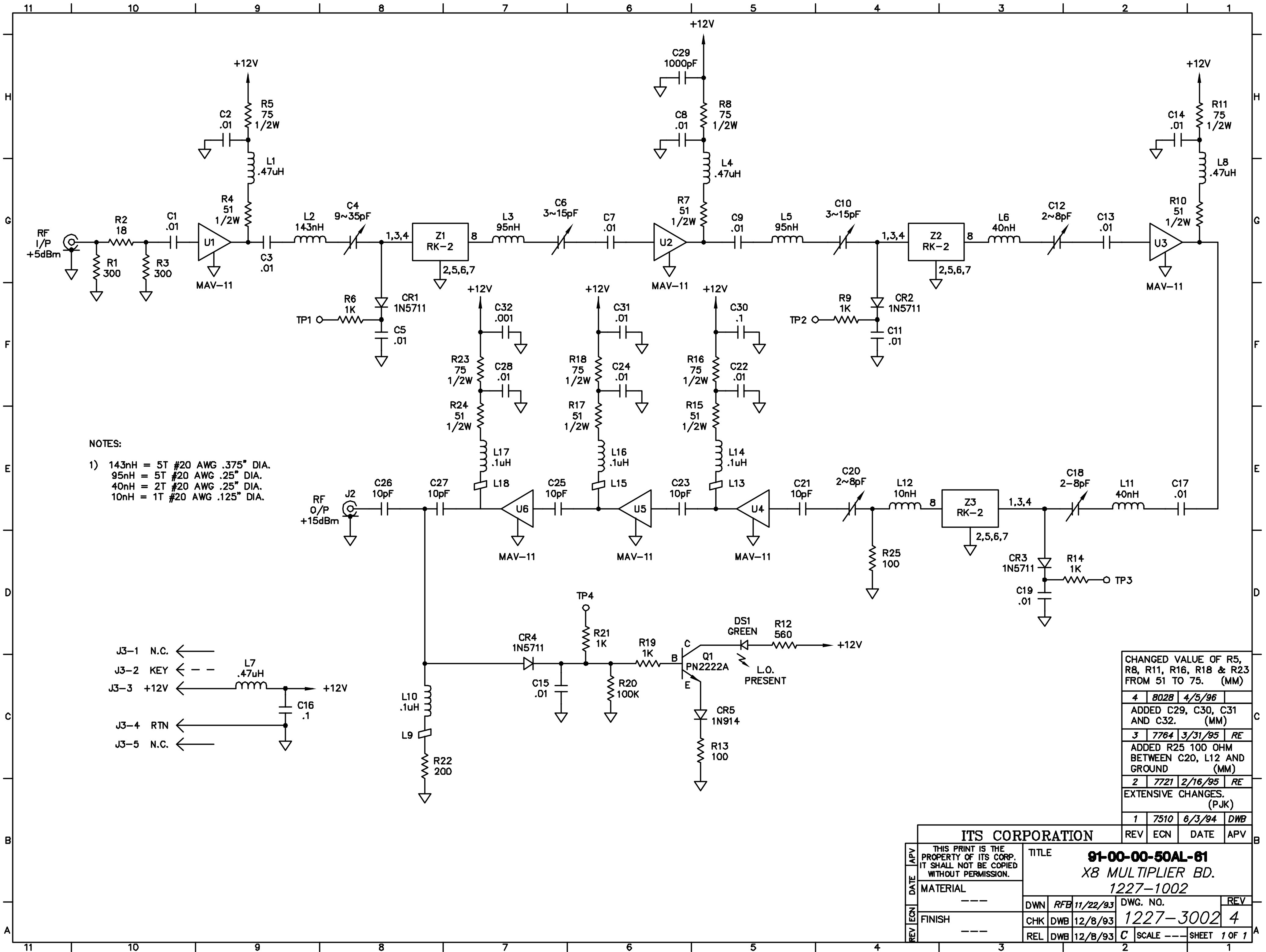
R9 WAS 10K. C2 WAS 25V.
(RFB)

2	3237	7/23/91	JCM
---	------	---------	-----

C25 WAS C24 (RFB)

1	2849	10/29/90	JCM
---	------	----------	-----

ITS CORPORATION				REV	ECN	DATE	APV
THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.				TITLE SCHEMATIC, VCXO BD. DUAL OVEN (1145-1204)			
				DWG. NO. 1145-3204			
MATERIAL ---				DWN	RLS	5/9/90	REV 7
FINISH ---				CHK	JM	7/30/90	
				REL	JCM	8/1/90	
				B	SCALE ---	SHEET 1 OF 1	

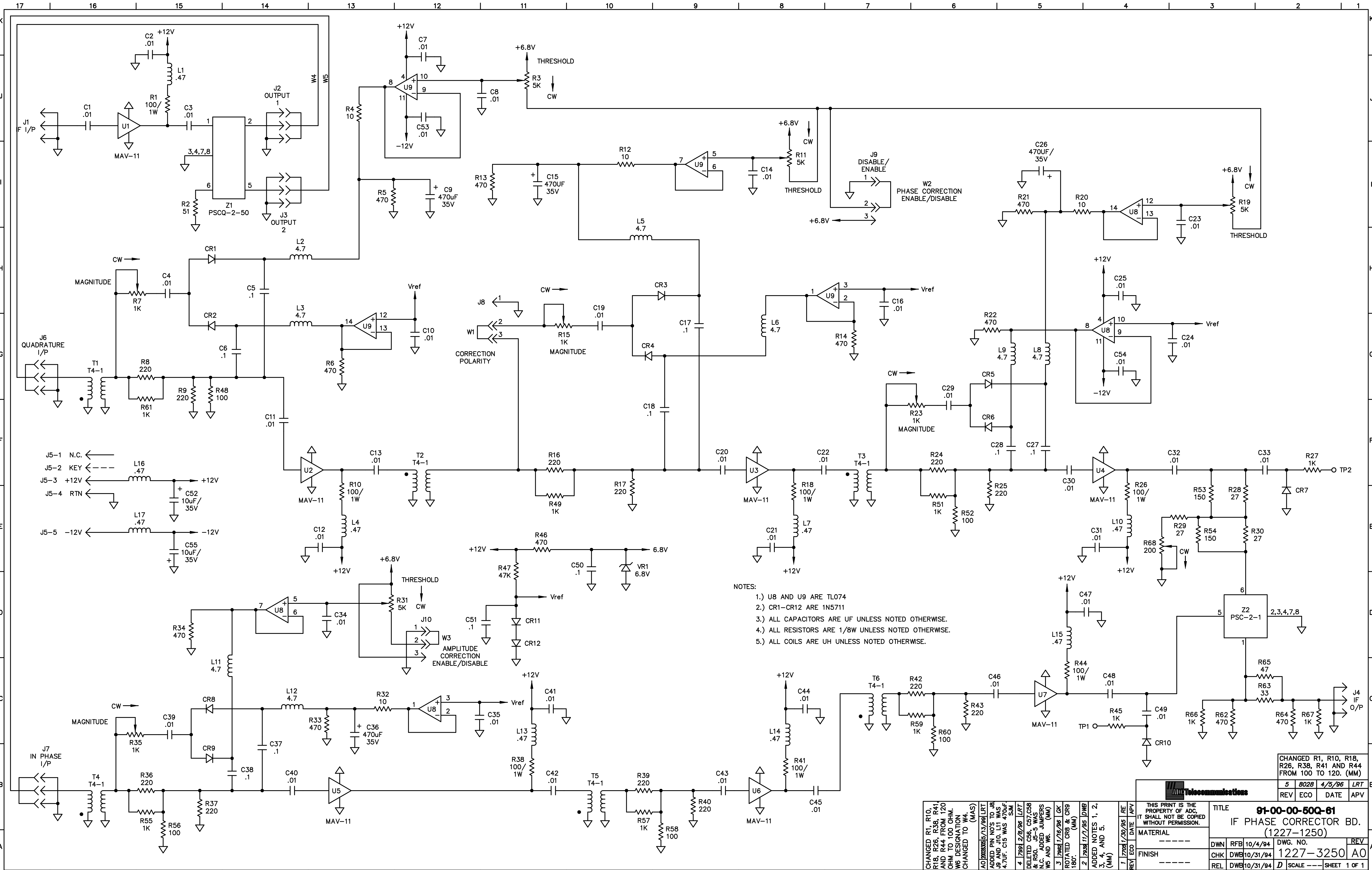


NOTES:
1) 143nH = 5T #20 AWG .375" DIA.
95nH = 5T #20 AWG .25" DIA.
40nH = 2T #20 AWG .25" DIA.
10nH = 1T #20 AWG .125" DIA.

J3-1 N.C. ←
J3-2 KEY ← --
J3-3 +12V ←
J3-4 RTN ←
J3-5 N.C. ←

CHANGED VALUE OF R5, R8, R11, R16, R18 & R23 FROM 51 TO 75. (MM)			
4	8028	4/5/96	
ADDED C29, C30, C31 AND C32. (MM)			
3	7764	3/31/95	RE
ADDED R25 100 OHM BETWEEN C20, L12 AND GROUND (MM)			
2	7721	2/16/95	RE
EXTENSIVE CHANGES. (PJK)			
1	7510	6/3/94	DWB

ITS CORPORATION			
REV	ECN	DATE	APV
1	7510	6/3/94	DWB
TITLE 91-00-00-50AL-61 X8 MULTIPLIER BD. 1227-1002			
MATERIAL ---		DWN RFB 11/22/93	DWG. NO.
FINISH ---		CHK DWB 12/8/93	1227-3002
		REL DWB 12/8/93	4
		C	SCALE --- SHEET 1 OF 1



- NOTES:
- 1.) U8 AND U9 ARE TL074
 - 2.) CR1-CR12 ARE 1N5711
 - 3.) ALL CAPACITORS ARE UF UNLESS NOTED OTHERWISE.
 - 4.) ALL RESISTORS ARE 1/8W UNLESS NOTED OTHERWISE.
 - 5.) ALL COILS ARE UH UNLESS NOTED OTHERWISE.

CHANGED R1, R10, R18, R26, R38, R41 AND R44 FROM 120 OHM TO 100 OHM. W6 DESIGNATION CHANGED TO W4. (MAS)

AO 170205/5/13/99 LRT
ADDED PIN NOS TO J8, J9 AND J10. L11 WAS 470UF. C15 WAS 470UF. SUM

4 17991 2/9/96 LRT
DELETED C56, C57, C58 & R50. J5-5 WAS N.C. ADDED JUMPERS W5 AND W6. (MM)

3 17991 1/16/96 LK
ROTATED CR8 & CR9 180°. (MM)

2 17991 1/17/95 DWB
ADDED NOTES 1, 2, 3, 4, AND 5. (MM)

1 17769 1/30/95 RE
REV ECO DATE APV

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MATERIAL
FINISH

5 8028 4/5/96 LRT
REV ECO DATE APV

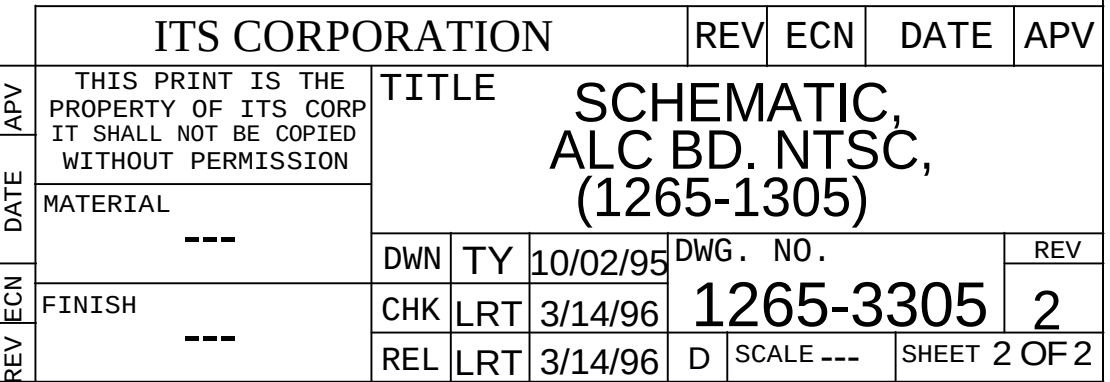
TITLE
91-00-00-50Q-61
IF PHASE CORRECTOR BD.
(1227-1250)

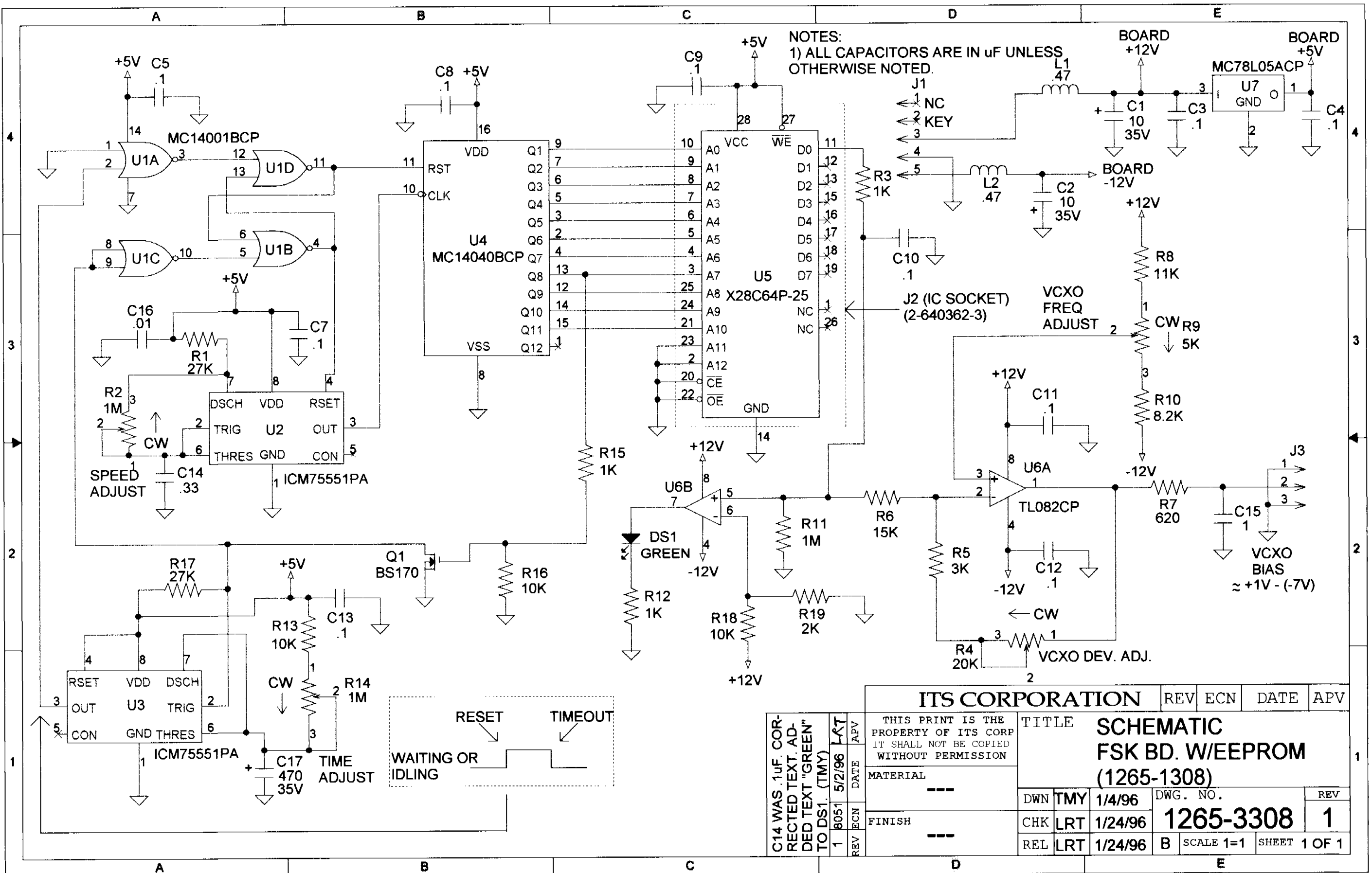
DWN RFB 10/4/94
CHK DWB 10/31/94
REL DWB 10/31/94

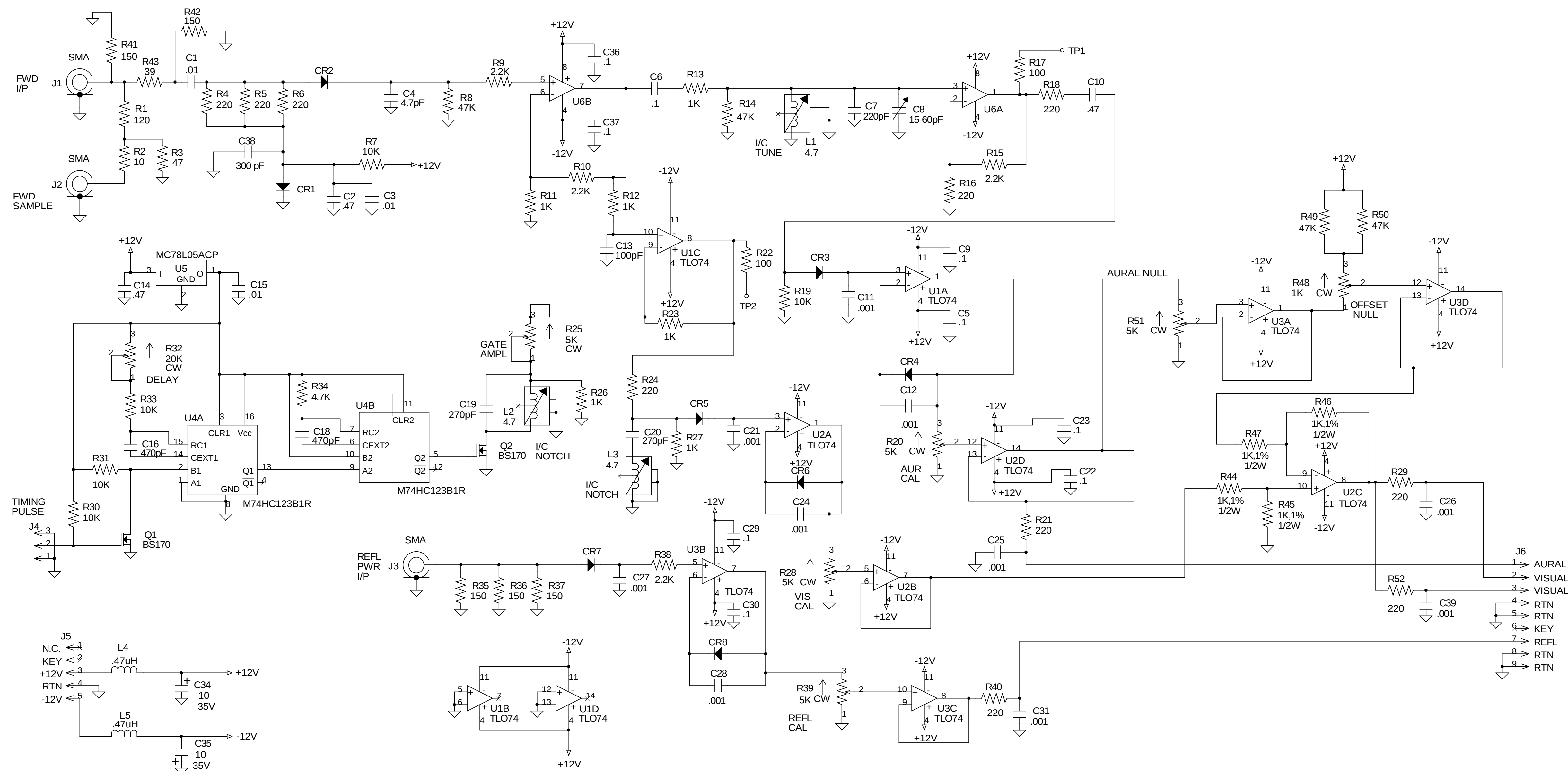
DWG. NO.
1227-3250

REV
A0

SCALE --- SHEET 1 OF 1





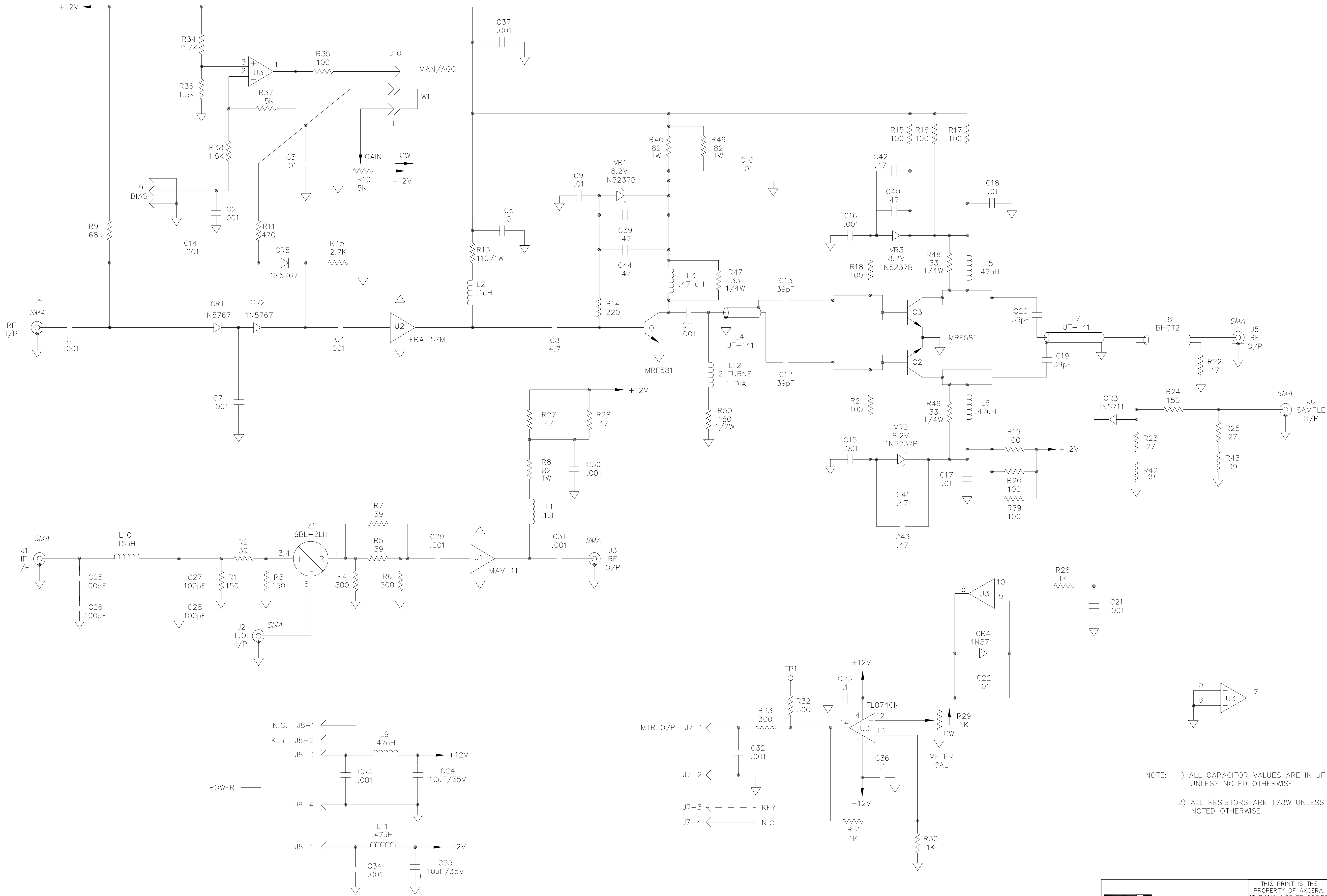


NOTES:

- 1) ALL RESISTORS ARE 1/8 WATT UNLESS NOTED OTHERWISE.
- 2) ALL CAPACITORS ARE μ F UNLESS NOTED OTHERWISE.
- 3) ALL INDUCTORS ARE μ H UNLESS NOTED OTHERWISE.
- 4) ALL DIODES ARE 5082-2811 UNLESS NOTED OTHERWISE.
- 5) ALL TRANSISTORS ARE BS170 UNLESS NOTED OTHERWISE.
- 6) U6 IS HA50231B OR LT12 29C58.

ADDED R52 AND C39. (PJK)			
A0	20092325	5/31/01	LRT
R8 WAS 4.7K. (TMY)			
5	8470	9/30/96	LRT
R7 WAS 100K. (PJK)			
4	8408	9/9/96	JAG
ADDED R41 - R51, & C38. R13 WAS 10K, R19 WAS 2.2K, R7 WAS 10K. CHGD U1D TO U6B. CHGD U1D TO U1A. ADDED CIRCUIT TO U2B & U2D. ENLARGED TO "D" SIZE DWG. (TMY)			
3	8052	7/16/96	JAG
P7 WAS 1600F. J6 WAS A 7 PIN CONNECTOR. (PJK)			
2	8026	4/2/96	LRT
ADDED U6, C36, C37. ADDED NOTE #6. U5 WAS M7405ACN. U5 WAS CONNECTED TO J5-1. U6A WAS U1A. U1A WAS U1B. U2D WAS U2A. U2A WAS U2B. U2B WAS U3D. (PJK)			
1	7999	3/4/96	LRT

APR	ADC Telecommunications				REV	ECO	DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF ADC. IT SHALL NOT BE COPIED WITHOUT PERMISSION				TITLE 91-00-00-50AC-61			
ECO					SCH., VIS/AURAL MTRNG BD (1265-1309)			
REV	MATERIAL -----				DWN	PJK	12/21/95	DWG. NO. 1265-3309
	FINISH -----				CHK	DWB	1/3/96	REV
					REL	DWB	1/3/96	D
					SCALE --- SHEET 1 OF 1			



REMOVE R12 AND R44. CHANGE R13 FROM 82 OHMS TO 110 OHMS. CHANGE U2 FROM MAV-11 TO ERA-5SM. PWN			
B0	20110428	09/16/02	LRT
ADDED C42-C44, L12, R47-R50. C39 - C41 WERE 1uF. REMOVED C6 FROM THE OUTPUT OF U2. C8 WAS 3pF. (PJK)			
A0	20028596	6/1/99	LRT
CHANGED C8 FROM 1.8-10pF TO 3pF. ADDED R46 AND DELETED C38. (RFB)			
4	9279	1/23/98	LTR
ADDED IT-5 CY30C105P CAP AT C39. C40 AND C41 IN PARALLEL WITH VR1, VR3 AND VR2. (MAS)			
3	8816	1/23/97	LRT
DELETED R41 AND ADDED C38. (RFB)			
2	8635	11/18/96	LRT
ADDED "SMA" TO J1-J3, J5, J6. ADDED "uH" TO L1. ADDED "KEY" TO J7-3. "N.C." TO J7-4. C8 WAS .8-10pF. (PJK)			
1	8035	4/11/96	LRT
REV	ECO	DATE	APV

NOTE: 1) ALL CAPACITOR VALUES ARE IN uF UNLESS NOTED OTHERWISE.
2) ALL RESISTORS ARE 1/8W UNLESS NOTED OTHERWISE.



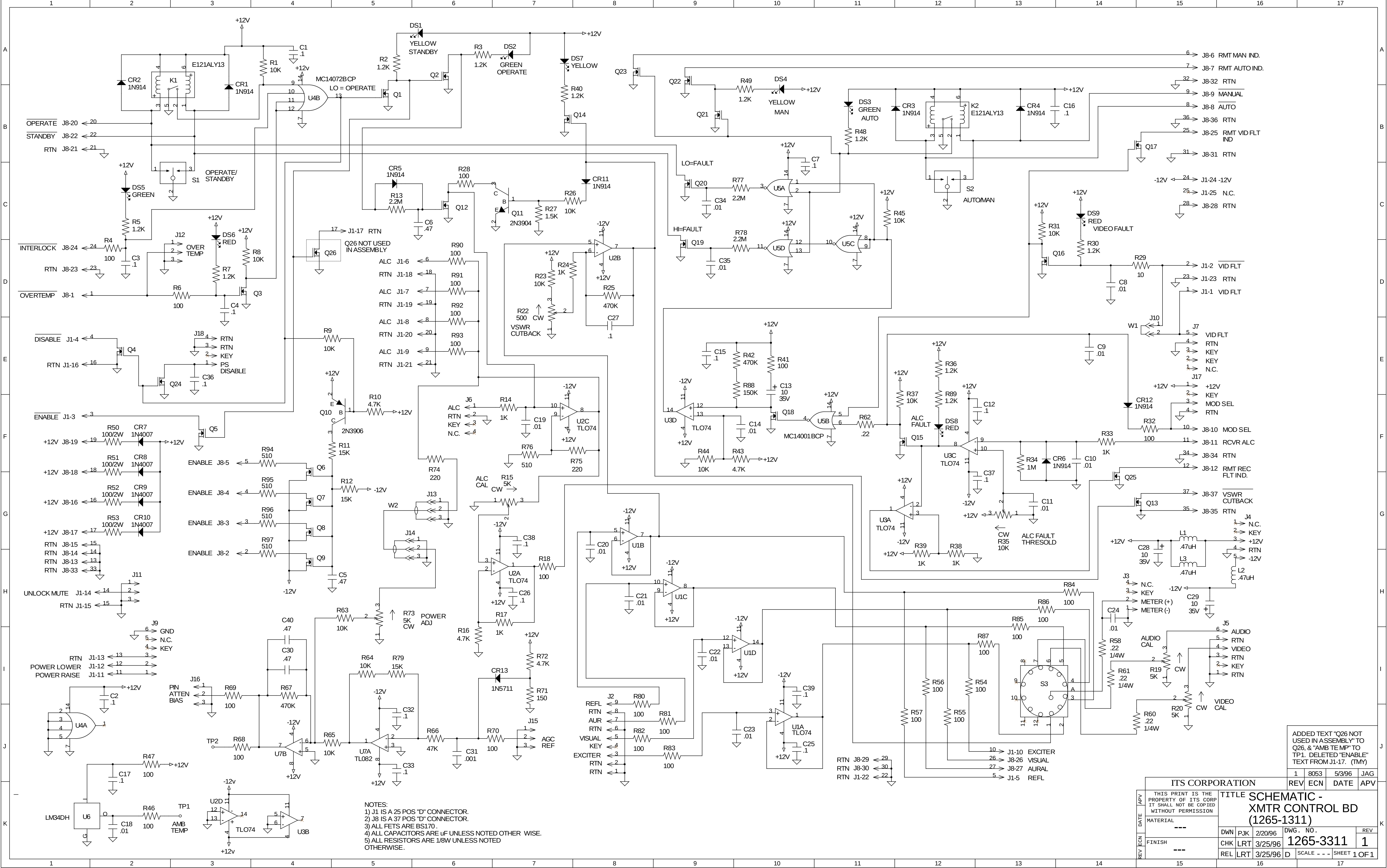
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MATERIAL

FINISH

TITLE
91-00-00-50R-61
UHF UPCONVERTER BD.

DWN	PJK	1/25/96	DWG. NO.	REV
CHK	LRT	3/14/96	1265-3310	B0
REL	LRT	3/14/96	SCALE 1=1	SHEET 1 OF 1



NOTES:
1) J1 IS A 25 POS "D" CONNECTOR.
2) J8 IS A 37 POS "D" CONNECTOR.
3) ALL FETS ARE BS170.
4) ALL CAPACITORS ARE uF UNLESS NOTED OTHER WISE.
5) ALL RESISTORS ARE 1/8W UNLESS NOTED OTHERWISE.

ADDED TEXT "Q26 NOT USED IN ASSEMBLY" TO Q26, & "AMB TEMP" TO TP1. DELETED "ENABLE" TEXT FROM J1-17. (TMY)

ITS CORPORATION			
REV	ECN	DATE	APV
1	8053	5/3/96	JAG
TITLE SCHEMATIC - XMTR CONTROL BD (1265-1311)			
APV	DATE	MATERIAL	REV
---	---	---	---
FINISH	---	---	---
REL	LRT	3/25/96	D
16	17	SCALE - - -	SHEET 1 OF 1

