

TABLE OF CONTENTS

CHAPTER 1 ID LABEL/LOCATION INFO

1.1 Rear Panel FCC Label (Translator)	1-1
1.2 Rear Panel Manufacturer's Label (Translator)	1-1
1.3 Front view, complete Axcera-836A Translator	1-2
1.4 Rear view, Axcera-836A Translator w/FCC ID Label	1-3
1.5 Rear view, Axcera-836A Translator w/Manufacturer's Label	1-3

CHAPTER 2 EXTERNAL PHOTOS

2.1 Front view, complete Axcera-836A	2-1
2.2 Rear view, complete Axcera-836A Translator	2-2

CHAPTER 3 Schematics

CHAPTER 4 TEST REPORT

4.1 RF Power Measurements	4-1
4.2 Modulation Characteristics	4-2
4.2.1 Video Modulation	4-2
4.2.2 Video Envelope Delay	4-3
4.2.3 Video Noise	4-3
4.2.4 Video Frequency Response	4-3
4.2.5 RF Sideband Response	4-4
4.2.6 Audio Modulation	4-6
4.2.7 AM and FM Noise	4-7
4.3 Occupied Bandwidth	4-7
4.4 Conducted Spurious Emissions	4-9
4.5 Radiated Emissions	4-10
4.6 Frequency Stability	4-12
4.7 Automatic Level Control Test	4-14
4.8 Test Equipment	4-15

CHAPTER 5 USER'S MANUAL

CHAPTER 6 INTERNAL PHOTOS

6.1 Top view, UHF Exciter Tray	6-1
6.2 Side view, UHF Amplifier Tray (left side)	6-2
6.3 Side view, UHF Amplifier Tray (right side)	6-2
6.4 Top view, Receiver Tray	6-3

CHAPTER 7 PARTS LIST/TUNE-UP INFO

7.1 Parts List	7-1
7.2 Tune-up Information	7-1
7.2.1 RF Input Signal Path (Receiver)	7-2
7.2.2 Local Oscillator Signal Path (Receiver)	7-2
7.2.3 Combined IF Signal Path (Receiver)	7-2
7.2.4 Frequency Correction Option (Receiver)	7-3
7.2.5 Voltages for Operation of the Tray (Receiver)	7-4
7.2.6 +12VDC for External Preamplifier (Receiver)	7-4
7.2.6.1 UHF/VHF Filter, DC Multiplexed	7-4

7.2.6.2 Dual Stage Amplifier Board	7-4
7.2.6.3 UHF/VHF High-band/VHF Low-band Filter	7-5
7.2.6.4 Channel Oscillator Assembly, Dual Oven.....	7-5
7.2.6.5 x8/x4/x2 Multiplier Board	7-5
7.2.6.6 UHF/VHF High-band/VHF Low-band Filter	7-6
7.2.6.7 Downconverter/Amplifier Board.....	7-6
7.2.6.8 IF Filter/ALC Board	7-7
7.2.6.9 SAW Filter/Amplifier Board	7-7
7.2.6.10 ± 12 VDC Power Supply Board	7-7
7.2.7 IF Signal Path (UHF Exciter).....	7-7
7.2.7.1 ALC Board	7-8
7.2.7.2 IF Phase Corrector Board.....	7-8
7.2.7.3 ALC Board, NTSC.....	7-9
7.2.7.4 UHF Upconverter Board	7-9
7.2.7.5 Channel Oscillator Board	7-9
7.2.7.6 x8 Multiplier Board	7-10
7.2.7.7 Visual/Aural Metering Board.....	7-10
7.2.7.8 ± 12 VDC Power SupplyBoard.....	7-11
7.2.8 RF Signal Path (UHF Amplifier Tray).....	7-11
7.2.8.1 Variable Gain/Phase Board	7-11
7.2.8.2 1 Watt UHF Amplifier Board	7-11
7.2.8.3 UHF Filter.....	7-11
7.2.8.4 Single Stage Amplifier Assembly, Class A	7-11
7.2.8.5 Coupler Board Assembly.....	7-11
7.2.8.6 Dual Stage Amplifier Assembly, Class AB.....	7-11
7.2.8.7 4 Way Splitter Assembly.....	7-12
7.2.8.8 Dual Stage Amplifier Assemblies, Class AB.....	7-12
7.2.8.9 4 Way Combiner Assembly	7-13
7.2.8.10 Circulator	7-13
7.2.8.11 Dual Peak Detector Board, Single Supply	7-13
7.2.8.12 Amplifier Protection Board	7-13
7.2.8.13 Amplifier Control Board	7-13
7.2.8.14 Switching Power Supply.....	7-13
7.2.8.15 Calibration of Output Power and VSWR Cutback	7-13
7.2.10 System Alignment	7-14
7.2.10.1 UHF/VHF Receiver Tray	7-14
7.2.10.2 UHF Exciter Tray.....	7-14
7.2.10.3 UHF Amplifier Trays	7-15
7.2.10.4 System Set-up of Linearity	7-16
7.2.10.5 Phase and Gain Adjustment of the UHF Amplifier Trays	7-17
7.2.10.6 Calibration of the Forward Power Level of the Translator.....	7-17
7.2.10.7 Calibration of the Reflected Power Level of the Translator	7-18
7.2.10.8 IF Phase Corrector Adjustment	7-18
7.2.10.9 UHF Power Tees	7-19
7.2.10.10 Bandpass Filter.....	7-19
7.2.10.11 One or Two Section Trap Filter (Optional)	7-19
CHAPTER 8 OPERATIONAL DESCRIPTION	
8.1 General Description	8-1
8.2 Technical Specifications.....	8-1
8.3 Performance Specifications	8-1
8.4 System Overview	8-2
8.4.1 Receiver Tray	8-3

8.4.2 UHF Exciter Tray	8-4
8.4.3 UHF Amplifier Tray	8-7
8.5 Control and Status	8-9
8.5.1 Receiver Tray	8-9
8.5.2 UHF Exciter Tray	8-10
8.5.3 UHF Amplifier Tray	8-11
8.6 Input and Remote Connections	8-13
8.7 AC Input	8-13
8.7.1 Receiver Tray	8-14
8.7.2 UHF Exciter Tray	8-14
8.7.3 UHF Amplifier Tray	8-15
CHAPTER 9 CERTIFICATION OF TEST DATA	