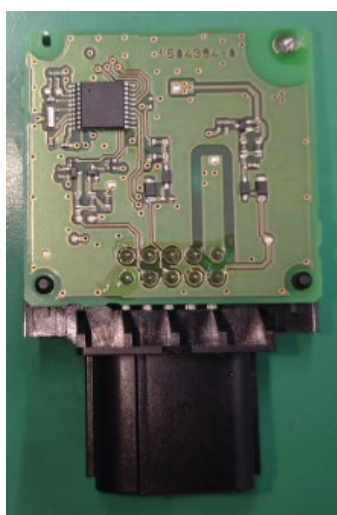


**Figure 8.7 PCB assy (top view)**



**Figure 8.8 PCB assy (bottom view)**