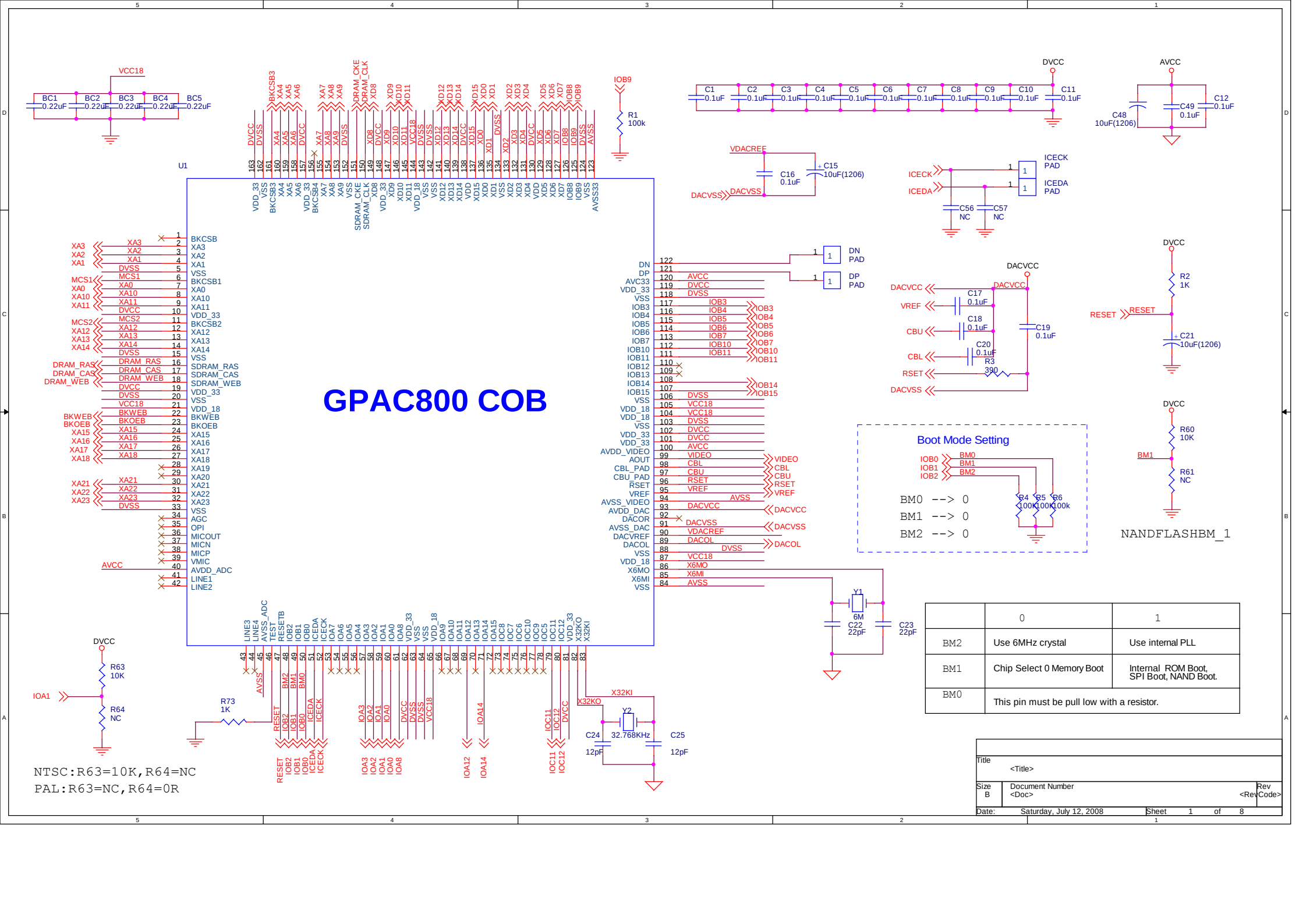

	0	1
BM2	Use 6MHz crystal	Use internal PLL
BM1	Chip Select 0 Memory Boot	Internal ROM Boot, SPI Boot, NAND Boot.
BM0	This pin must be pull low with a resistor.	

Title	<Title>	
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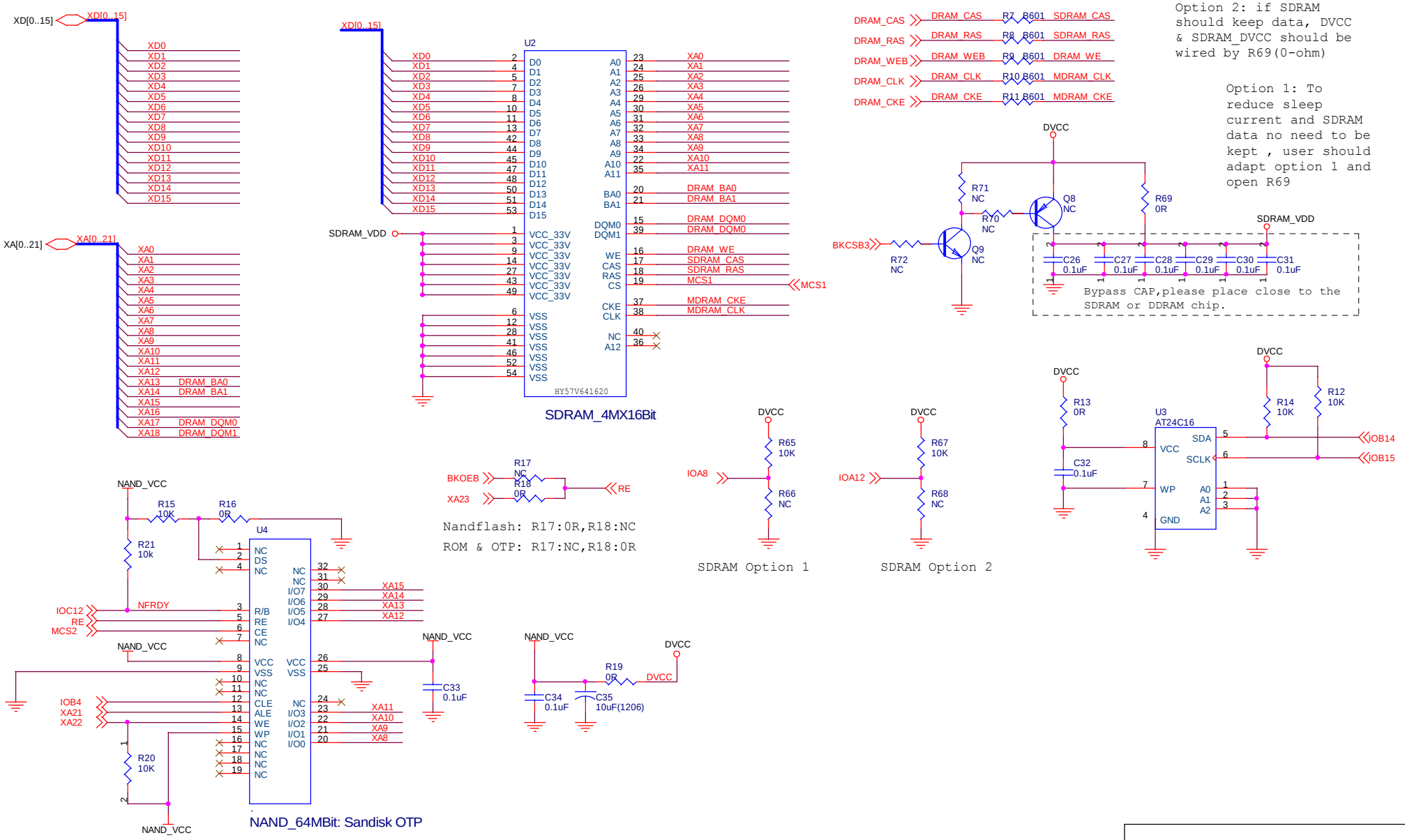

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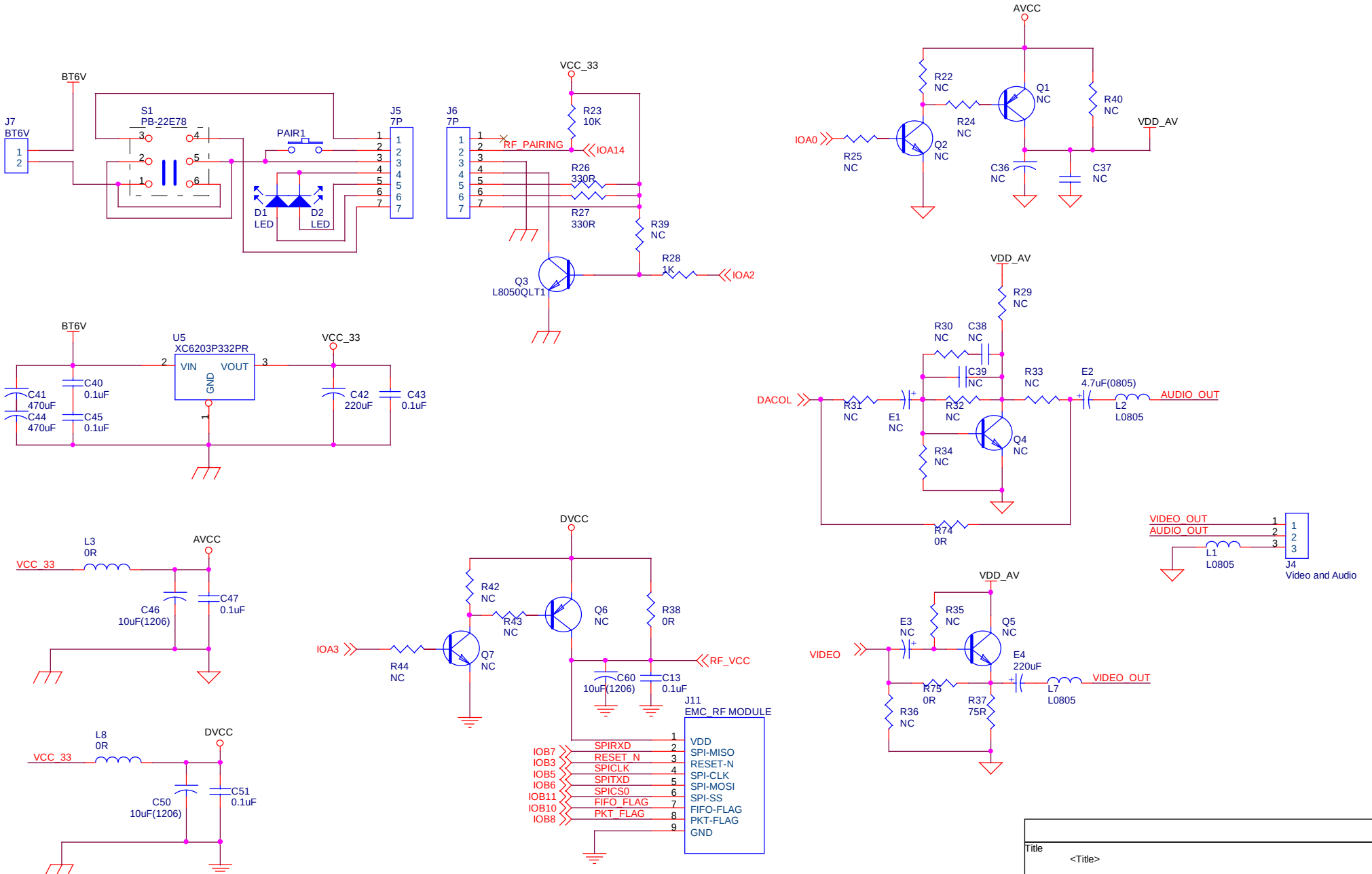

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Date: Saturday, July 12, 2008	Sheet 3 of 8	

6. Application circuit