



雷騰電子產品有限公司
LITECH ELECTRONIC PRODUCTS LTD.

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Date : Jan 13, 2000

To : Intertek Testing Services

Attn : Ben Ho

From : Gordon Wong

Re : Document for FCC Testing (Model: DST01)

Attached please find the labeling artwork, block diagram and the technical description of the transmitter.

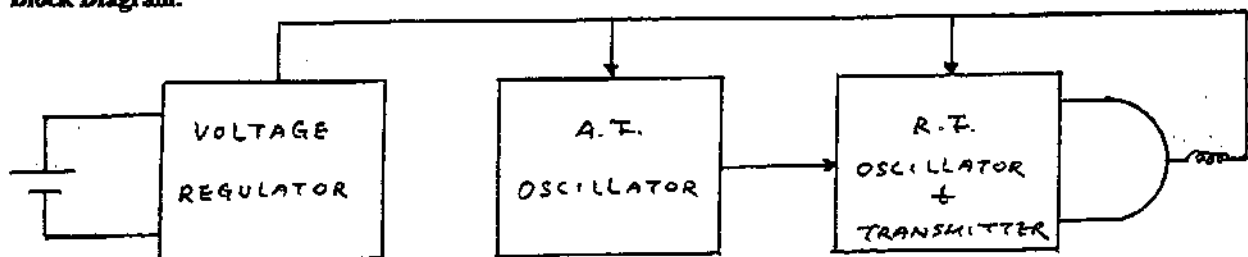
Filing fee US\$ 940 will be send to you early next week.

Circuit description:

The input voltage from 9V battery is being stepped down and regulated by the Voltage Regulator circuit (formed by Q1, ZD1 and R1) to provide a constant 5.5V supply to the Transmitter circuit.

The audio frequency oscillator (formed by IC1, C1, R4 and R5) generate a modulating signal and fed to the RF oscillator & transmitter circuit (formed by Q3, R8, C2 - C6). The modulated RF signal is then send out through the antenna.

Block Diagram:



ETL SEMKO ⑤			
Incoming Date:		14.1.00	
		Action By	
		Ben	

Best Regards,

Gordon