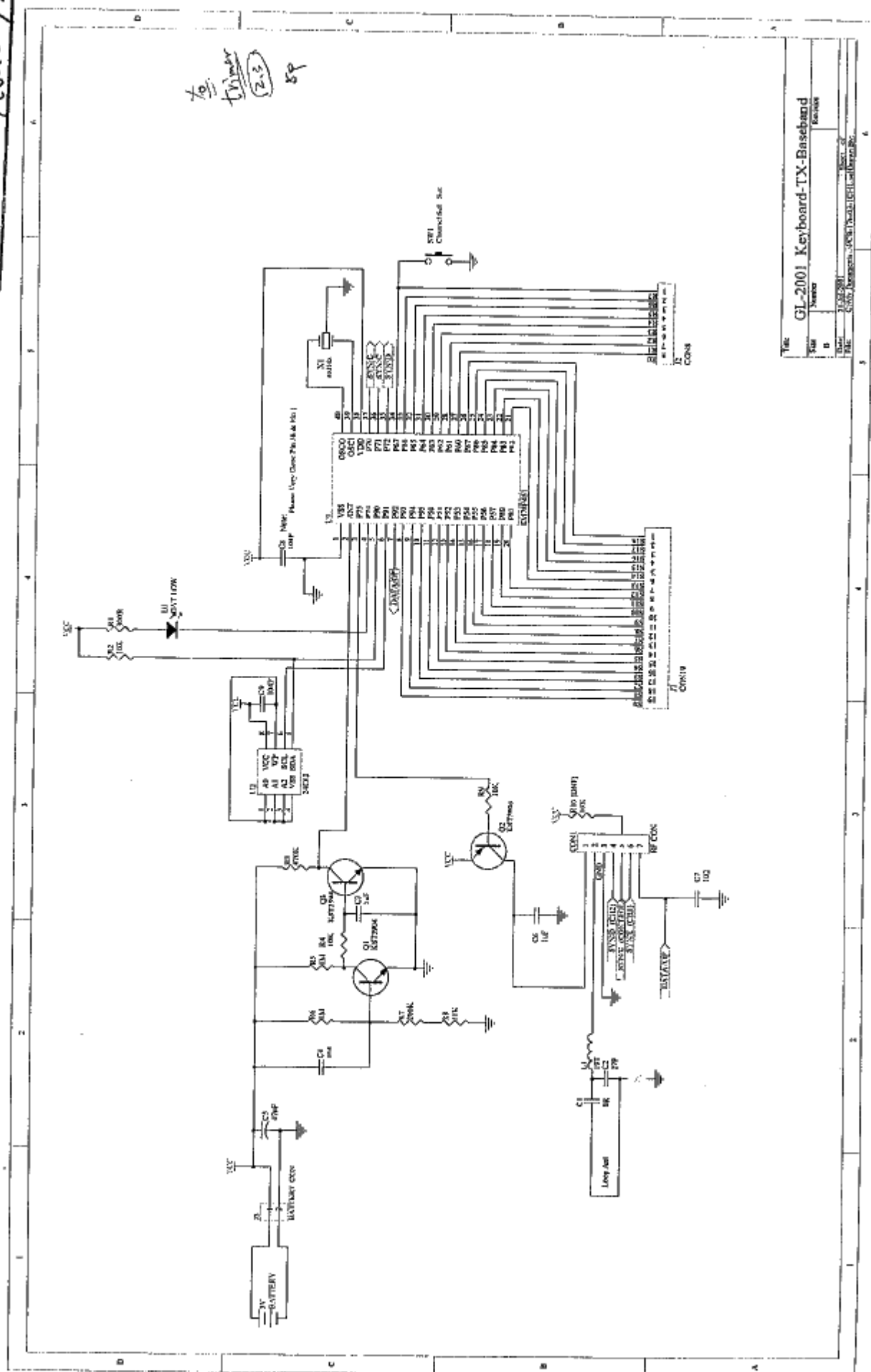
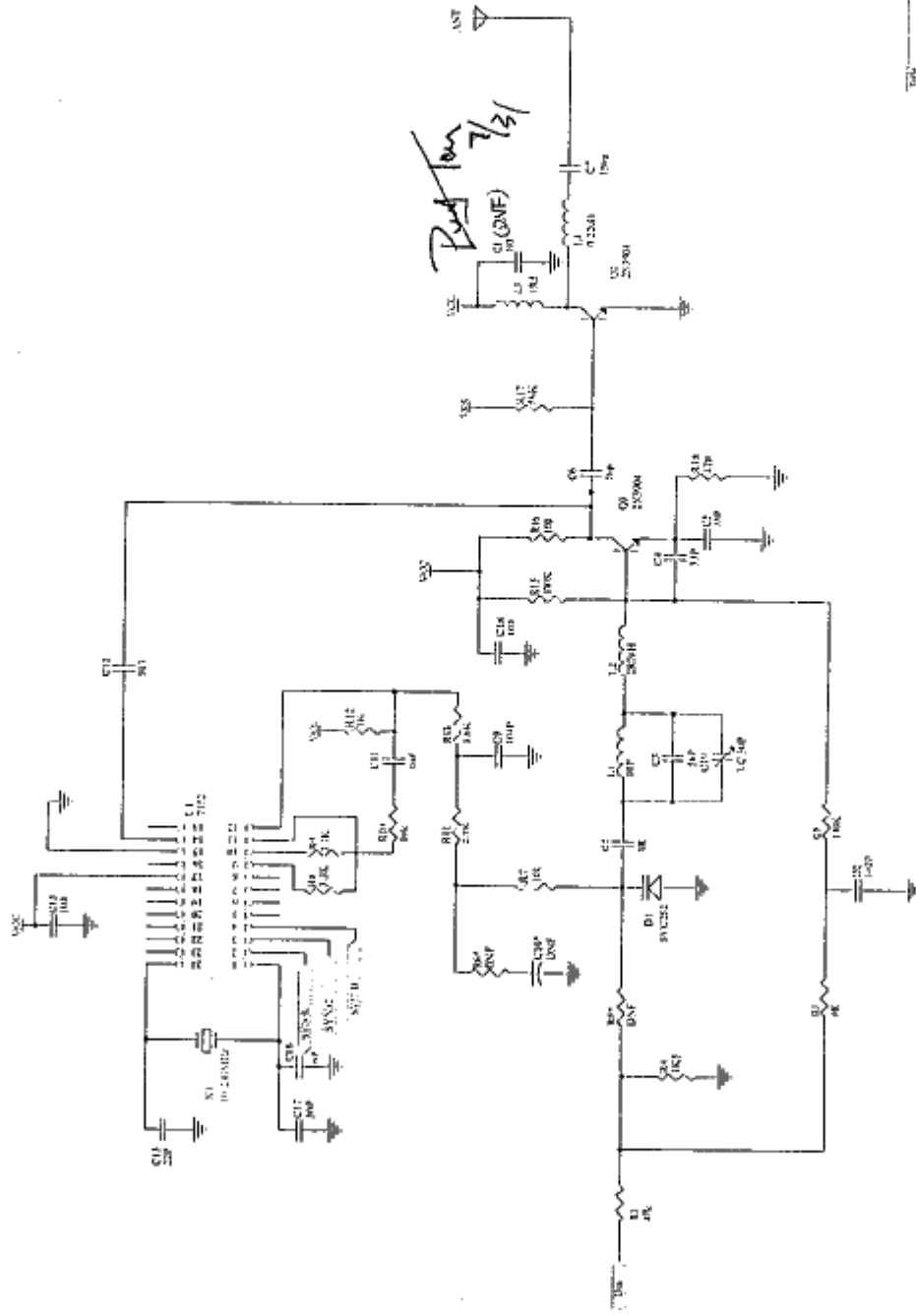


EXHIBIT D
Circuit Diagram

單位主管 *何* $h7/3$
R&D 承辦人 *Stone* $7/31$





PCB-114B-04

單位主管 *[Signature]* 7/12
R&D *[Signature]* 7/12

單位主管人 7/31

R&D 承辦人 Stone 7/31

