

PTM-1000L CONFIGURATION LIST**SYSTEM INFORMATION:**

Customer Name: **FCC Compliance System**

Sales Order Number: **NA**

Shipping Address: **Attn: Tom Mehrkens**

Hub Serial Number: **M005**

Number of Remotes: **2**

Remote Serial Numbers: **566, 567**

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EQUIPMENT CONFIGURATION AS TESTED & SHIPPED

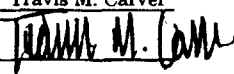
Assembly	Part #	Rev	Description	Ref. Designator	Integration Release Date/Checksum
HUB SN: M005					
IMP	2250-0008-03	11	CPU VxWorks	U50,51,58,59 (FLASH)	6/9/99
IP:			CPU Boot Code	U50,51,58,59 (FLASH)	6/9/99
SN: 07-051599-006			Timing Generation	U19 (PLD)	B9C9
			General Logic	U20 (PLD)	E760
MCA (Master)	2250-0007-05	07	CPU VxWorks	U2-5 (FLASH)	6/9/99
IP:			BMCL FPGA	U48 (FPGA)	
SN: 08-052099-013			RX FPGA	U70 (FPGA)	
			TX FPGA	U61 (FPGA)	
			CPU Boot Code	U2-5 (FLASH)	6/9/99
			Misc. Digital Logic	U26 (PLD)	9C27
MCA	2250-00077-05	07	CPU VxWorks	U2-5 (FLASH)	6/9/99
IP:			BMCL FPGA	U48 (FPGA)	
SN: 08-052099-036			RX FPGA	U70 (FPGA)	
			TX FPGA	U61 (FPGA)	
			CPU Boot Code	U2-5 (FLASH)	6/9/99
			Misc. Digital Logic	U26 (PLD)	9C27
MCA	2250-00077-05	07	CPU VxWorks	U2-5 (FLASH)	6/9/99
IP:			BMCL FPGA	U48 (FPGA)	
SN: 08-052099-042			RX FPGA	U70 (FPGA)	
			TX FPGA	U61 (FPGA)	
			CPU Boot Code	U2-5 (FLASH)	6/9/99
			Misc. Digital Logic	U26 (PLD)	9C27
MCA	2250-00077-05	07	CPU VxWorks	U2-5 (FLASH)	6/9/99
IP:			BMCL FPGA	U48 (FPGA)	
SN: 08-123098-006			RX FPGA	U70 (FPGA)	
			TX FPGA	U61 (FPGA)	
			CPU Boot Code	U2-5 (FLASH)	6/9/99
			Misc. Digital Logic	U26 (PLD)	9C27
IFD	2250-0009-03	01	RCCH	U69 (FPGA)	
SN: 07-101298-005	Dev. 0008		Config Logic	U80 (PLD)	CA63
IFD	NA		RCCH	U69 (FPGA)	
SN: 07-081198-005	2250-0009-04	00	Config Logic	U80 (PLD)	CA63
NET	2250-0020-02	03	TDM Bus I/F RX (SubMB)/TX (Net)	U12,U22 (FPGA)	
SN: 08-070199-004			TDM Bus I/F TX (SubMB)/RX (Net)	U34,U45 (FPGA)	
			IMP I/F	U55 (FPGA)	
			Chip Select for the IMP I/F	U28 (CPLD)	5291
			Chip Select for the IMP I/F	U35 (CPLD)	B494
OC3	2250-0021-02	02	SONET Path Termination	U4 (FPGA)	
SN: 08-042799-003					
10 Base T	NA	NA	NA	NA	NA
SN:					
Atlas Modules	NA	NA	NA	NA	NA
SN:			Module 1		
SN:			Module 2		
SN:			Module 3		
ODB	2250-0041-02	03	NA	NA	NA
SN: 08-050799-010					
PSI	2250-0025-02	03	NA	NA	NA
SN: 07-051399-006					
HUB IDU Fuse BD	2250-0038-01	02			
SN: 07-051399-006					
CTRL Shelf Backplane	2250-0023-03	02	NA	NA	NA
SN: 08-061599-002					

Test Technician: Travis M. CarverSignature: Travis M. CarverDate: 8/5/99

EQUIPMENT CONFIGURATION AS TESTED & SHIPPED

Assembly	Part #	Rev	Description	Ref. Designator	Integration Release Date/Checksum
HUB Master			TRA SN:		
TRX Controller			CPU Code A	U30 (FLASH)	
SN:			CPU Code B	U30 (FLASH)	
			FPGA Code (Analog Subsystem Ctrl)	U43 (FPGA)	
			Boot Code	U30 (FLASH)	
			Config Page	U30 (FLASH)	
			Address Bus Decode	U25 (PLD)	
IFP	NA		NA	NA	NA
SN:					
FE	NA		NA	NA	NA
SN:					
HUB			TRA SN:		
TRX Controller			CPU Code A	U30 (FLASH)	
SN:			CPU Code B	U30 (FLASH)	
			FPGA Code (Analog Subsystem Ctrl)	U43 (FPGA)	
			Boot Code	U30 (FLASH)	
			Config Page	U30 (FLASH)	
			Address Bus Decode	U25 (PLD)	
IFP	NA		NA	NA	NA
SN:					
FE			NA	NA	NA
SN:					
Remote			TRA SN:		
TRX Controller			CPU Code A	U30 (FLASH)	
SN:			CPU Code B	U30 (FLASH)	
			FPGA Code (Analog Subsystem Ctrl)	U43 (FPGA)	
			Boot Code	U30 (FLASH)	
			Config Page	U30 (FLASH)	
			Address Bus Decode	U25 (PLD)	
IFP	NA		NA	NA	NA
SN:					
FE			NA	NA	NA
SN:					
Remote			TRA SN:		
TRX Controller			CPU Code A	U30 (FLASH)	
SN:			CPU Code B	U30 (FLASH)	
			FPGA Code (Analog Subsystem Ctrl)	U43 (FPGA)	
			Boot Code	U30 (FLASH)	
			Config Page	U30 (FLASH)	
			Address Bus Decode	U25 (PLD)	
IFP	NA		NA	NA	NA
SN:					
FE			NA	NA	NA
SN:					
Remote			TRA SN:		
TRX Controller			CPU Code A	U30 (FLASH)	
SN:			CPU Code B	U30 (FLASH)	
			FPGA Code (Analog Subsystem Ctrl)	U43 (FPGA)	
			Boot Code	U30 (FLASH)	
			Config Page	U30 (FLASH)	
			Address Bus Decode	U25 (PLD)	
IFP	NA		NA	NA	NA
SN:					
FE			NA	NA	NA
SN:					

Test Technician: Travis M. Carver

Signature: 

Date: 8/5/99

EQUIPMENT CONFIGURATION AS TESTED & SHIPPED

Assembly	Part #	Rev	Description	Ref. Designator	Integration Release Date/Checksum
HUB SPARES					
IMP	2250-0008-03	11	CPU VxWorks	U50,51,58,59 (FLASH)	6/9/99
IP:			CPU Boot Code	U50,51,58,59 (FLASH)	6/9/99
SN: 08-070899-010			Timing Generation	U19 (PLD)	B9C9
			General Logic	U20 (PLD)	E760
MCA	2250-0007-05	07	CPU VxWorks	U2-5 (FLASH)	6/9/99
IP:			BMCL FPGA	U48 (FPGA)	
SN: 08-052099-017			RX FPGA	U70 (FPGA)	
			TX FPGA	U61 (FPGA)	
			CPU Boot Code	U2-5 (FLASH)	6/9/99
			Misc. Digital Logic	U26 (PLD)	9C27
MCA	2250-0007-05	07	CPU VxWorks	U2-5 (FLASH)	6/9/99
IP:			BMCL FPGA	U48 (FPGA)	
SN: 08-052099-016			RX FPGA	U70 (FPGA)	
			TX FPGA	U61 (FPGA)	
			CPU Boot Code	U2-5 (FLASH)	6/9/99
			Misc. Digital Logic	U26 (PLD)	9C27
MCA	2250-0007-05	07	CPU VxWorks	U2-5 (FLASH)	6/9/99
IP:			BMCL FPGA	U48 (FPGA)	
SN: 08-052099-026			RX FPGA	U70 (FPGA)	
			TX FPGA	U61 (FPGA)	
			CPU Boot Code	U2-5 (FLASH)	6/9/99
			Misc. Digital Logic	U26 (PLD)	9C27
MCA	NA		CPU VxWorks	U2-5 (FLASH)	
IP:			BMCL FPGA	U48 (FPGA)	
SN:			RX FPGA	U70 (FPGA)	
			TX FPGA	U61 (FPGA)	
			CPU Boot Code	U2-5 (FLASH)	
			Misc. Digital Logic	U26 (PLD)	
IFD	2250-0009-05	04	RCCH	U69 (FPGA)	
SN: 07-042799-004			Config Logic	U80 (PLD)	CA63
NET	2250-0020-02	03	TDM Bus I/F RX (SubMB)/TX (Net)	U12,U22 (FPGA)	
SN: 08-070199-008			TDM Bus I/F TX (SubMB)/RX (Net)	U34,U45 (FPGA)	
			IMP I/F	U55 (FPGA)	
			Chip Select for the IMP I/F	U28 (CPLD)	5291
			Chip Select for the IMP I/F	U35 (CPLD)	B494
OC3	2250-0021-02	03	SONET Path Termination	U4 (FPGA)	
SN: 08-042799-002					
PSI	2250-0025-02	04	NA	NA	NA
SN: 08-062399-030					
ODB	NA		NA	NA	NA
SN:					

Test Technician: Travis M. CarverSignature: Date: 8/5/99

EQUIPMENT CONFIGURATION AS TESTED & SHIPPED

Assembly	Part #	Rev	Description	Ref. Designator	Integration Release Date/Checksum
REMOTE SN: 566					
Remote Motherboard	2250-0011-05	10	CPU VxWorks	U5-8 (FLASH)	6/9/99
IP:			TDM Bus I/F RX (SubMB)/TX (Net)	U89 (FPGA)	
SN: 07-061899-015			TDM Bus I/F TX (SubMB)/RX (Net)	U90 (FPGA)	
			CCH & Ethernet I/F	U91 (FPGA)	
			Timing Generation	U65 (FPGA)	
			CPU Boot Code	U5-8 (FLASH)	6/9/99
			Address Decode	U62 (PLD)	9AE9
Remote MCA	2250-0037-03	06	CPU VxWorks	U2-5 (FLASH)	6/9/99
IP:			BMCL FPGA	U48 (FPGA)	
SN: 07-052099-020			RX FPGA	U70 (FPGA)	
			TX FPGA	U61 (FPGA)	
			CPU Boot Code	U2-5 (FLASH)	6/9/99
			Misc. Digital Logic	U26 (PLD)	9C27
DS1	2250-0032-02	04	Chip Select & Bus Mux for Mapper	U5 (PLD)	BAA5
SN: 08-062599-001					
Ethernet Card	2990019	NA		NA	NA
SN: 75-00-16-63					
Backplane	2250-0031-03	01	NA	NA	NA
SN: 08-031699-004					
LED	2250-0033-01	02	NA	NA	NA
SN: 08-012299-039					
TRA SN:					
TRX Controller	NA		CPU Code A	U30 (FLASH)	
SN:			CPU Code B	U30 (FLASH)	
			FPGA Code (Analog Subsystem Ctrl)	U43 (FPGA)	
			Boot Code	U30 (FLASH)	
			Config Page	U30 (FLASH)	
			Address Bus Decode	U25 (PLD)	
IFP	NA		NA	NA	NA
SN:					
FE	NA		NA	NA	NA
SN:					

Test Technician: Travis M. CarverSignature: Travis M. CarverDate: 07 / 28 / 99

EQUIPMENT CONFIGURATION AS TESTED & SHIPPED

Assembly	Part #	Rev	Description	Ref. Designator	Integration Release Date/Checksum
REMOTE SN: 567					
Remote Motherboard	2250-0011-05	10	CPU VxWorks	U5-8 (FLASH)	6/9/99
IP:			TDM Bus I/F RX (SubMB)/TX (Net)	U89 (FPGA)	
SN: 07-061899-016			TDM Bus I/F TX (SubMB)/RX (Net)	U90 (FPGA)	
			CCH & Ethernet I/F	U91 (FPGA)	
			Timing Generation	U65 (FPGA)	
			CPU Boot Code	U5-8 (FLASH)	6/9/99
			Address Decode	U62 (PLD)	9AE9
Remote MCA	2250-0037-03	06	CPU VxWorks	U2-5 (FLASH)	6/9/99
IP:			BMCL FPGA	U48 (FPGA)	
SN: 07-052099-028			RX FPGA	U70 (FPGA)	
			TX FPGA	U61 (FPGA)	
			CPU Boot Code	U2-5 (FLASH)	6/9/99
			Misc. Digital Logic	U26 (PLD)	9C27
DS1	2250-0032-02	04	Chip Select & Bus Mux for Mapper	U5 (PLD)	BAA5
SN: 08-062599-002					
Ethernet Card	2990019		NA	NA	NA
SN: 75-00-16-35					
Backplane	2250-0031-03	01	NA	NA	NA
SN: 08-061199-003					
LED	2250-0033-01	02	NA	NA	NA
SN: 08-012299-027					
TRA SN:					
TRX Controller	NA		CPU Code A	U30 (FLASH)	
SN:			CPU Code B	U30 (FLASH)	
			FPGA Code (Analog Subsystem Ctrl)	U43 (FPGA)	
			Boot Code	U30 (FLASH)	
			Config Page	U30 (FLASH)	
			Address Bus Decode	U25 (PLD)	
IFP	NA		NA	NA	NA
SN:					
FE	NA		NA	NA	NA
SN:					

Test Technician: Travis M. CarverSignature: Date: 07/29/99

TRX Assembly Machine Configuration List

This is a permanent record of the originally manufactured TRX Assembly,
fill in part number, version, serial number, and software/firmware code information for each TRX Assembly

TRX Assy, 28 GHz

2000-0158-01R01

Serial Number 28-022

Assembly	Version	Revision	Serial #	Description
2000-0157	01	01	28/31 - 022	Front End Assy, 28 GHz
2250-0053	03	02	0622-006	PCA, TRX-S IFP
2250-0054	02	04	0525-025	PCA, TRX-S Controller
2250-0074	01	01	?	PCA, TRX Power Supply

	Page A	Page B
2350-0006-00 CPU code	I1.12.01-TRAC	
2360-0006-00 FPGA code	I1.00.00-RAF	
2355-0006-00 Config code	0628	
2340-0006-00 Boot load	I0001990517	

Date June 25th 199
Technician M. FARRBURN

TRX Assembly Machine Configuration List

This is a permanent record of the originally manufactured TRX Assembly,
fill in part number, version, serial number, and software/firmware code information for each TRX Assembly

TRX Assy, 28 GHz

2000-0158-01R01

Serial Number 28-25

Assembly	Version	Revision	Serial #	Description
2000-0157	01	01	28/31-025	Front End Assy, 28 GHz
2250-0053	03	02	0622-013	PCA, TRX-S IFP
2250-0054	02	04	0525-052	PCA, TRX-S Controller
2250-0074	01	01	—	PCA, TRX Power Supply

	Page A	Page B
2350-0006-00 CPU code	I 1.14.00-TRAC	
2360-0006-00 FPGA code	I 1.14.00 TRAF	
2355-0006-00 Config code	I	
2340-0006-00 Boot load	I 1.12.00-TRAB	

Date _____
Technician _____

TRX Assembly Machine Configuration List

This is a permanent record of the originally manufactured TRX Assembly,
Fill in part number, version, serial number, and software/firmware code information for
each TRX Assembly.

TRX Assy Serial Number 28-35

(Circle appropriate configuration)

Frequency	Part Number
<u>28 GHz</u>	2000-0158-01R01
31 GHz	2000-0163-00R01
38 GHz	2000-0156-01R03

(Circle part number for Front End and fill in serial numbers)

Assembly	Serial #	Description
2000-0157-01R01	<u>28-036</u>	Front End Assy, 28 GHz
2000-0162-00R01		Front End Assy, 31 GHz
2000-0155-01R02		Front End Assy, 38 GHz
2250-0053-03R03	<u>0622-038</u>	PCA, TRX-S IFP
2250-0054-02R06	<u>0525-036</u>	PCA, TRX-S Controller
2250-0074-01R02		PCA, TRX Power Supply

(Fill in software/firmware code)

	Page A	Page B
2350-0006-00 CPU code	<u>E 1.14.00</u>	<u>TRAC</u>
2360-0006-00 FPGA code	<u>E 1.14.00</u>	<u>TRAF</u>
2355-0006-00 Config code	<u>E 1.14.00</u>	<u>TCFG</u>
2340-0006-00 Boot load	<u>E 1.12.01</u>	<u>TRAB</u>

Date 7-27-99
Technician JCC

TRX Assembly Machine Configuration List

This is a permanent record of the originally manufactured TRX Assembly.
Fill in part number, version, serial number, and software/firmware code information for each TRX Assembly.

TRX Assy Serial Number 31-03

(Circle appropriate configuration)

Frequency

Part Number

28 GHz

2000-0158-01R01

31 GHz

2000-0163-00R01

38 GHz

2000-0156-01R03

(Circle part number for Front End and fill in serial numbers)

Assembly

Serial #

Description

2000-0157-01R01

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Front End Assy, 28 GHz

2000-0162-00R01

31-040

Front End Assy, 31 GHz

2000-0155-01R02

--

Front End Assy, 38 GHz

2250-0053-03R03

0622-026

PCA, TRX-S IFP

2250-0054-02R06

0525-004

PCA, TRX-S Controller

2250-0074-01R02

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PCA, TRX Power Supply

(Fill in software/firmware code)

2350-0006-00 CPU code

2360-0006-00 FPGA code

2355-0006-00 Config code

2340-0006-00 Boot load

Page A

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I 1.14.00	- TRAC
I 1.14.00	- TRAF
I 1.14.00	- TCFG
I 1.12.01	- TRAB

Date

7-22-99

Technician

Jca

TRX Assembly Machine Configuration List

This is a permanent record of the originally manufactured TRX Assembly.

fill in part number, version, serial number, and software/firmware code information for each TRX Assembly

TRX Assy, ³⁸~~28~~ GHz
Serial Number 38-27 *

2000-0^{0156-01R03}~~156-01R01~~

Assembly	Version	Revision	Serial #	Description
2000-0 ⁰¹⁵⁶ 156	01	0102	<u>38-025</u>	Front End Assy, ³⁸ 28 GHz
2250-0053	03	02	<u>0622-036</u>	PCA, TRX-S IFP
2250-0054	02	04	<u>0525-013</u>	PCA, TRX-S Controller
2250-0074	01	01	<u>—</u>	PCA, TRX Power Supply

		Page A	Page B
2350-0006-00	CPU code	<u>I 1.14.00</u>	
2360-0006-00	FPGA code	<u>I 1.14.00</u>	
2355-0006-00	Config code	<u>I 1.14.00</u>	
2340-0006-00	Boot load	<u>I 1.12.00</u>	

Date 7-14-99
Technician Joe

* For Internal use only