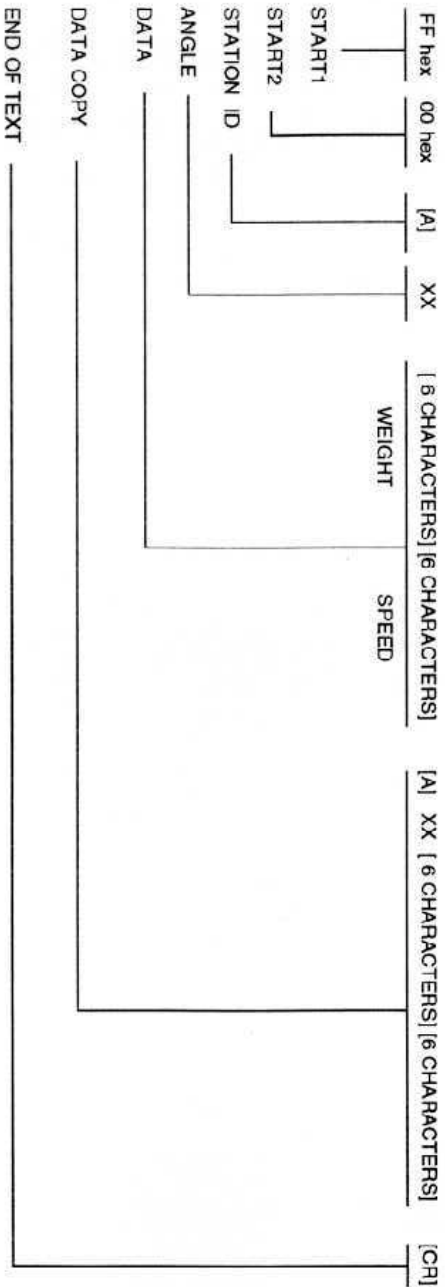


TECHNICAL DESCRIPTION OF BT10-02 TRANSMITTER AND DSP1000 (W110-02) RECEIVER PAGE 1 OF 2

DATA FROM THE BT10 IS TRANSMITTED CONTINUOUSLY AND UPDATED ONCE PER SECOND.

DATA OUTPUT IS ASCII AT 9600 BAUD, ONE STOP BIT, 8 DATA BITS, NO PARITY

DATA OUTPUTS AS FOLLOWS: (TOTAL OF 33 CHARACTERS)



DATA RECEIVED AT THE DSP1000 (W110-02) IS CHECKED AS FOLLOWS:

TOTAL CHARACTERS IS 33

FIRST 2 CHARACTERS ARE FF hex AND 00 hex.

DATA COPY COMPARES TO DATA

LAST CHARACTER = [CR]

DATA IS NOT ACCEPTED IF ANY OF THE ABOVE IS NOT TRUE.

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TECHNICAL DESCRIPTION OF BT10-02 TRANSMITTER AND DSP1000 (W110-02) RECEIVER PAGE 2 OF 2

DUTY CYCLE:

CONTINUOUS EXCEPT WHEN OUTPUT IS BEING UPDATED:



THE LCD DISPLAY ON THE DSP1000 (W110-02) WILL INDICATE WHETHER OR NOT PROPER RECEPTION IS OCCURRING. THE BOTTOM LINE OF THE DISPLAY WILL BE BLANK WHEN RECEPTION IS NOT OCCURRING, OTHERWISE IT IS A BAR GRAPH WHICH INDICATES PERCENT OF CORRECT DATA RECEIVED PER SECOND (0-100%). NOTE: AT 9600 BAUD AND 39 CHARACTERS PER MESSAGE THIS SYSTEM HAS 28 CHANCES PER SECOND TO RECEIVE CORRECT DATA MESSAGES.

AS CORRECT DATA IS RECEIVED, THE DSP1000 (W110-02) WILL THEN CALCULATE A 'RATE' AND 'TOTAL' INFORMATION TO BE DISPLAYED ON THE LCD DISPLAY.

TRANSMITTER FREQUENCY IS SELECTABLE FROM 903.3 MHZ TO 921.3 MHZ VIA DIP SWITCH SETTING. ALL TIMING IS DERIVED FROM 11.0592 MHZ, 4.9152 MHZ, AND 12.0 MHZ OSCILLATORS.

RECEIVER FREQUENCY IS SELECTABLE FROM 903.3 MHZ TO 921.3 MHZ VIA DIP SWITCH SETTING. ALL TIMING IS DERIVED FROM 11.0592 MHZ, 3.8964 MHZ, AND 4.0 MHZ OSCILLATORS.

THIS SYSTEM REQUIRES 120 VAC FOR THE DSP100 (W110-02) AND BT10 (BT10-02) ENCLOSURES.

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