Murandi
Communications Ltd.
Innovative Radio Frequency Solutions

EUM3006 Block Diagrams

The following are the block diagrams for the RF and digital sections of the EUM3006. The blocks shown are as follows:

Top level
RF section
Ethernet PHY
Microprocessor/MAC memory
Microprocessor/MAC
Baseband processor interface

1 Block Diagrams

1.1 Top Level Block Diagram

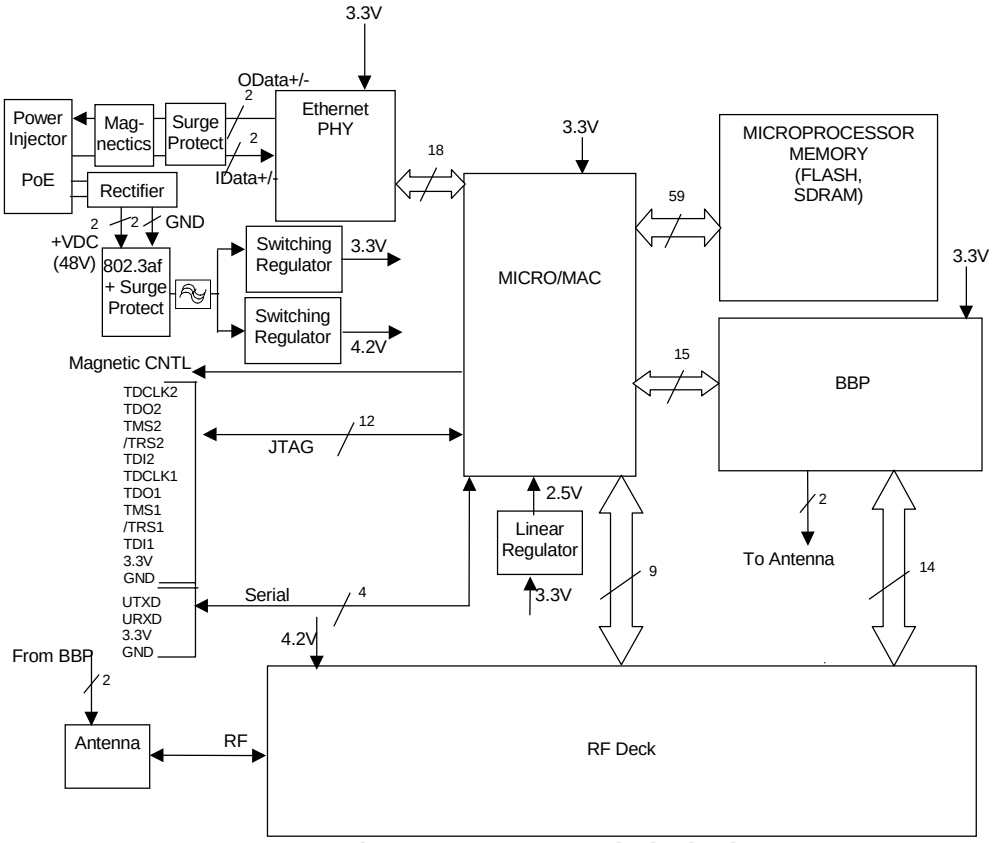


Figure 1.1 – Top Level Block Diagram

1.2 RF Section

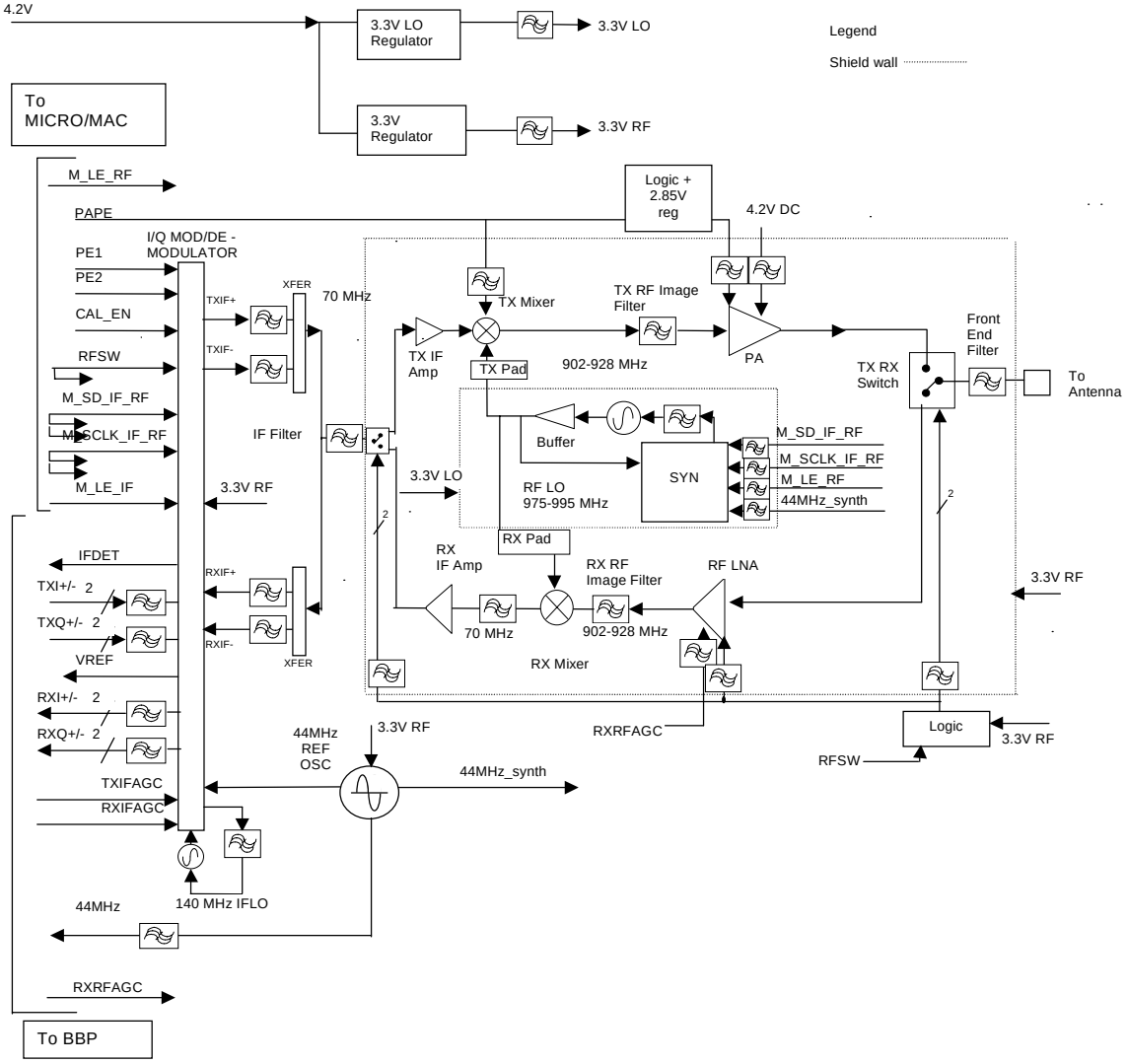
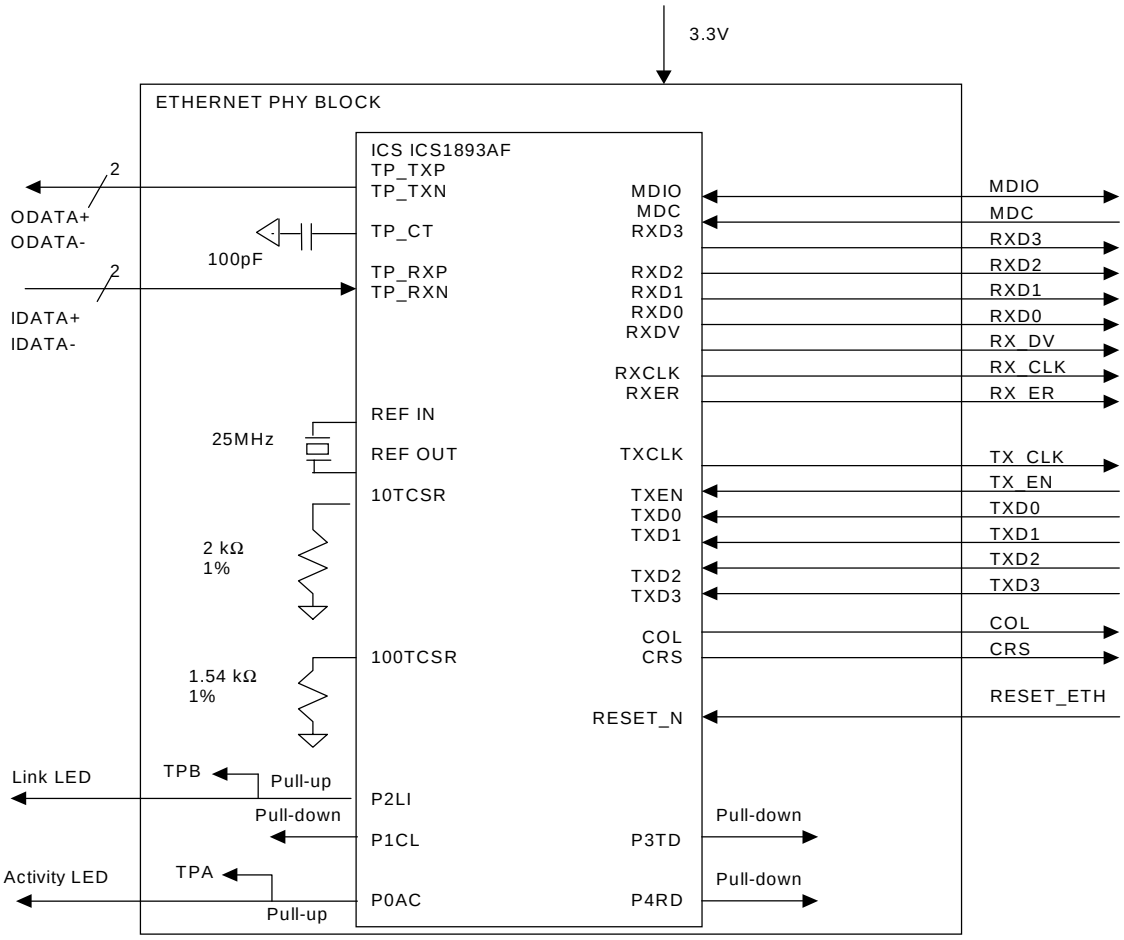


Figure 1.2 – RF Section Block Diagram

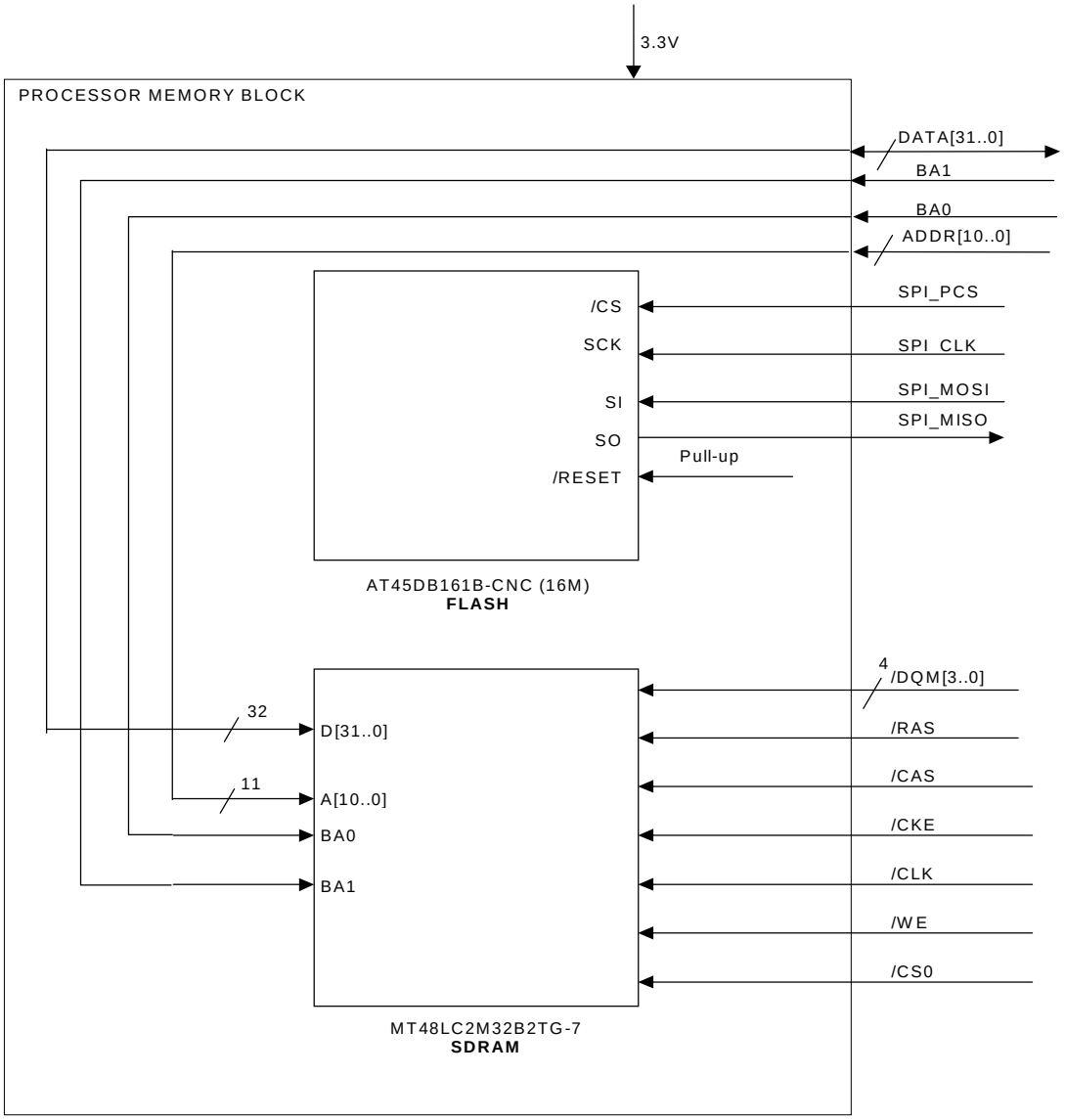
1.3 Digital Section

1.3.1 Ethernet PHY



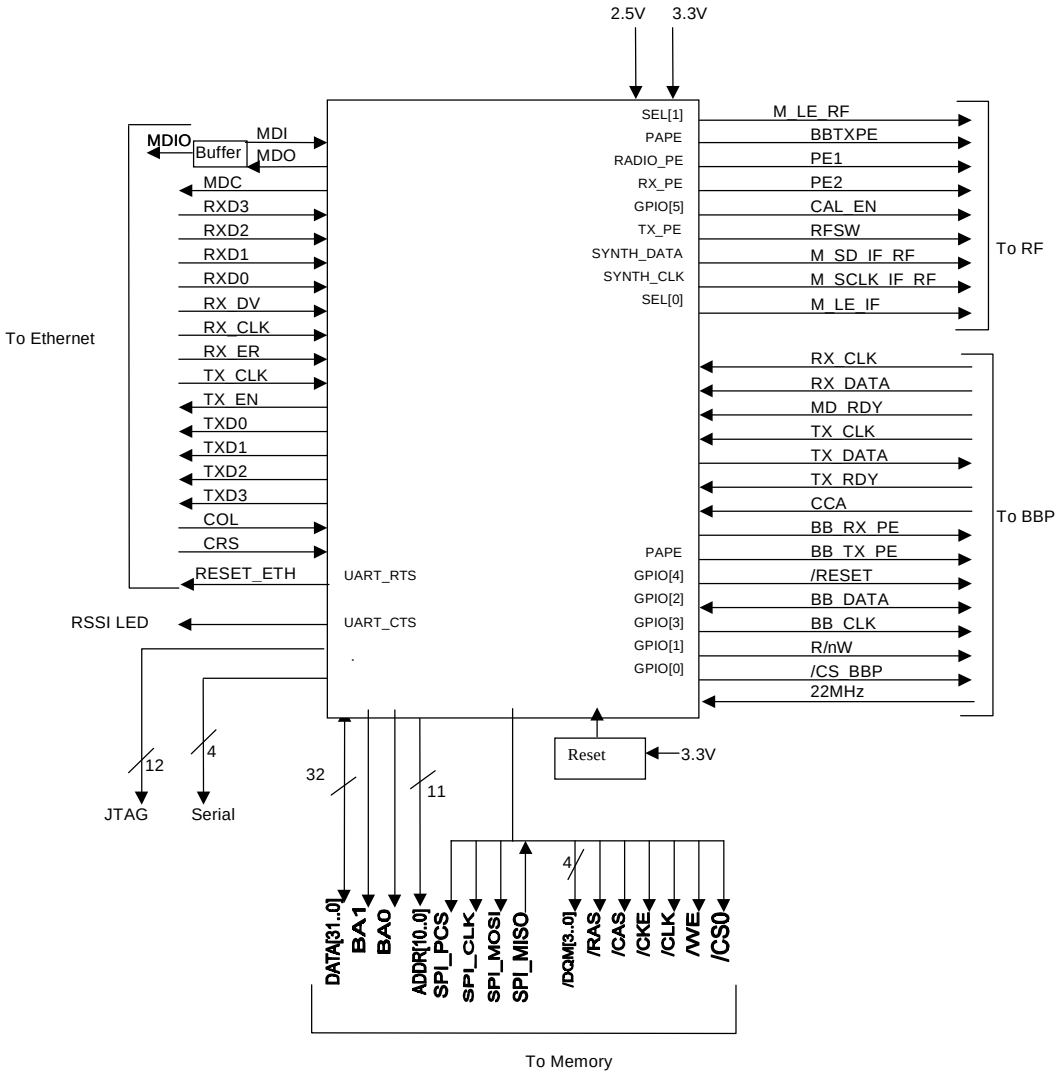
5.3.1 Ethernet PHY Block Diagram

1.3.2 Microprocessor/MAC Memory (FLASH,SDRAM)



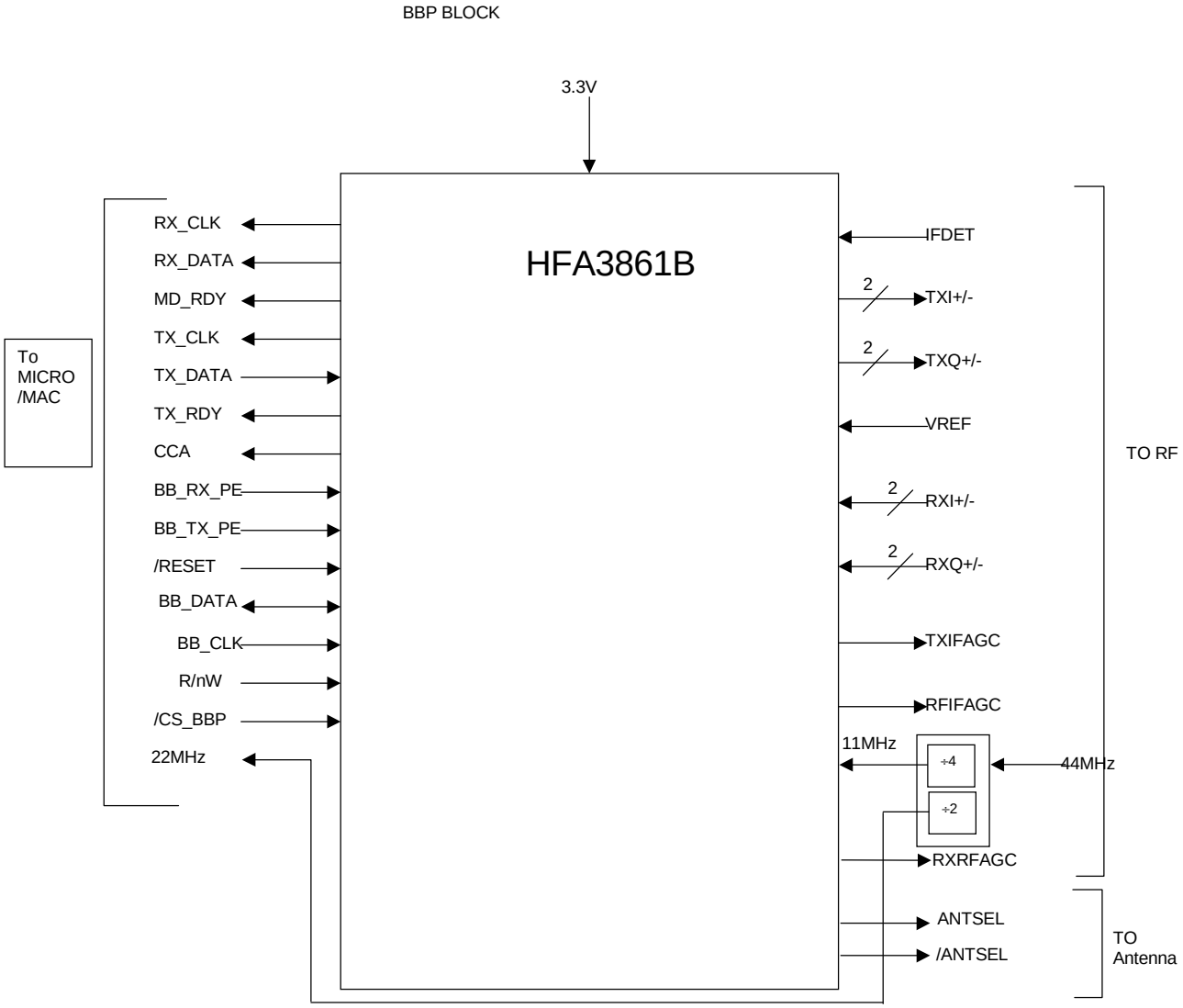
5.3.2 Microprocessor/MAC Memory Block Diagram

1.3.3 Microprocessor/MAC



5.3.3 Microprocessor/MAC Interface Block Diagram

1.3.4 BBP Interface



5.3.4 BBP Interface Block Diagram