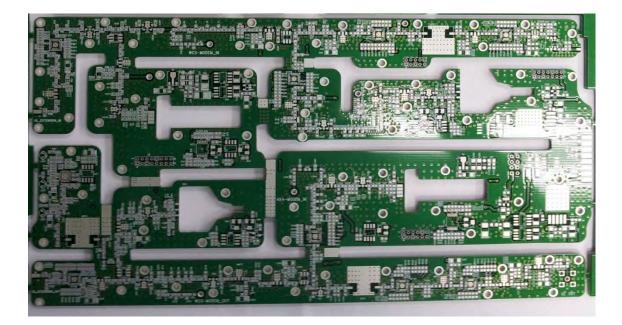
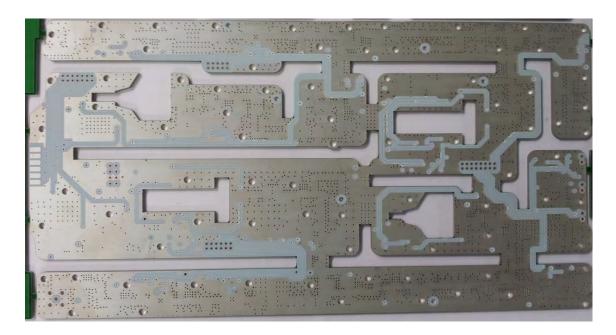




#### <Optic Module>

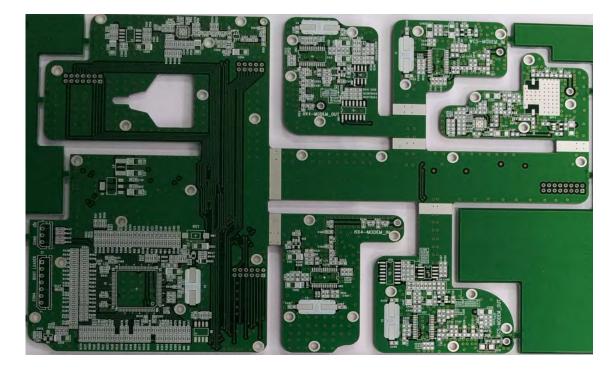
1. Top of Internal PCB1

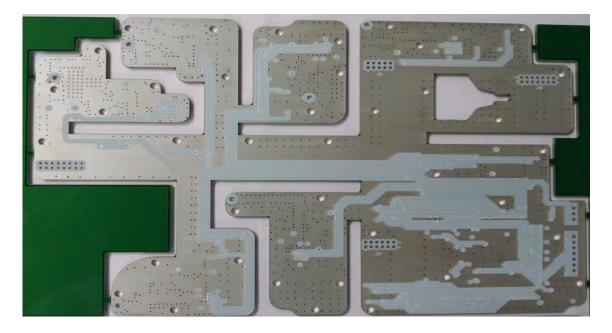






# 3. Top of Internal Optic PCB2

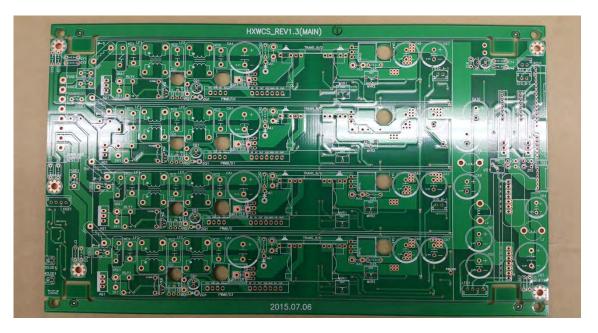


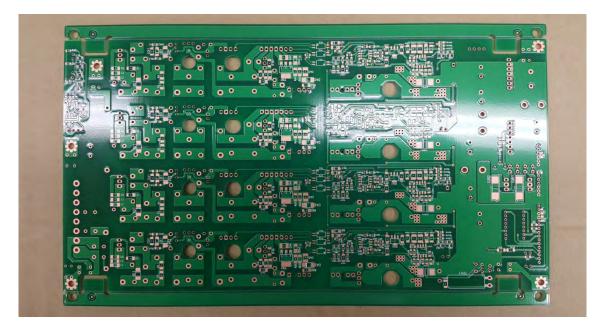




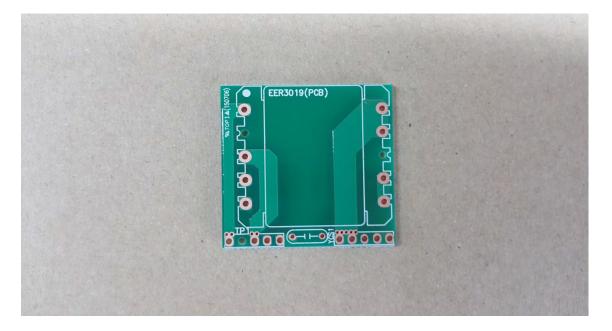
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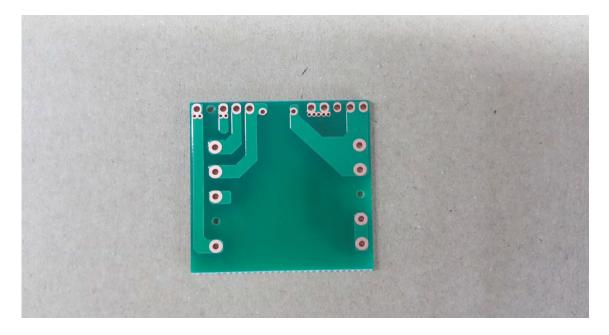
1. Top of Internal PCB1



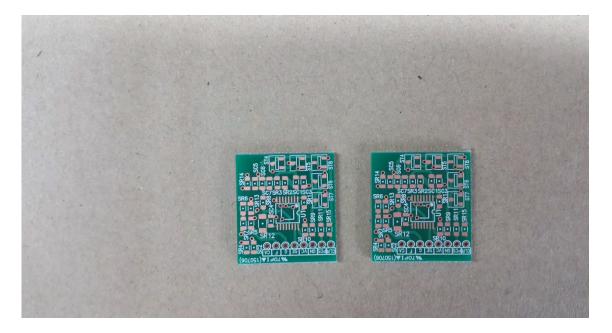


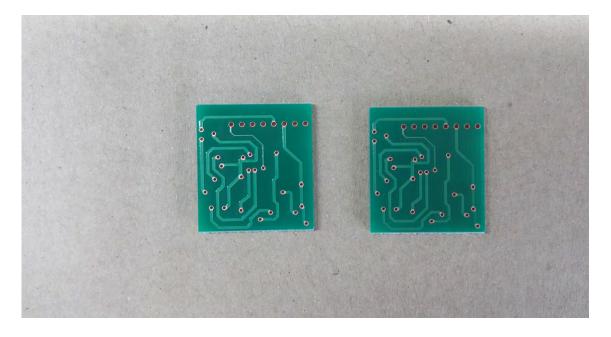




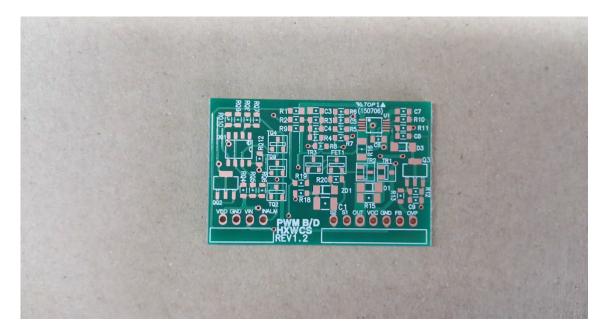


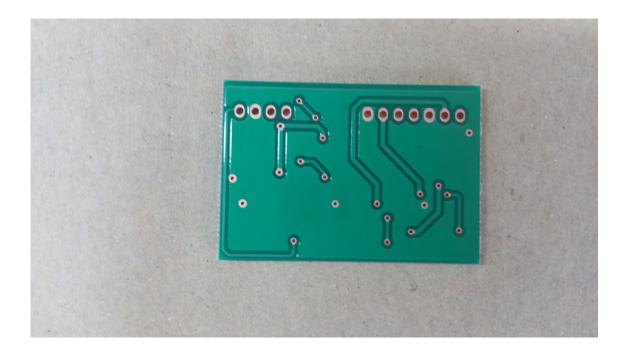








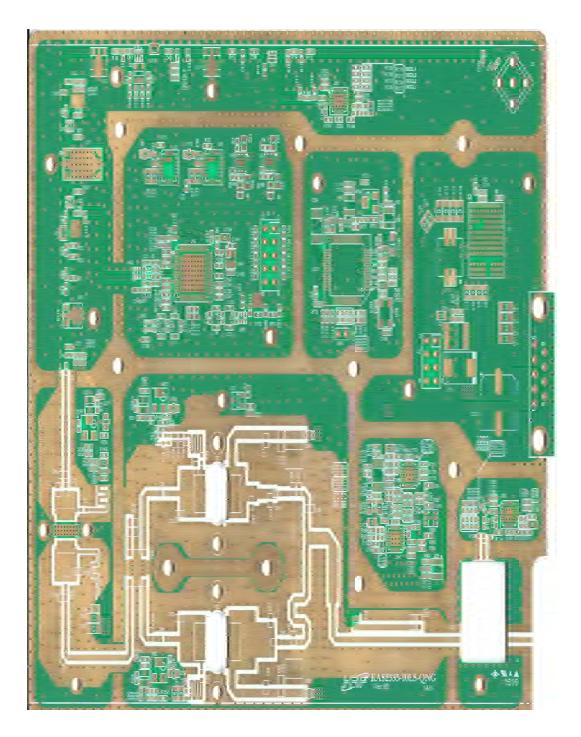




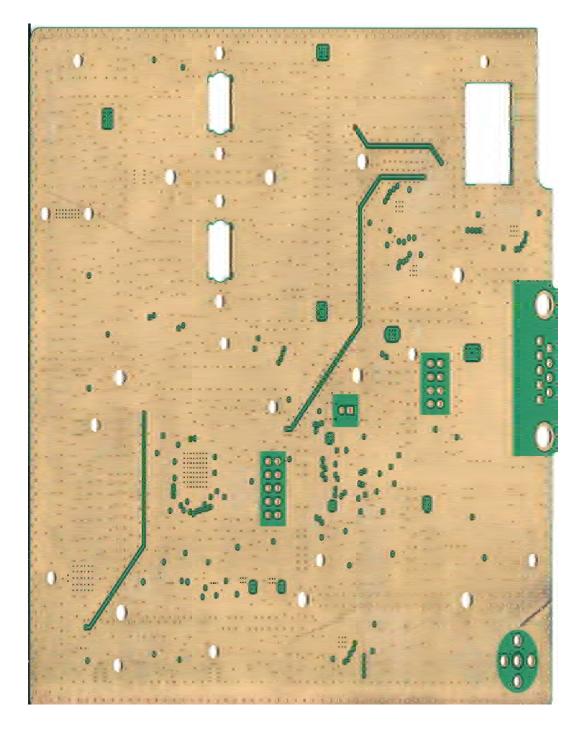


#### <AMP>

1. Top of Internal PCB









# <RCU>

1. Top of Internal PCB1

