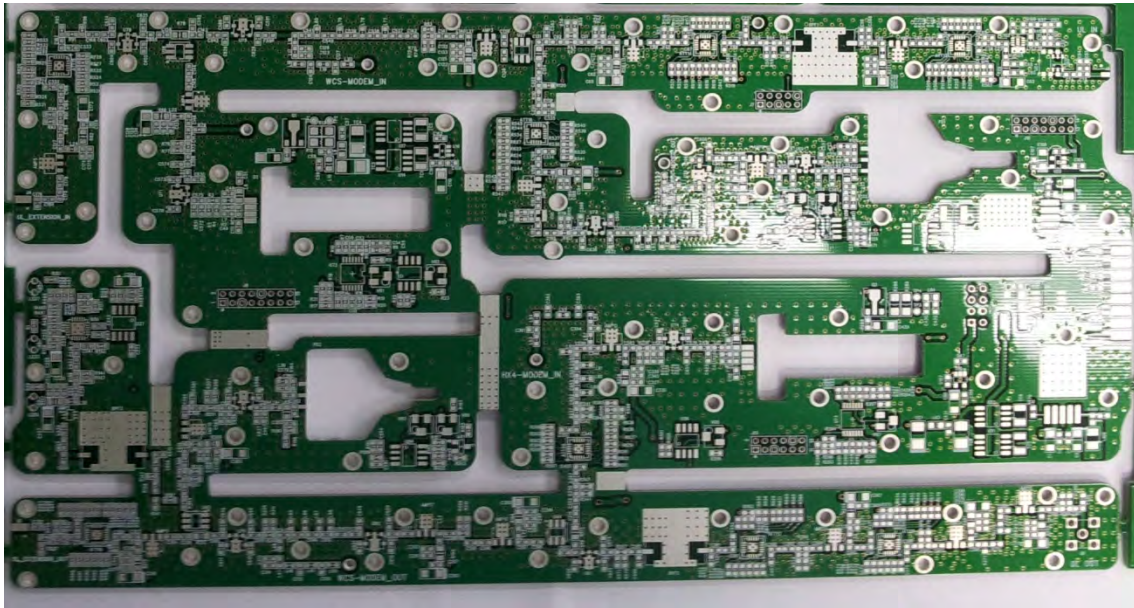


## **Appendix 4. Internal Photos of EUT**

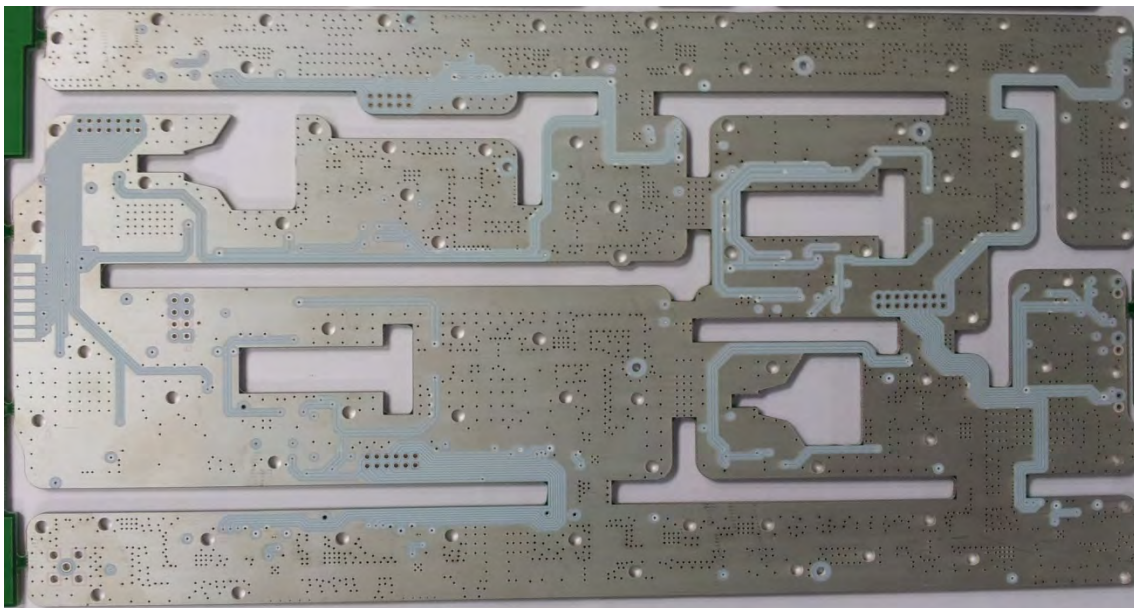
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### **<Optic Module>**

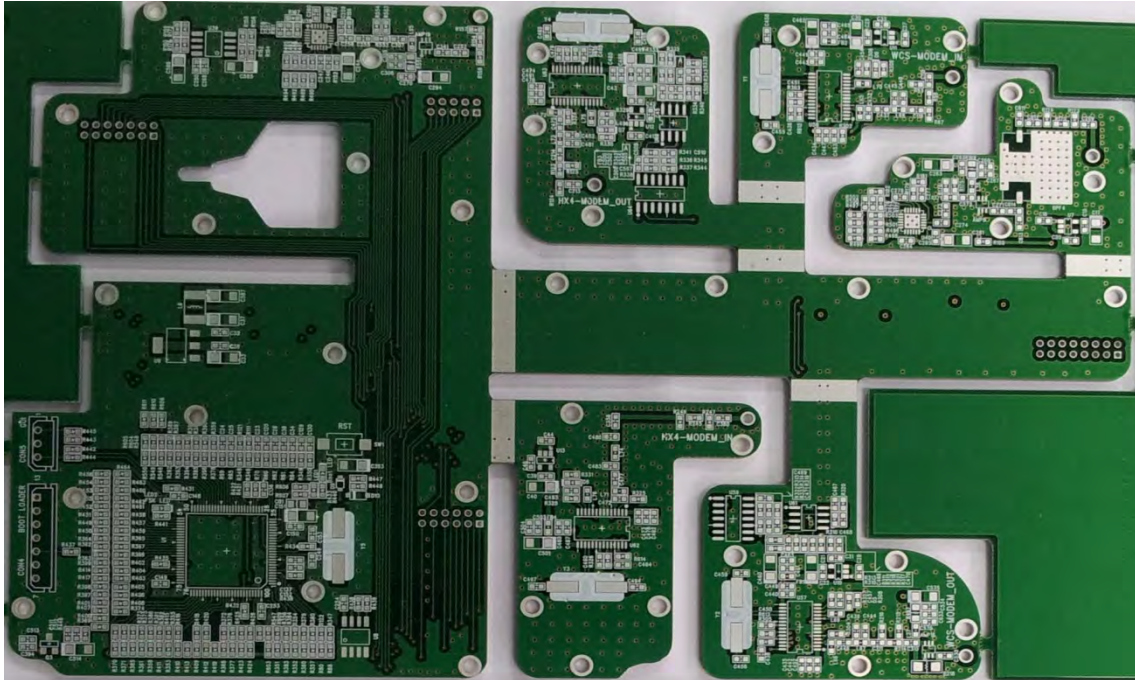
#### **1. Top of Internal PCB1**



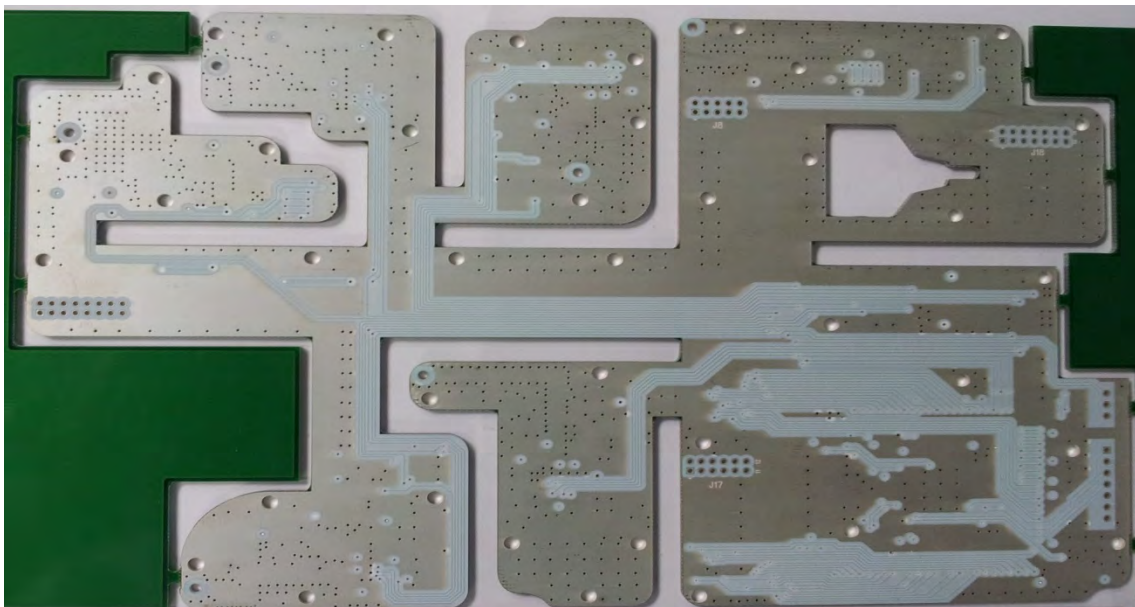
#### **2. Bottom of Internal PCB1**



### 3. Top of Internal Optic PCB2



### 4. Bottom of Internal PCB2

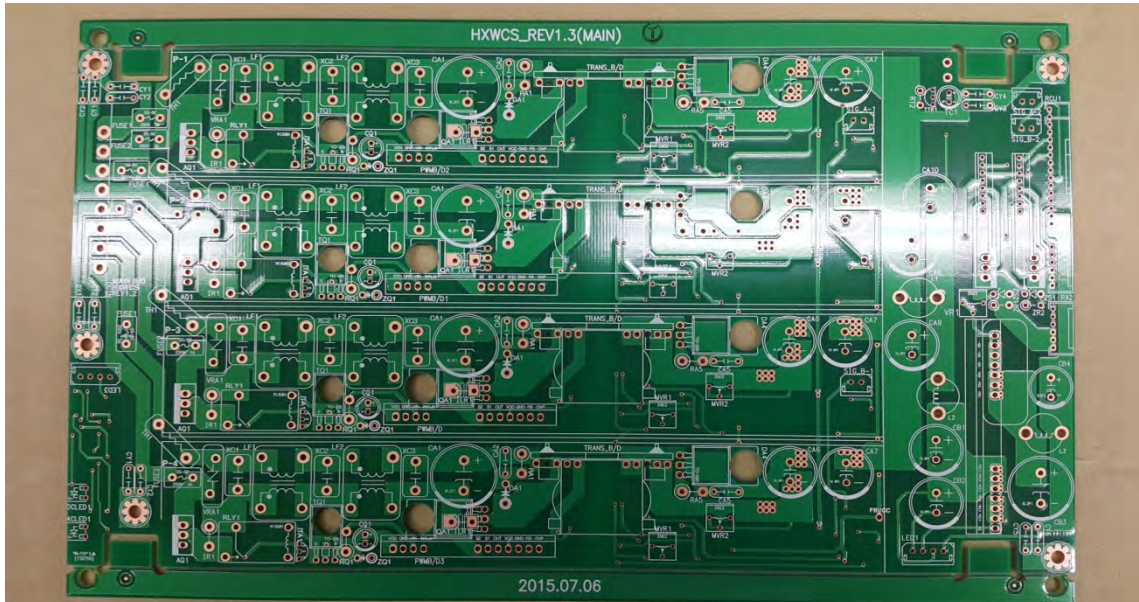




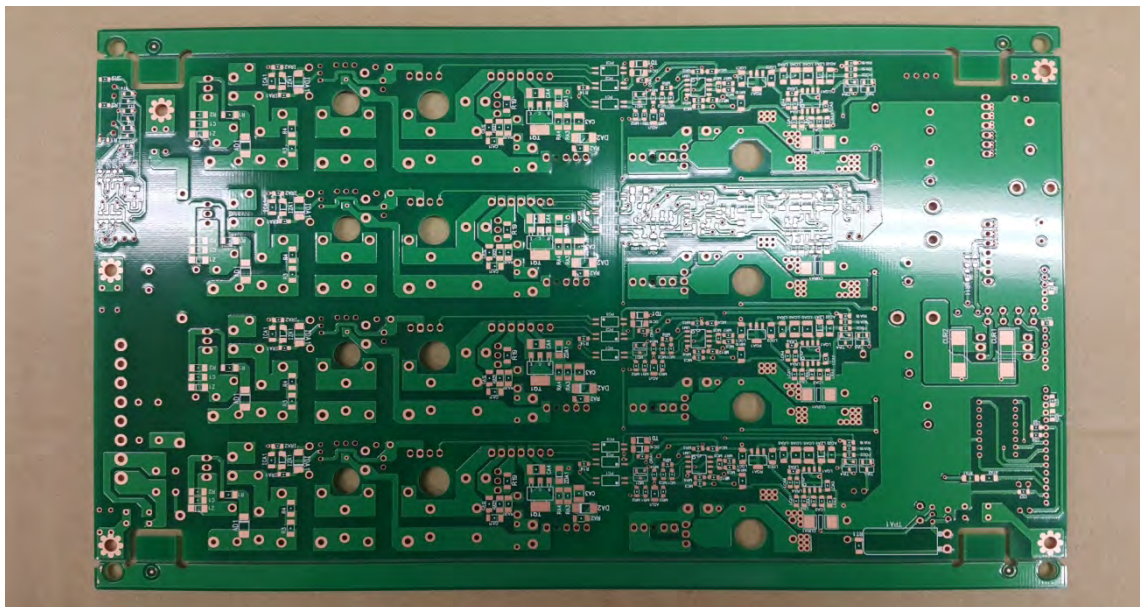
## Appendix 4. Internal Photos of EUT

<PSU>

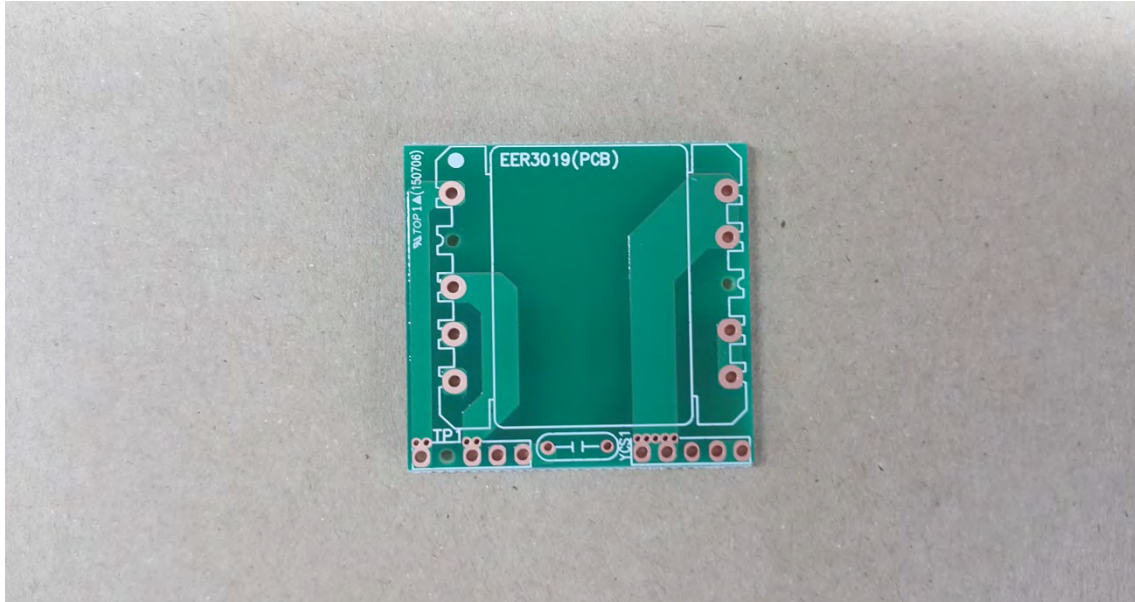
### 1. Top of Internal PCB1



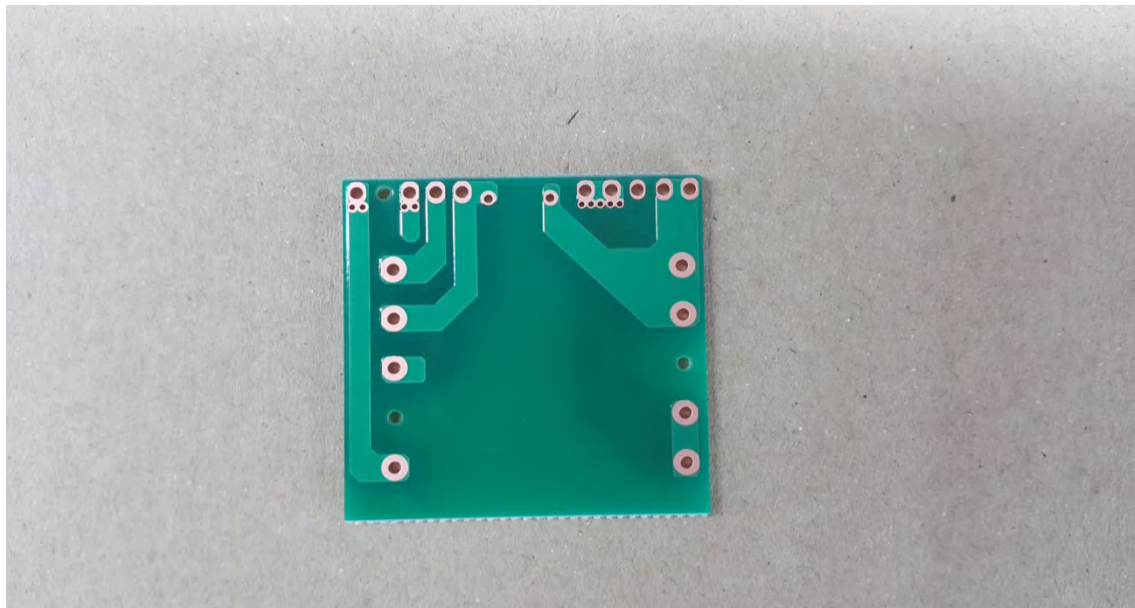
### 2. Bottom of Internal PCB1



### 3. Top of Internal PCB2

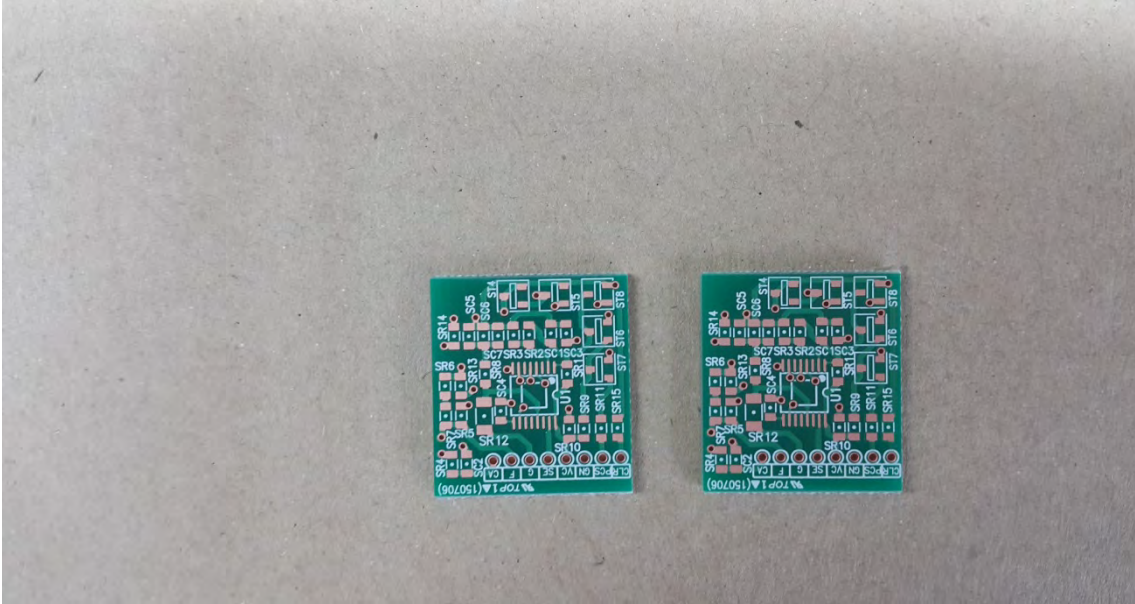


### 4. Bottom of Internal PCB2

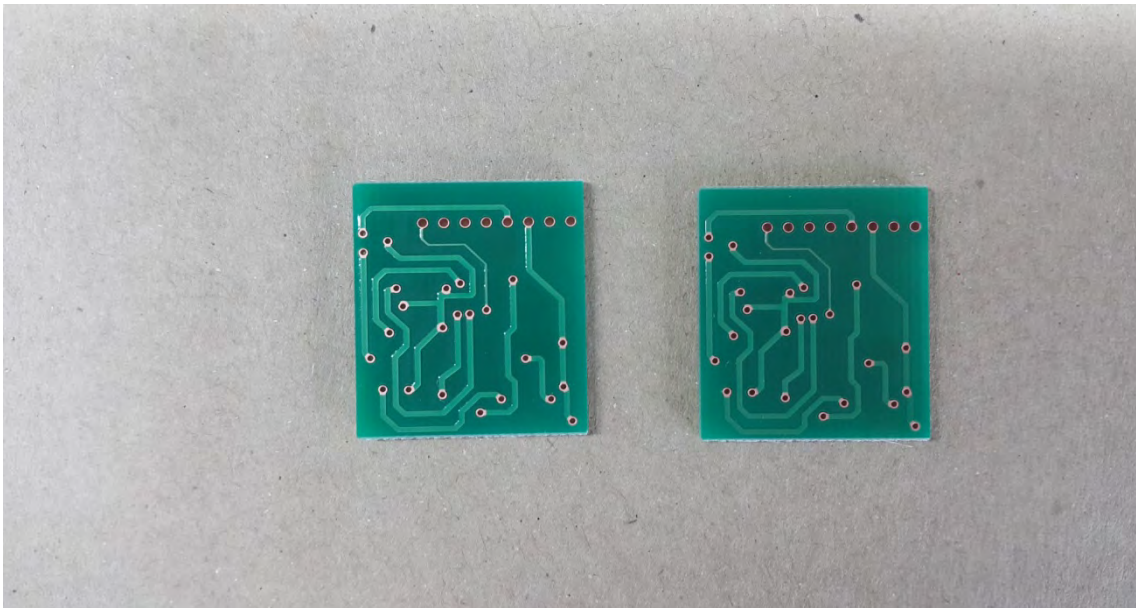




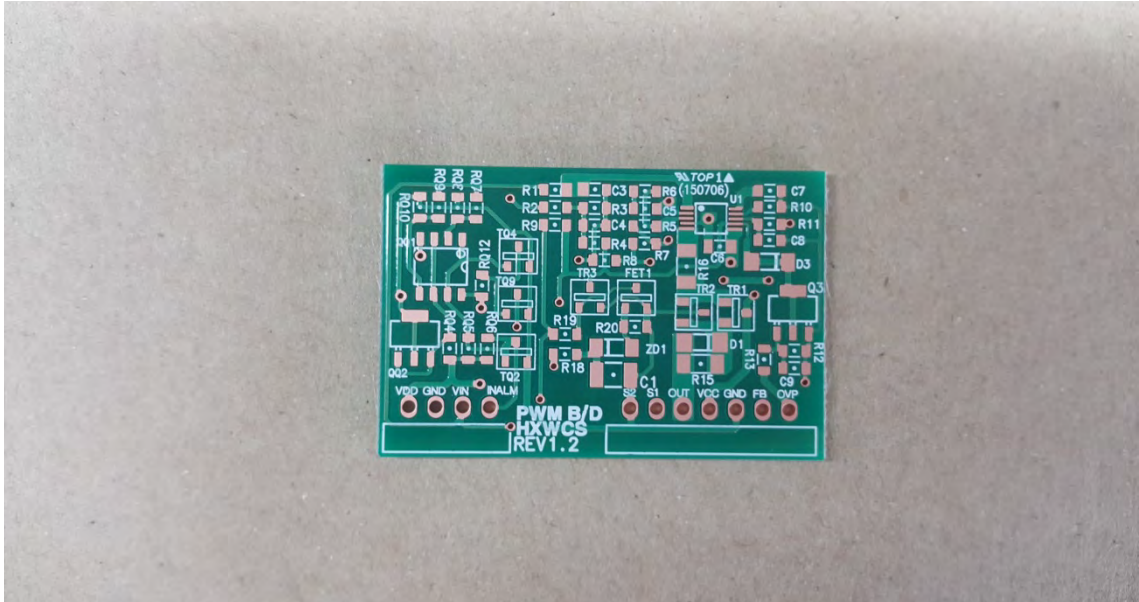
### 5. Top of Internal PCB3



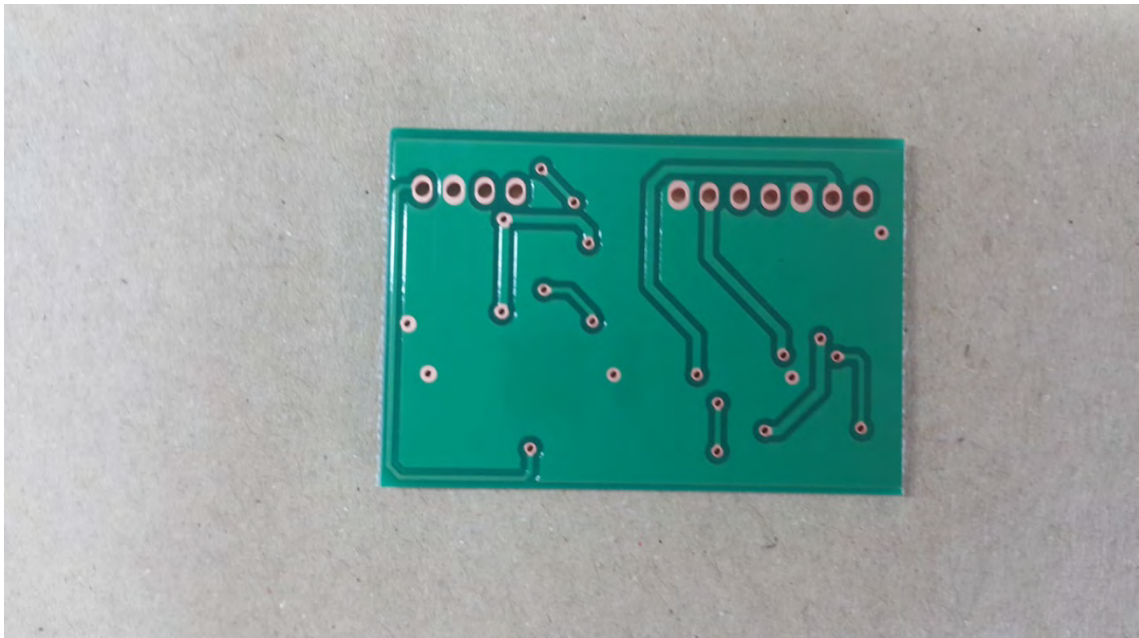
### 6. Bottom of Internal PCB3



## 7. Top of Internal PCB4



## 8. Bottom of Internal PCB4

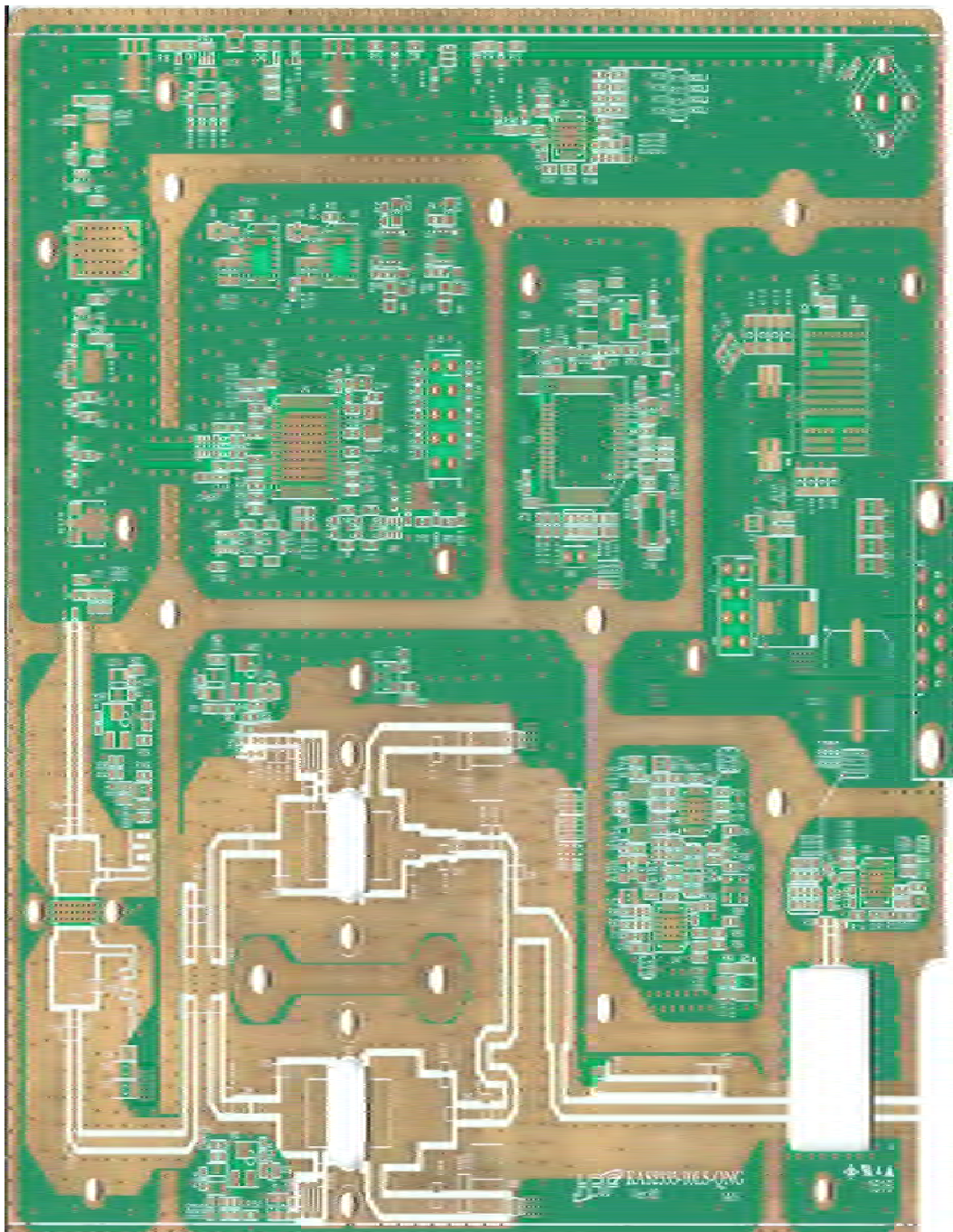




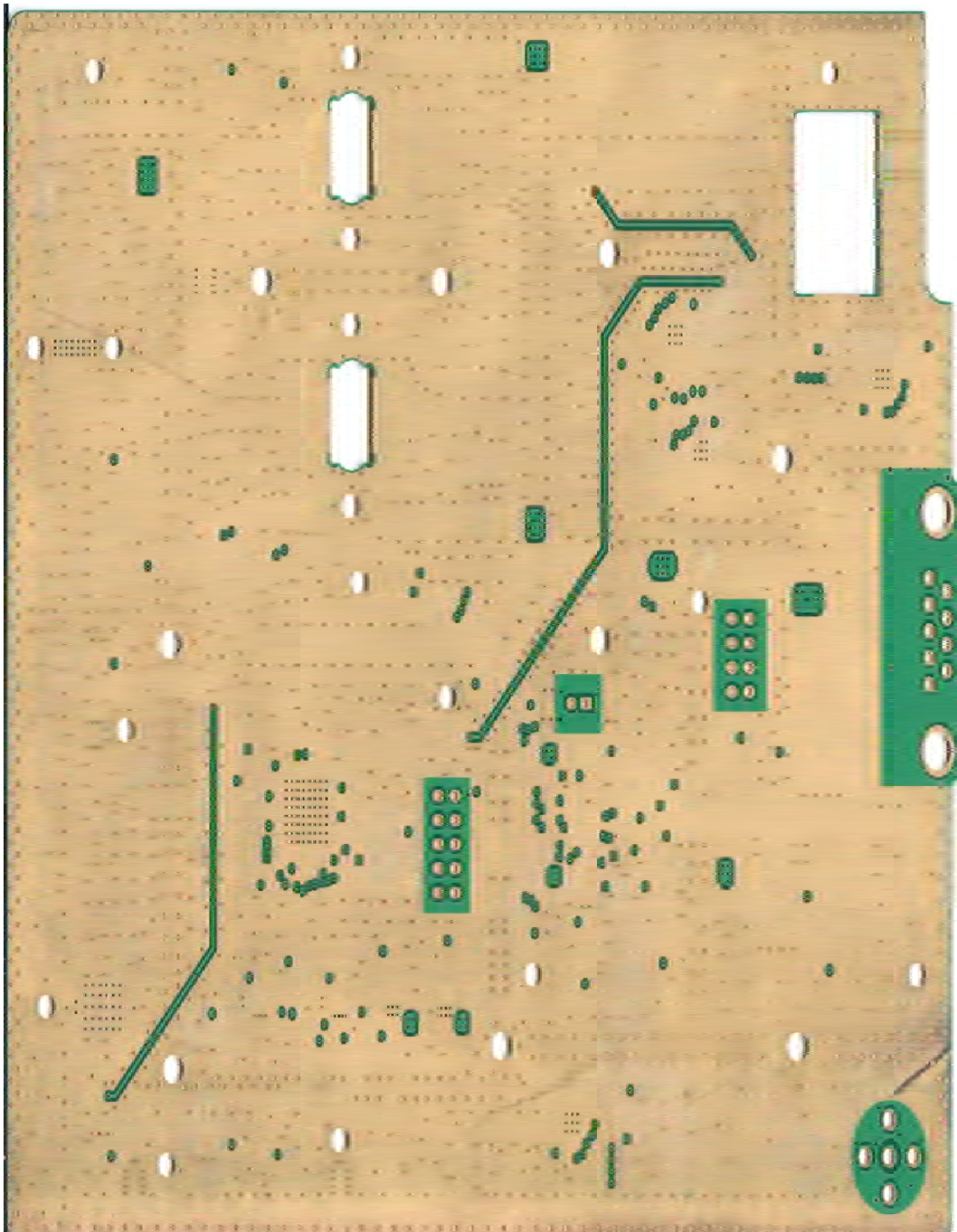
## Appendix 4. Internal Photos of EUT

<AMP>

### 1. Top of Internal PCB



## 2. Bottom of Internal PCB



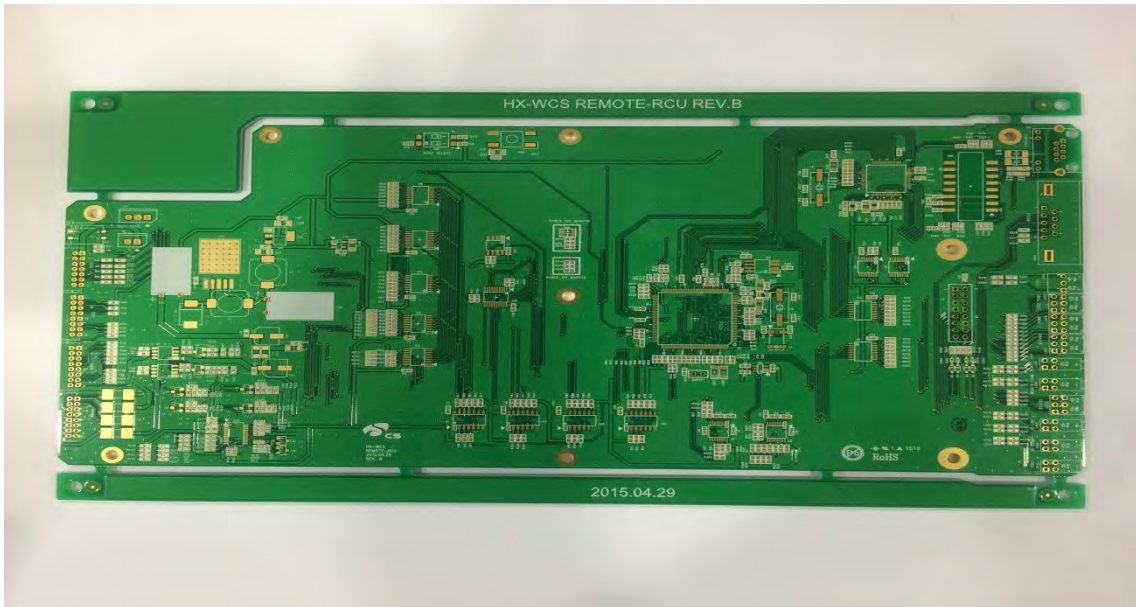


## **Appendix 4. Internal Photos of EUT**

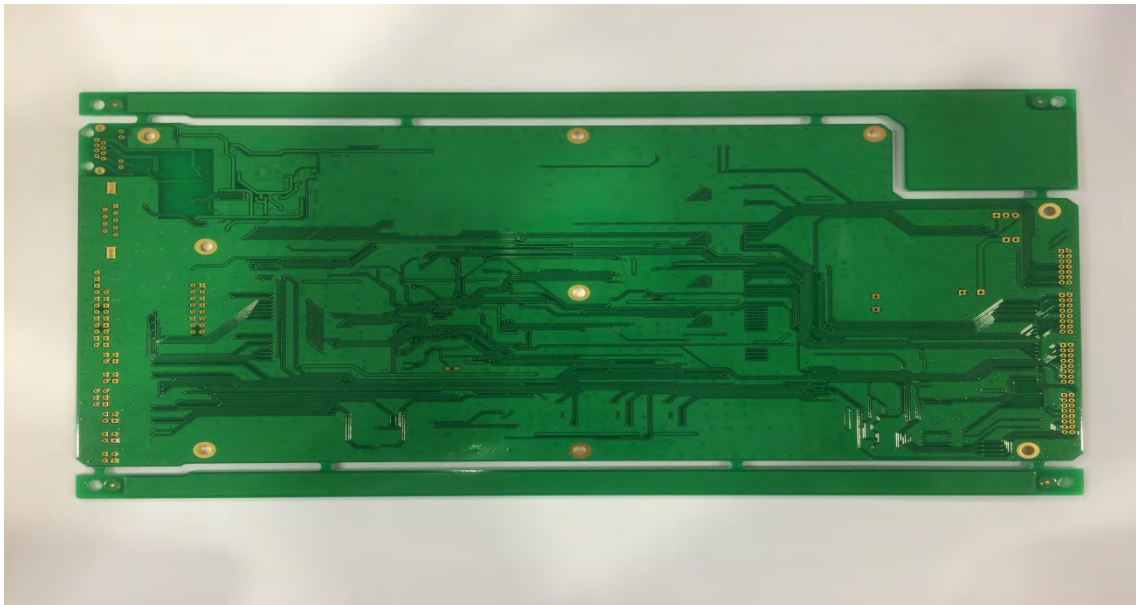
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**<RCU>**

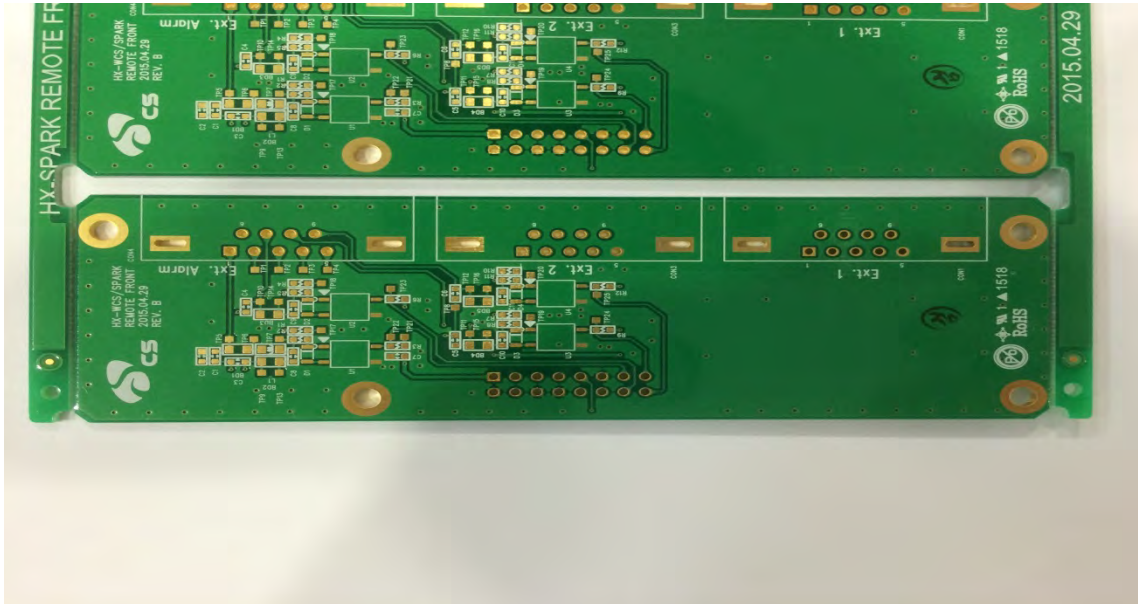
### **1. Top of Internal PCB1**



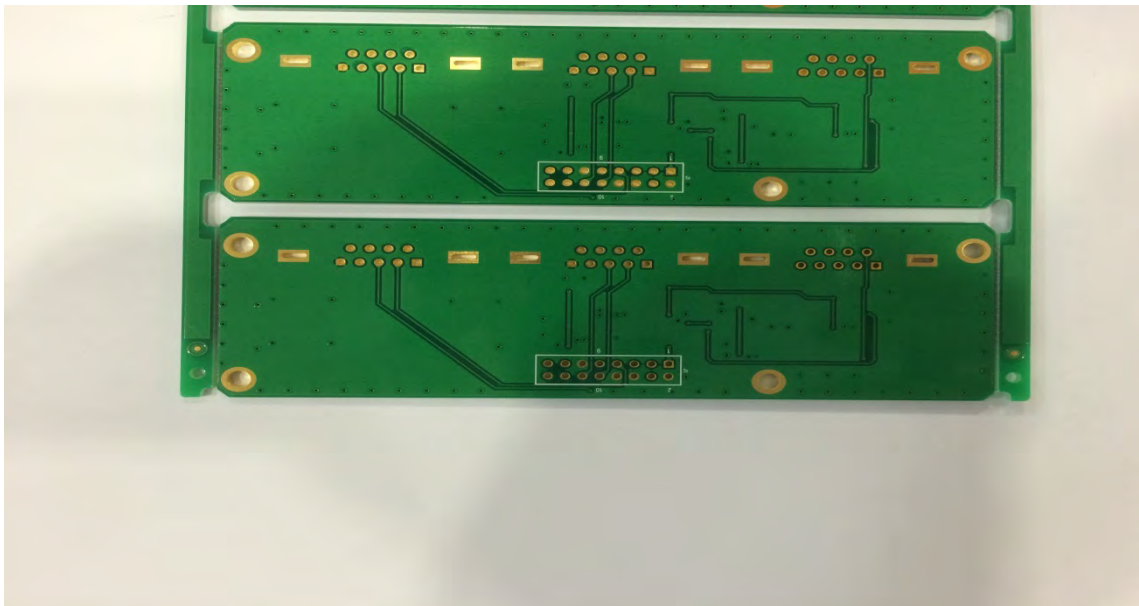
### **2. Bottom of Internal PCB1**



### 3. Top of Internal PCB2

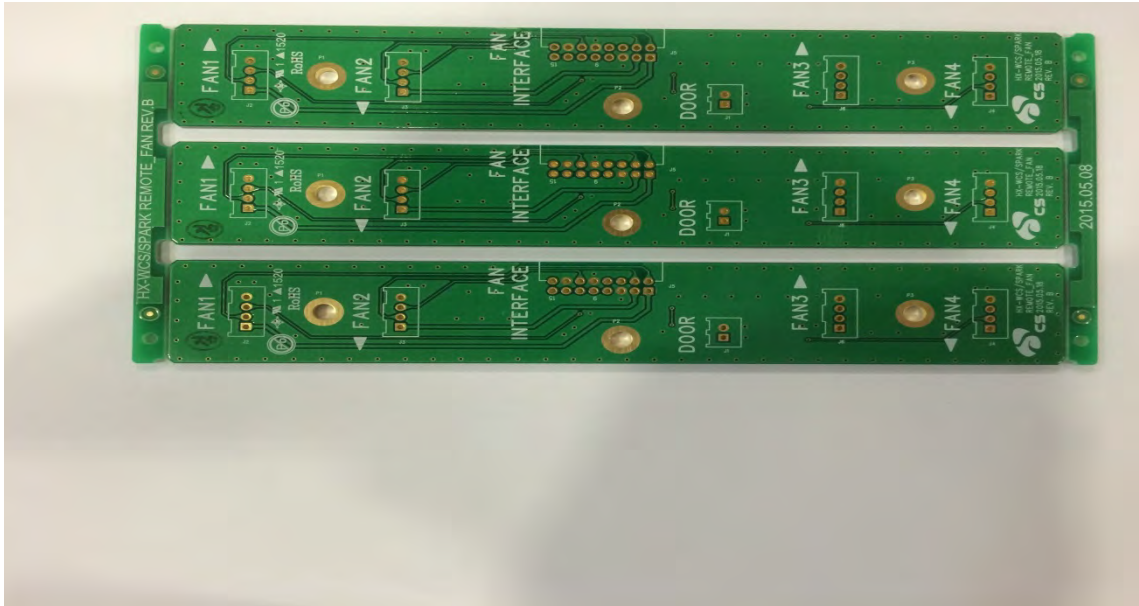


### 4. Bottom of Internal PCB2





### 5. Top of Internal PCB3



### 6. Bottom of Internal PCB3

