

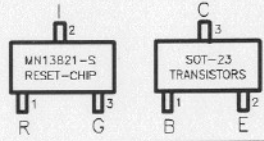
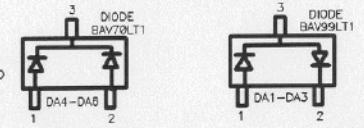
NORMAL / DUAL FUNCTION JUMPER
(IN FOR NORMAL TX OPERATION)

SEQUENCE TIMING
(SEE CHART)

SEQUENCE TIMING CHART

JP2	JP1	TIMING	# IN RTCC
OUT	OUT	18SEC	0
OUT	IN	5SEC	4
IN	OUT	3SEC	8
IN	IN	0SEC	12

NOTES:
ALL RESISTORS ARE 1206 UNLESS OTHERWISE STATED
ALL CAPS ARE 0805 UNLESS OTHERWISE STATED
ALL TRANSISTORS ARE SOT-23



Title MS SEDC0 TX-DUAL FREQ/ FUNCTION			
Size B	Number CP-TX HAND HELD A	Rev	
Date 8-8-03	Drawn by RBJ		Sheet of
Filename			