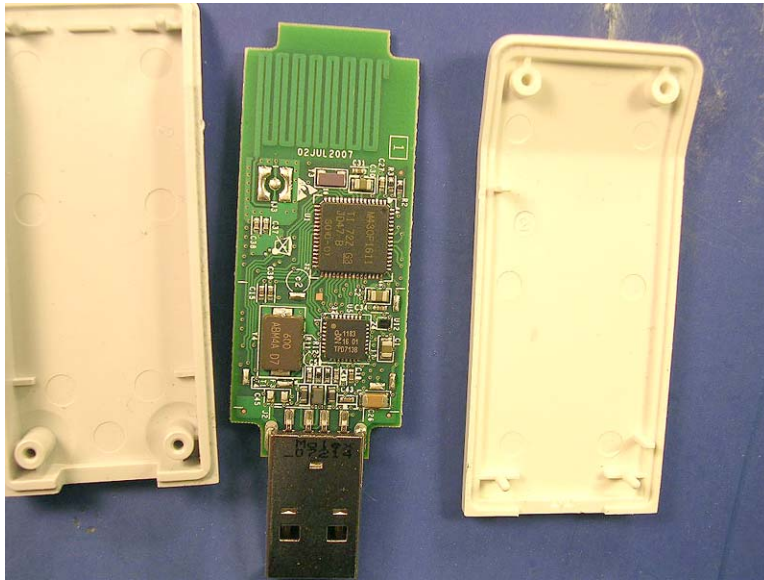
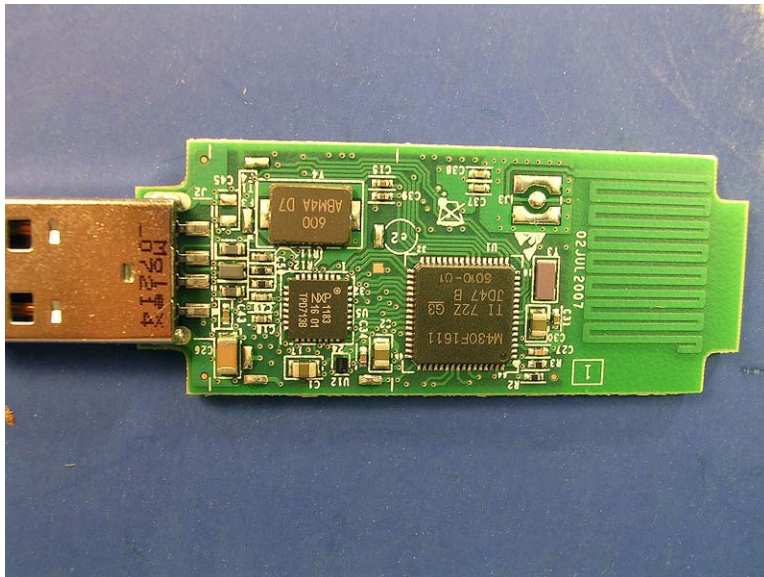


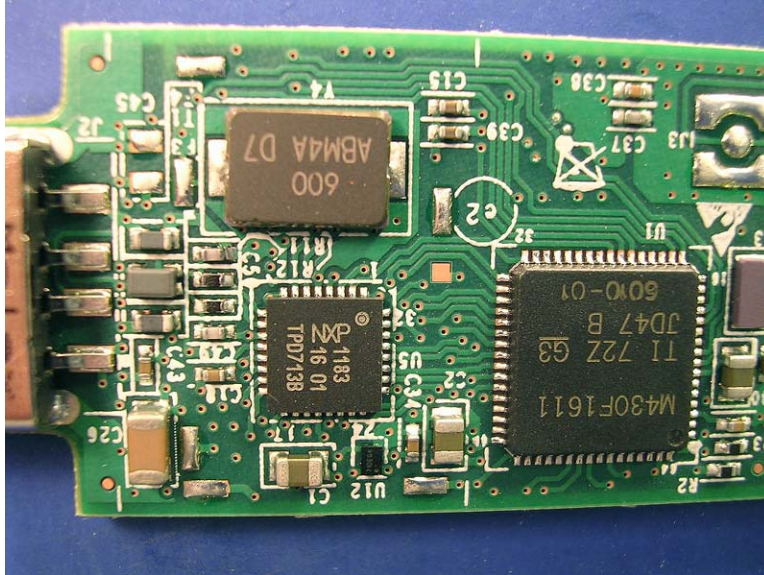
# Internal Photos



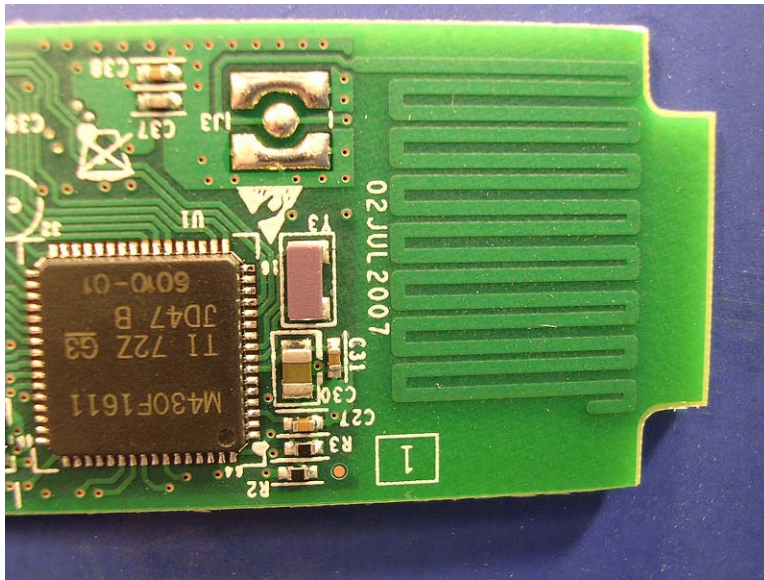
eut exposed



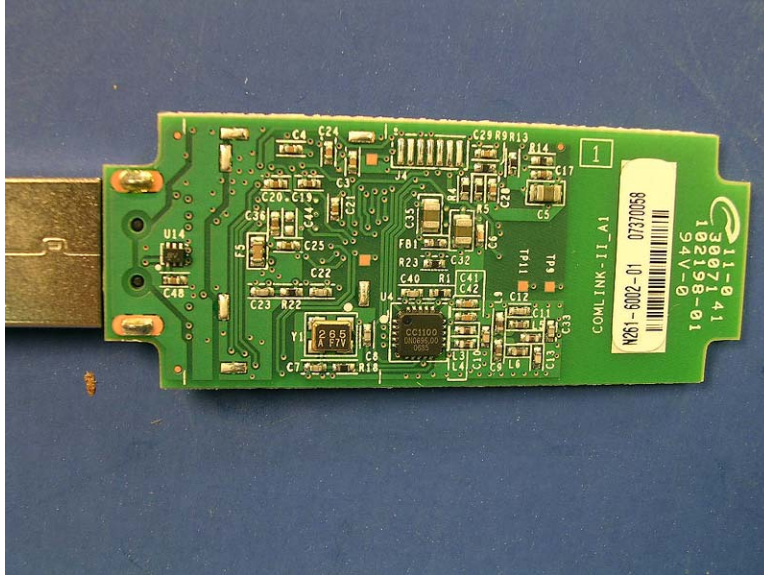
pcb top\_01



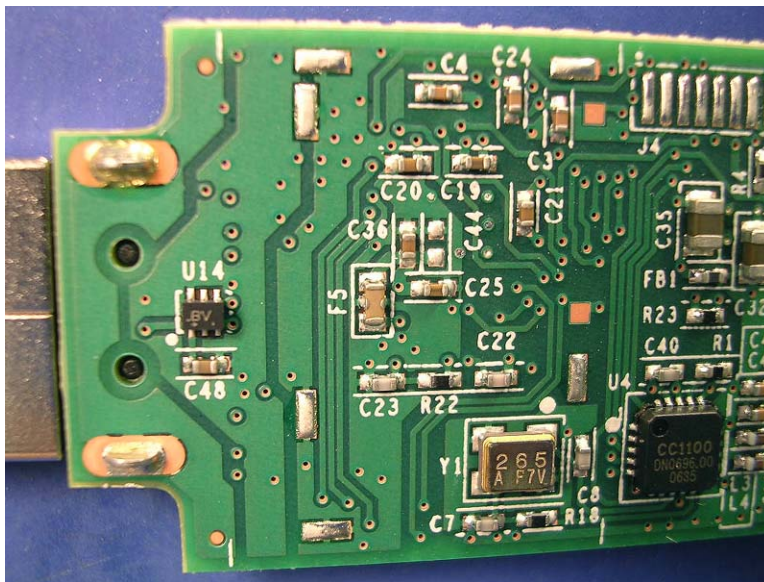
pcb top\_02



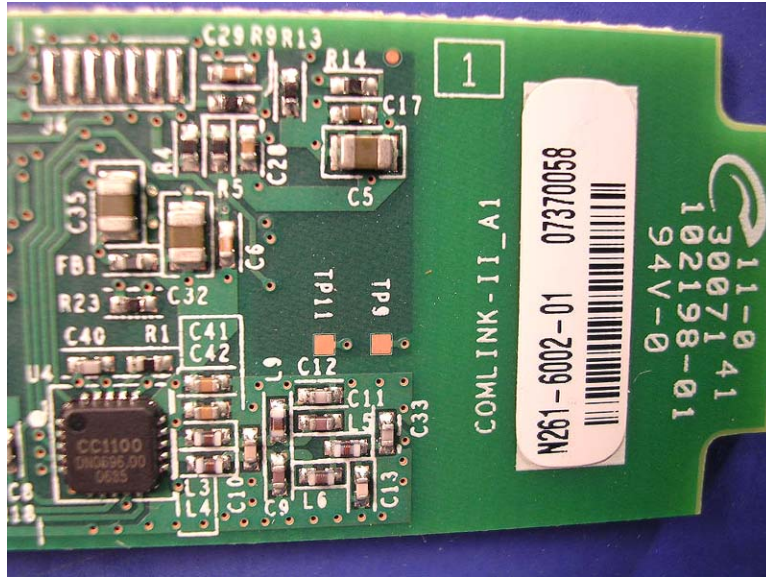
pcb top\_03



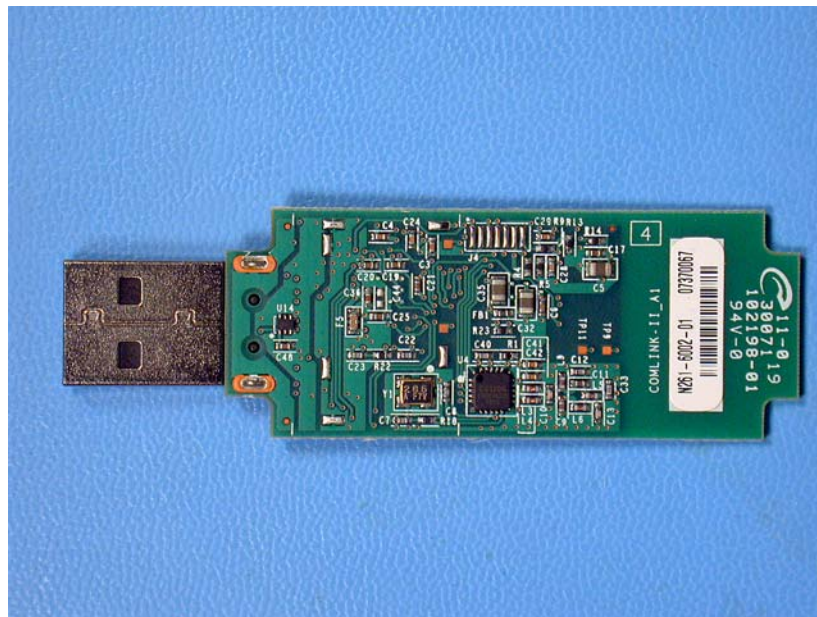
pcb bottom\_01



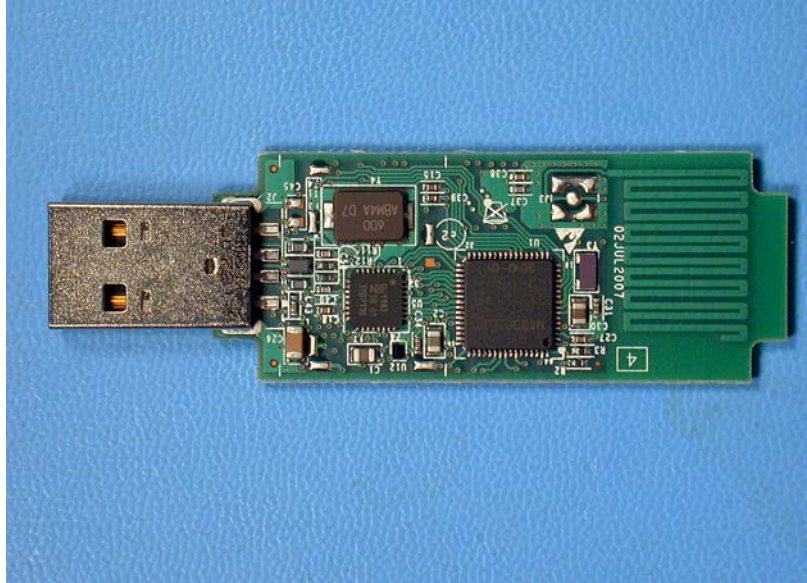
pcb bottom\_02



pcb bottom\_03



PCB Comlink Bottom 3



**PCB Comlink Top2**