FCC ID: OGM2K-018-49

Duty Cycle Correction During 100 msec:

Each function key send a different series of characters, but each packet period (53.73 msec) will not exceeds a series of 4 long (781 µsec) and short 58 short (293 µsec) pulses, transmit duty cycle would be considered (4 x 0.781ms) + (58 x 0.293ms) per 53.73 msec = 37.5% duty cycle.

Duty cycle correction = 20 log (0.375) = -8.5dB

Figure A to C show the characteristics of the pulse train for one of these function.

FIGURE A: PULSE TRAIN

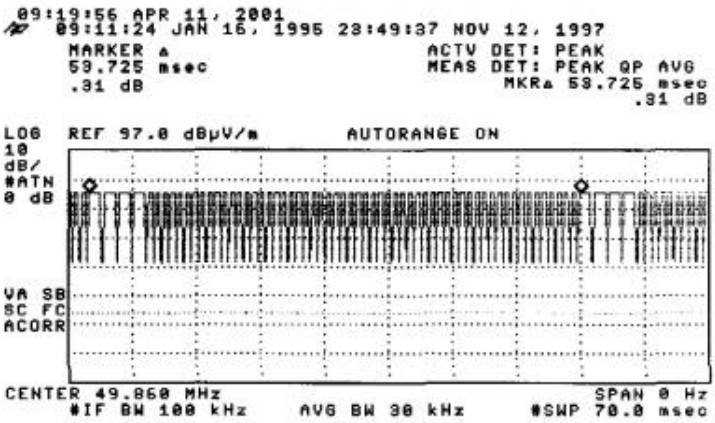

FIGURE B: LONG PULSE

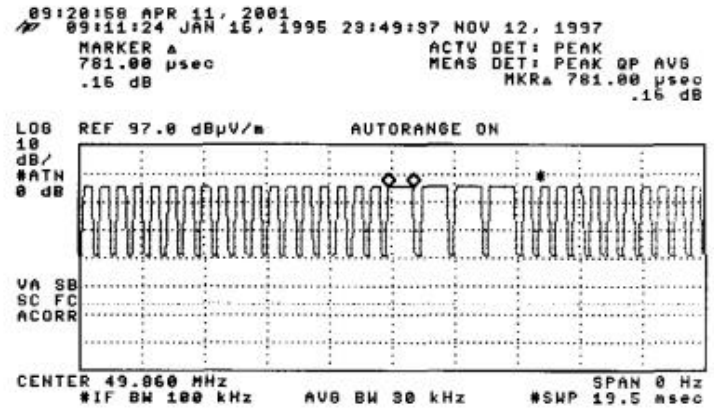

FIGURE C: SHORT TRAIN