

FCC ID: OGD10430200

Reference: EA97193

Date: May 18, 2000

Theoretical processing gain and Jammer to Signal ratios (11 Mbps DSSS radio).

Data rate (Mbps)	Modulation type	Symbol rate (SyR)	Spread rate (SpR)	Processing gain	Es/No+Lsys (dB)	Coding gain (dB)	Calculated J/S incl. code gain (dB)	Allowed J/S for 10 dB, 10dB-(Es/No+Lsys) (dB)
		Symbols/second (Msps)	Chips/symbol (Mcps)	$G_p=10\log(Spr/Syr)$				
1	DBPSK	1	11	10.4	22.5	0	-12.1	-12.5
2	DQPSK	1	11	10.4	23.0	0	-12.6	-13.0
5.5	CCK	1.375	8	9.0	22.2	2	-11.2	-12.2
11	CCK	1.375	8	9.0	22.7	2	-11.7	-12.7

All data as stated above has been provided by Harris Semiconductor and the datasheet of the HFA3861 DSSS baseband processor.