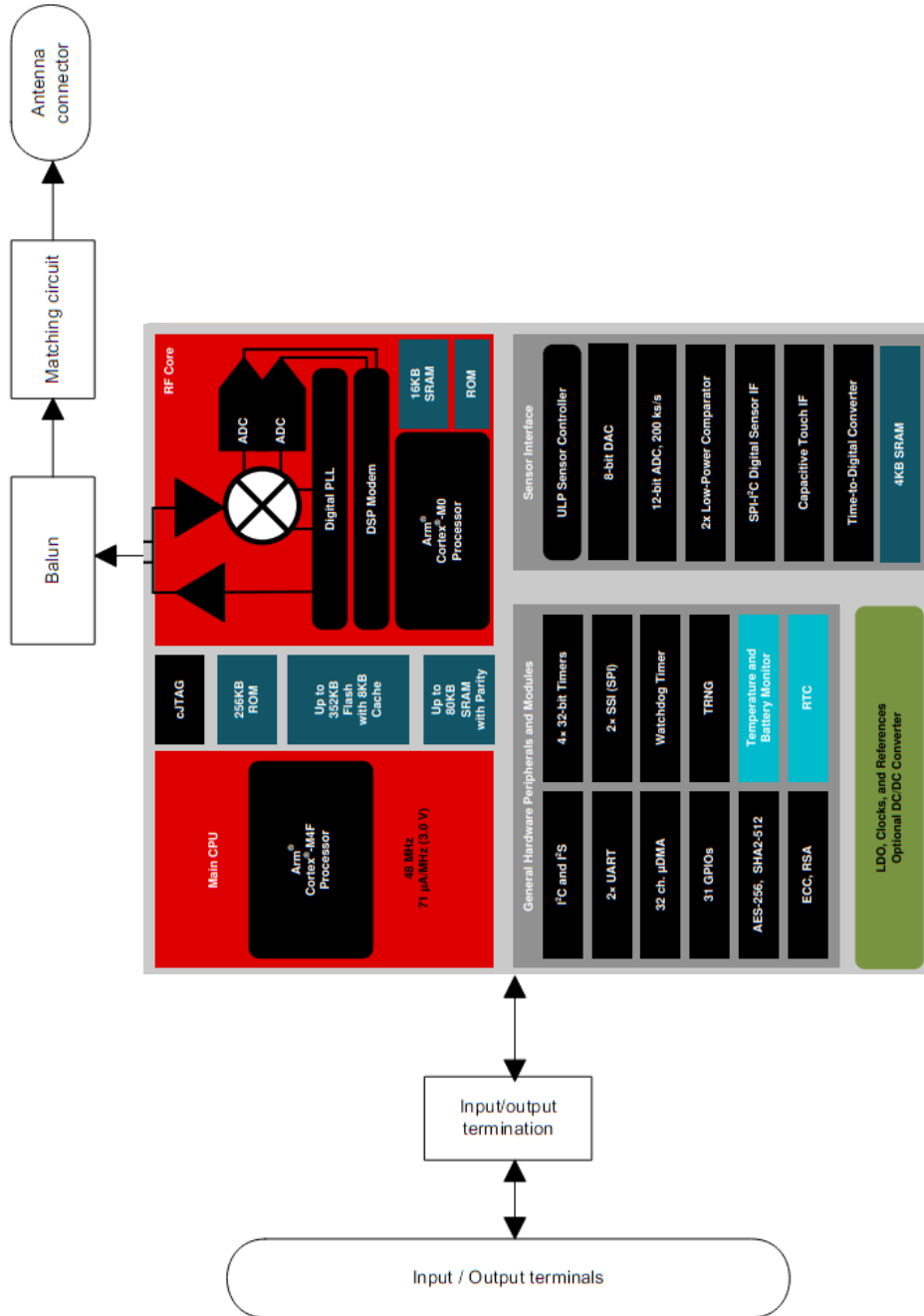


# Multistandard radio module (MSR module) block diagram

## MSR module block diagram



## RF chip block diagram



## Mode of operation

The radio part is software defined so it can operate in different modes, in the module this is as a Bluetooth or 802.15.4 transceiver. All time critical tasks for the radio are handled by the ARM M0 CPU which is dedicated for the radio. The communication with the main CPU is handled over a shared RAM. The main micro is an ARM M4F with up to 704K flash and 144K SRAM and 256K ROM (mostly used for operating system, BLE and 802.15.4 stack).