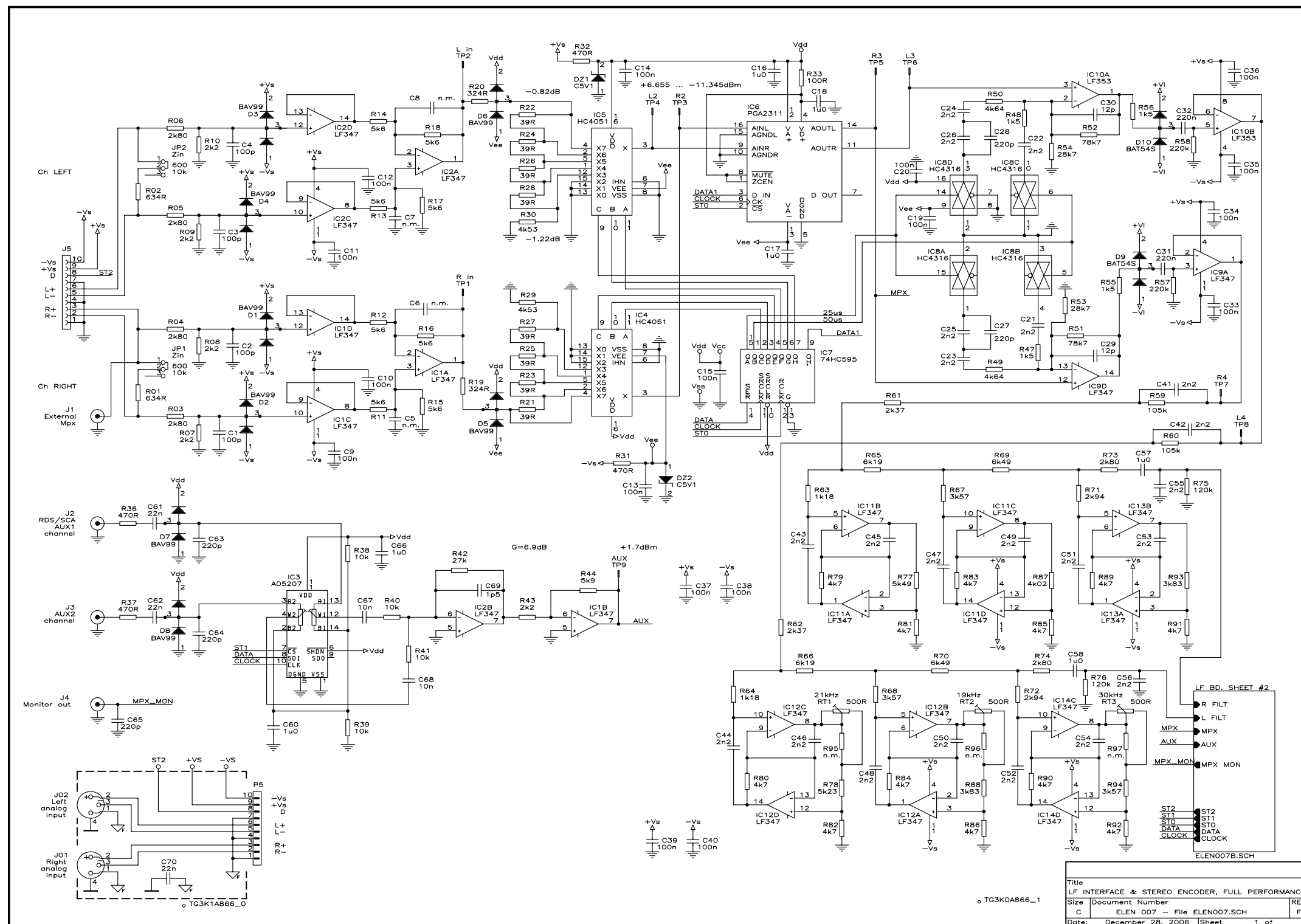


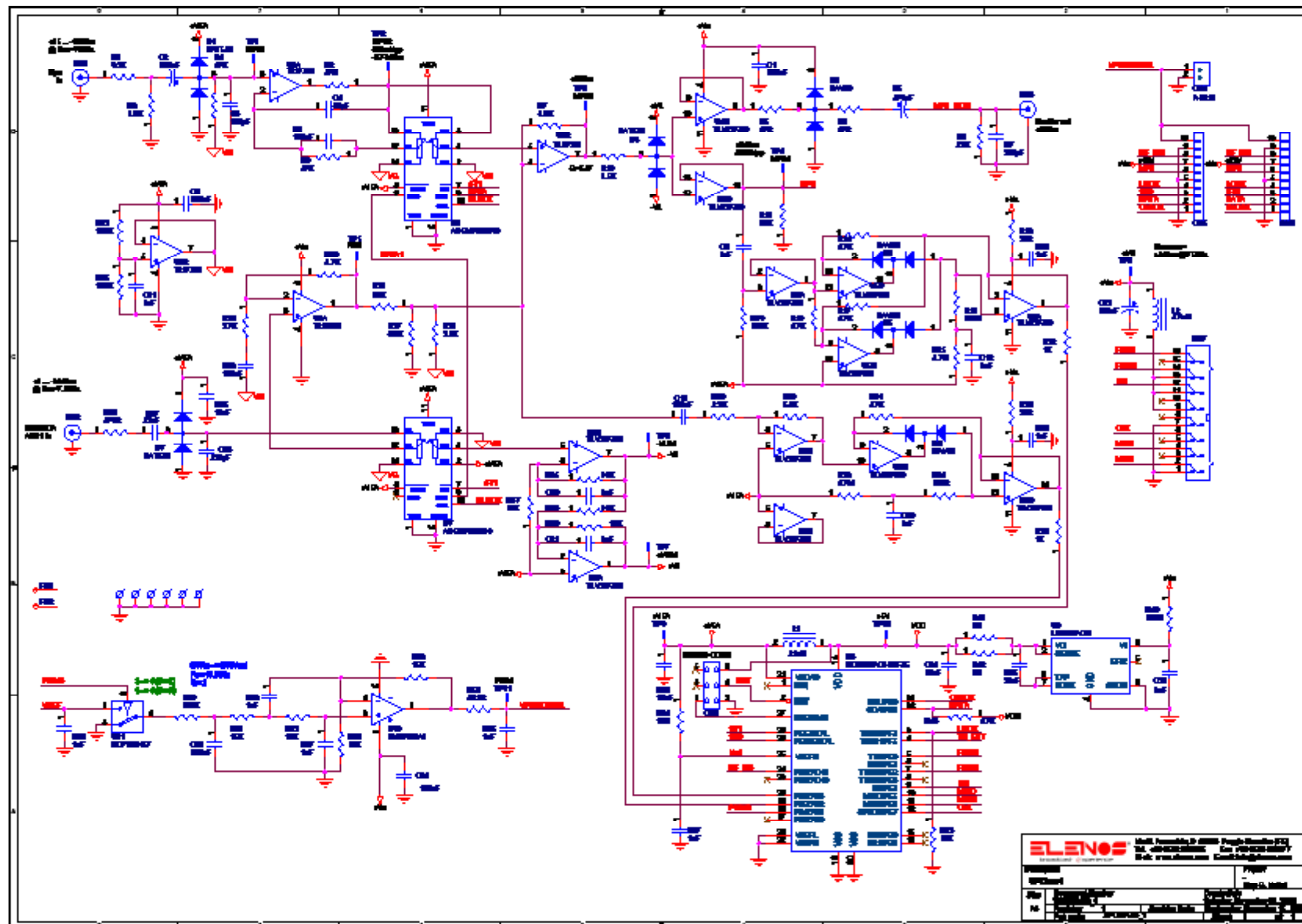
Schematics

STEREO CODER with MPX and AUX inputs (TG3K0B066-1)

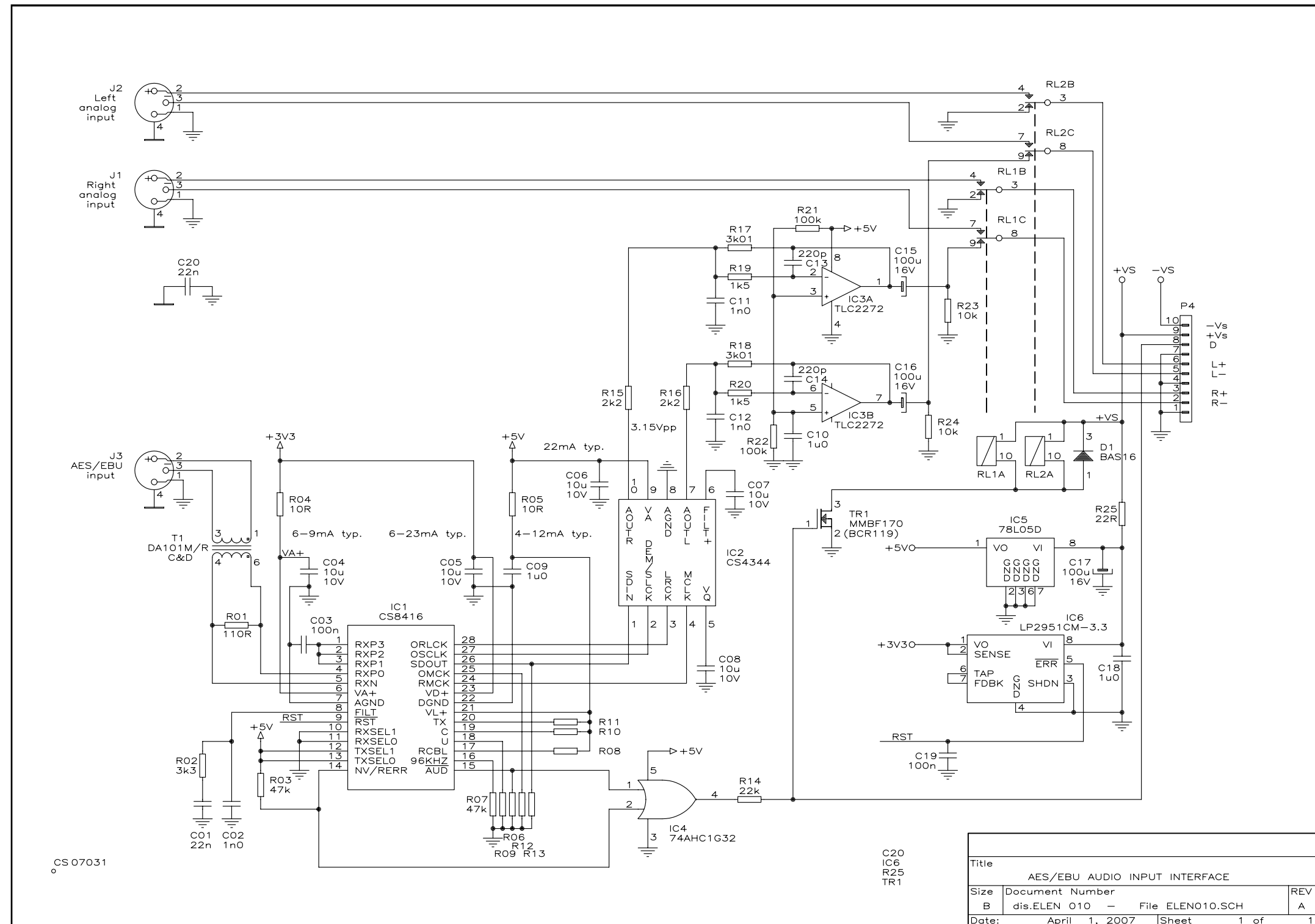




MPX CODER with AUX inputs (TG2UGA890)



STEREO + AES/EBU INPUTS (TG3K2A867-1)



22

FRONT PANEL

B_D[0..7] 1 2 B_D1
B_D2 3 4 B_D3
B_D4 5 6 B_D5
B_D6 7 8 B_D7
9 10
B_/CS1 11 12 B_/CS0
B_/RD 13 14 B_/WD
B_A0 15 16 B_D/OE
SCLK3 17 18 SPI_/CS_PANEL
MOSI3 19 20 MISO3
21 22
TXD1 23 24 RXD_1_F
/PGM_MODE 25 26 TXE1
27 28
VCC 29 30 VCC
+12V 31 32 +12V
33 34

CN6 LATCHNRIMV34

I/O

R78 10K
R116 10K
R61 10K
R55 10K
33 34
31 32
29 30
27 28
25 26
23 24
21 22
19 20
17 18
15 16
13 14
11 12
9 10
7 8
5 6
3 4
1 2

CN5 LATCHNRIMV34

POWER SUPPLY

VCC 15 16 VCC
+12V 13 14 +12V
-12V 11 12 -12V
9 10
7 8
5 6
3 4
1 2

CN7 MODU2MH8+8

C78 100uF 25V
C105 100uF 25V
C106 100uF 25V
C75 100nF
C101 100nF
C107 100nF

CPU

B1
B_D[0..7] A0 A1 A2 A3 A4 A5 A6 A7
B_A0 A0
B_WD /WD
B_/RD /RD
B_/CS0 B_/CS0
B_/CS1 B_/CS1
B_D/OE B_D/OE
/PGM_MODE /PGM_MODE
SCLK3 SCLK3
MOSI3 MOSI3
MISO3 MISO3
SPI_/CS_PANEL SPI_/CS_PANEL
/CS_IO /CS_IO
/CSDAC /CSDAC
SERIAL_OE SERIAL_OE
SCLK4 SCLK4
MOSI4 MOSI4
MISO4 MISO4
/CSETH /CSETH
/CSUART4 /CSUART4
/INT3 /INT3
/INT4 /INT4
RXD0 RXD0
TXD0 TXD0
TXEN0 TXEN0
RXD1 RXD1
TXD1 TXD1
TXE1 TXE1

ANALOG INPUT

B2
AN[1..8] AN[1..8]
MPX0 MPX0
MPX1 MPX1
MPX2 MPX2
Analog_input

ANALOG OUTPUT

B3
MDAC0 MDAC0
MDAC1 MDAC1
VREF VREF
/BIT9 /BIT9
/RES /RES
/RESLATCH /RESLATCH
PWM PWM
PWM1 PWM1
INT_ENABLE INT_ENABLE
EXT_ENABLE EXT_ENABLE
PLL_LOCK PLL_LOCK
Analog_output

Exciter Interface

B4
SCLK3 SCLK3
MOSI3 MOSI3
MISO3 MISO3
/CS_EXCITER /CS_EXCITER
Exciter_interface

PSU Interface

B5
RXD_2 RXD_2
TXD_2 TXD_2
TXEN_2 TXEN_2
PSU_ENABLE PSU_ENABLE
PSU_C PSU_C
PSU_T PSU_T
PSU_V PSU_V
PSU_I PSU_I
PSU_interface

Legend

PWM1 = PSU (analogica, 0/+5V)
PWM = Ipa (+/-12V)
DAC0 = Ventole(0/+5V)
DAC1 = non utilizzato.

Component Values

Component	Value
R78	10K
R116	10K
R61	10K
R55	10K
R75	10K
R62	10K
R60	10K
R109	10K
R110	10K
C78	100uF 25V
C105	100uF 25V
C106	100uF 25V
C75	100nF
C101	100nF
C107	100nF
C50	100nF

U7

SN74LVC1G08DBVT

H1 H2 H3 H4

ELENOS
broadcast experience

Via G. Amendola, 9 44028 Poggio Renatico (FE)
Tel. +39 0532 829965 Fax +39 0532 829177
Web: www.elenos.com E-mail: info@elenos.com

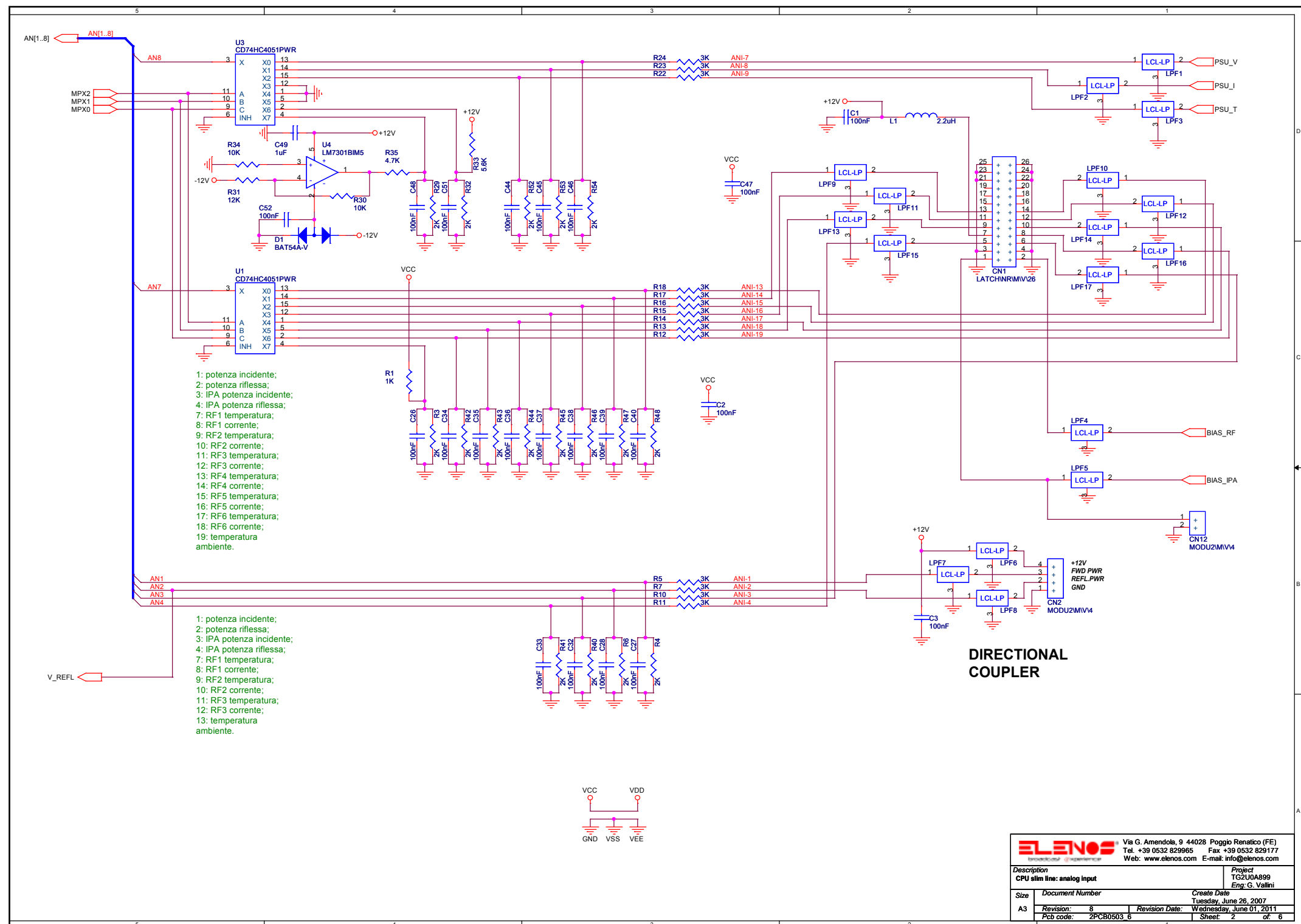
Description
CPU slim line: main

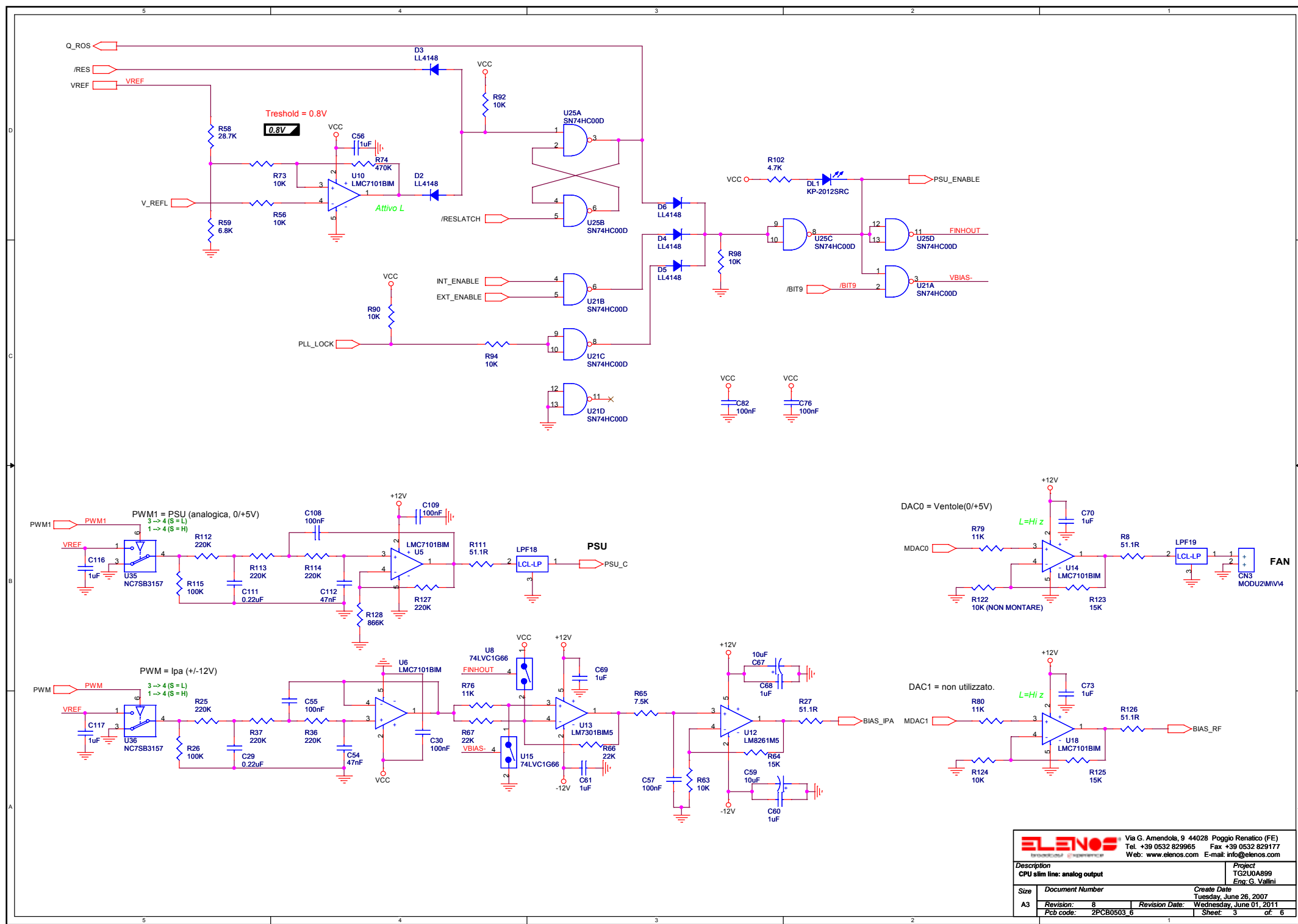
Project
TG2U0A899
Eng: G. Vallini

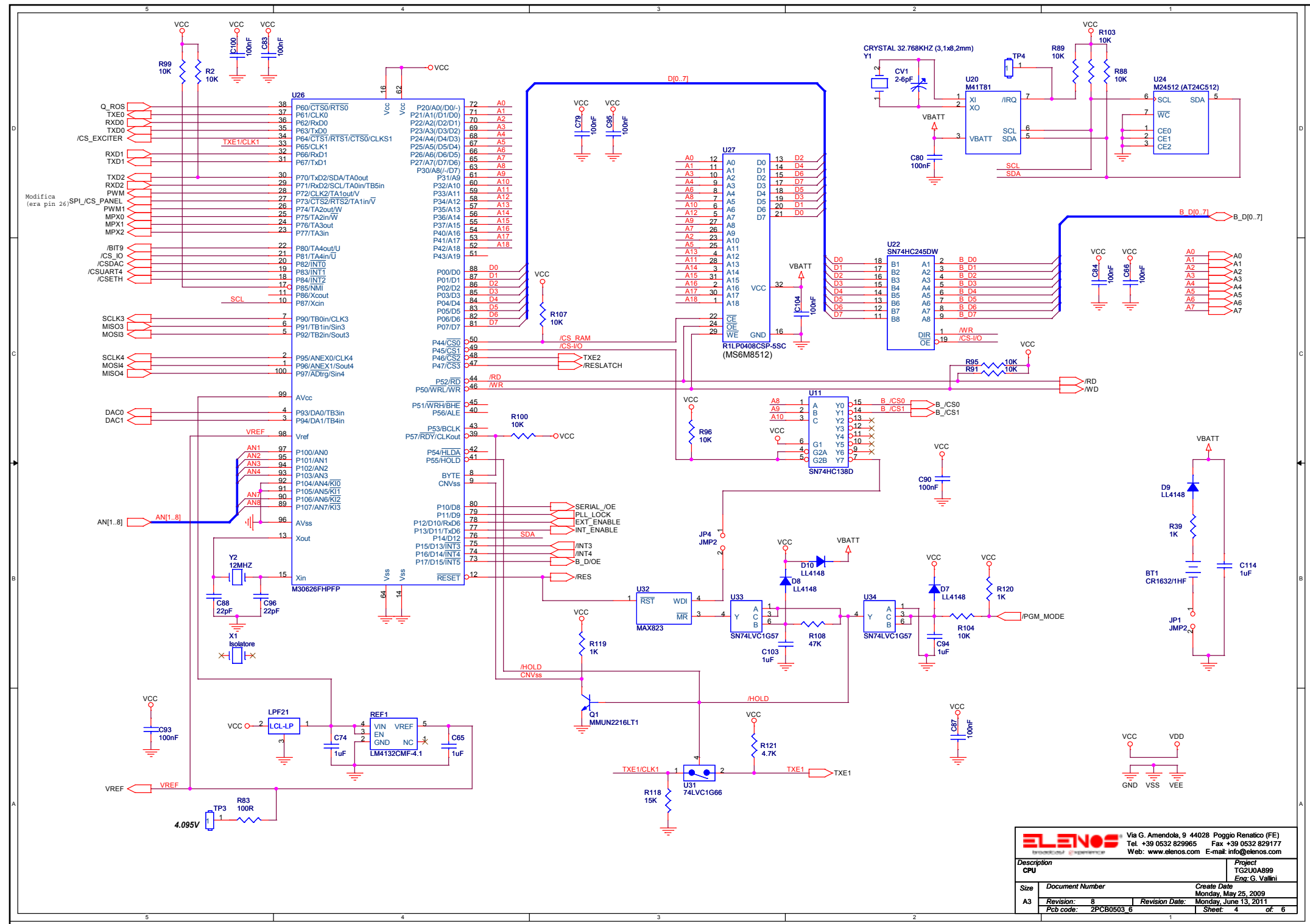
Size
A4

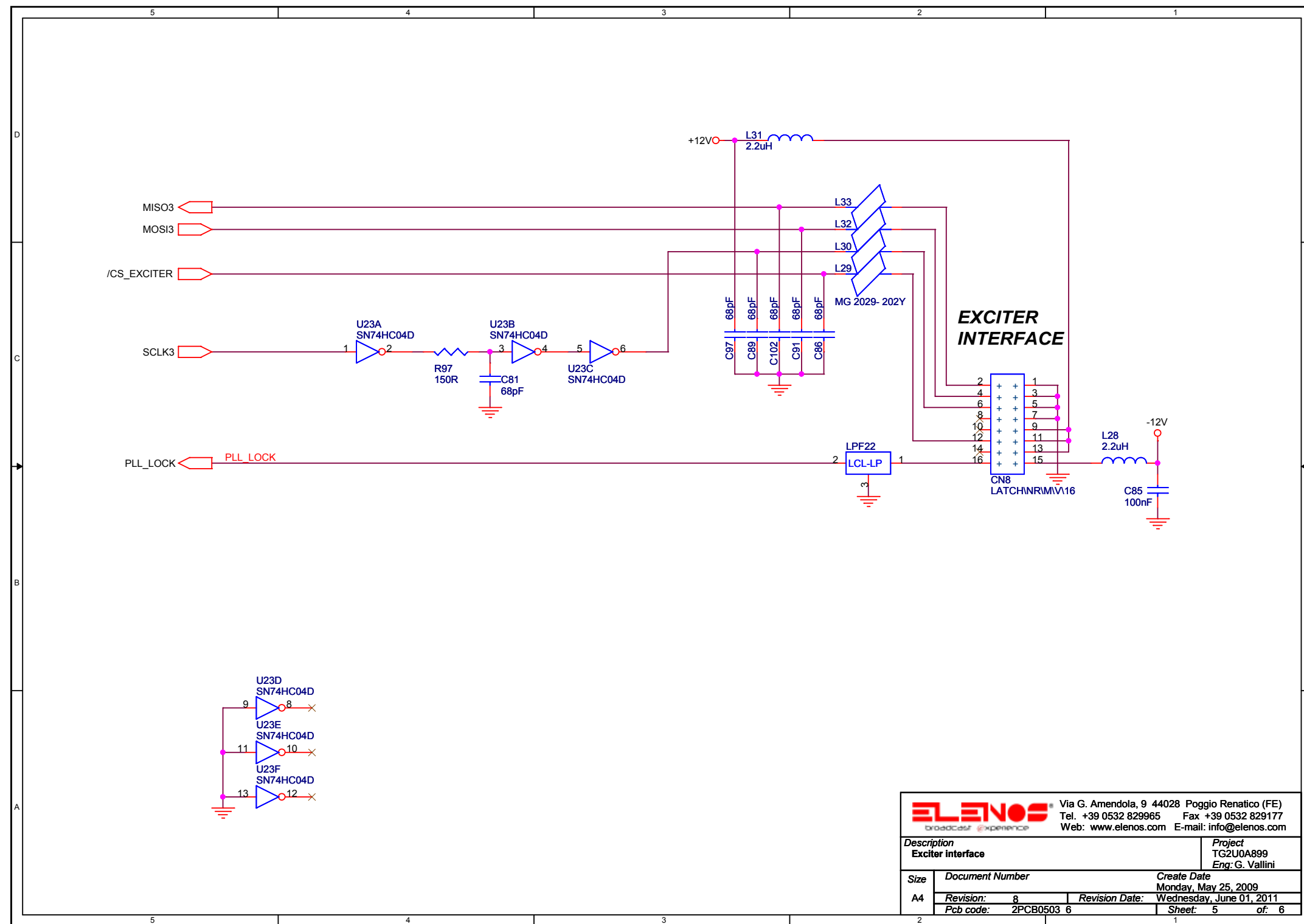
Document Number
Revision: 8
Pcb code: 2PCB0503 6

Create Date
Tuesday, January 15, 2008
Revision Date: Wednesday, June 01, 2011
Sheet: 1 of: 6



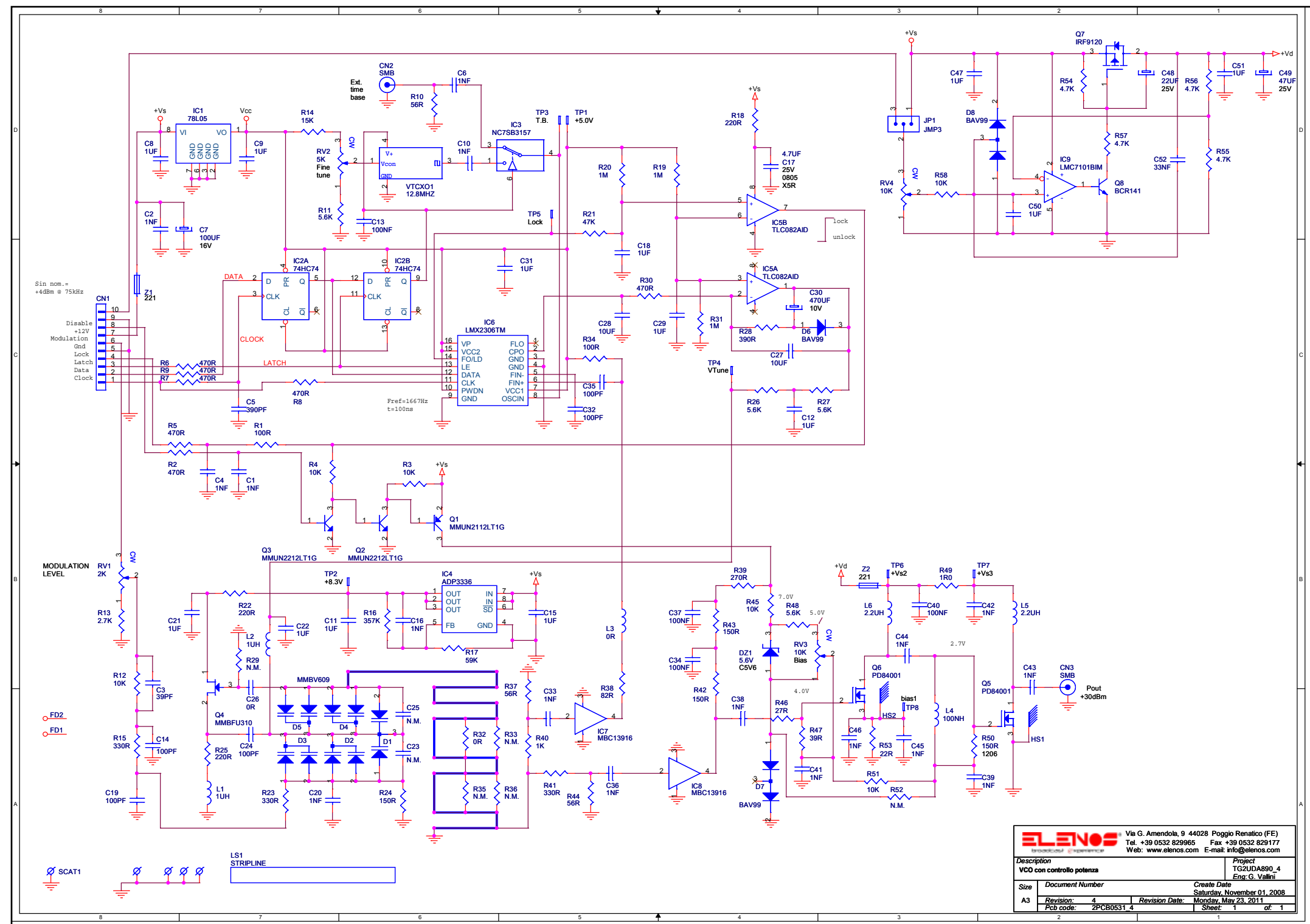




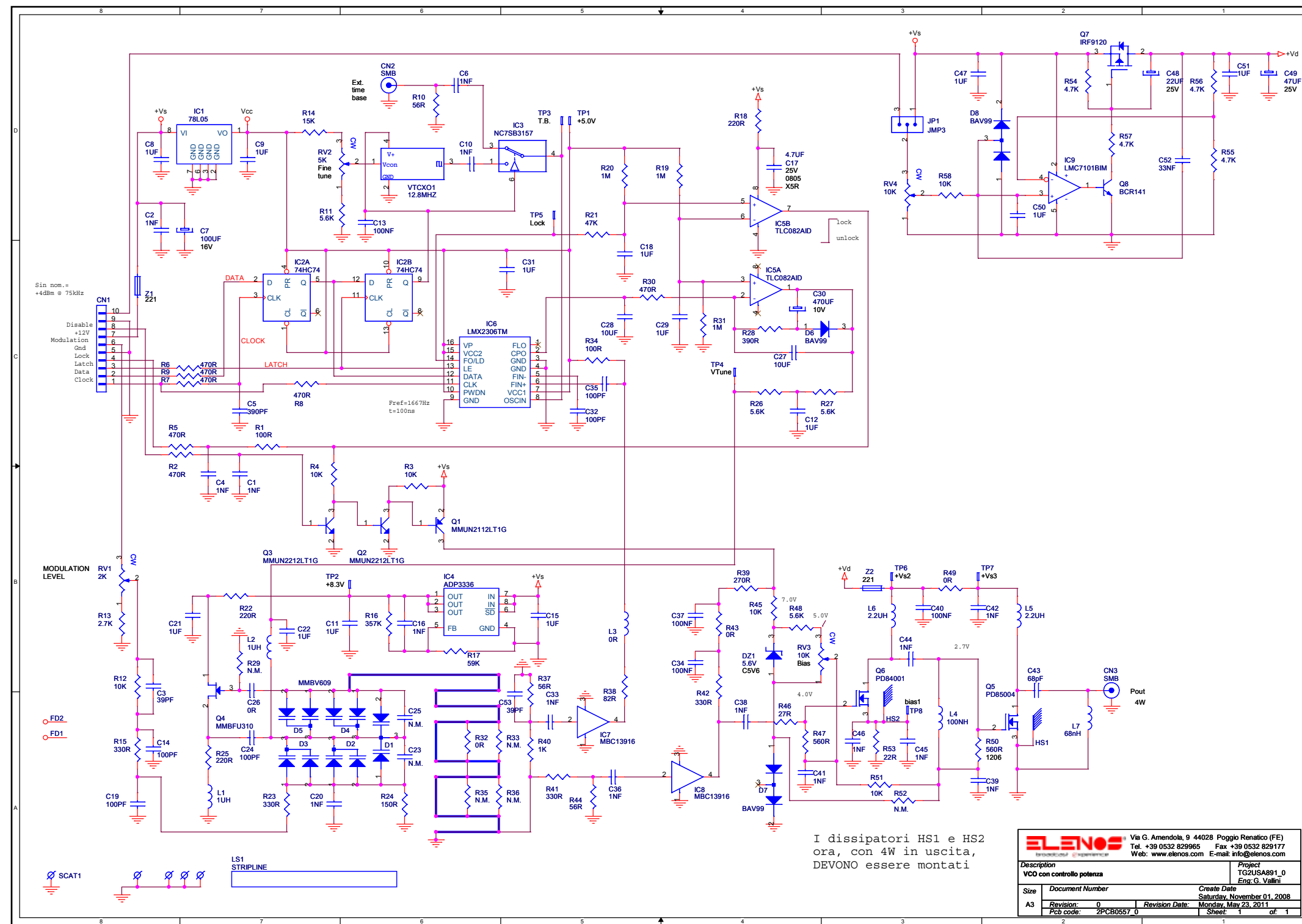


ELENOS broadcast experience		Via G. Amendola, 9 44028 Poggio Renatico (FE) Tel. +39 0532 829965 Fax +39 0532 829177 Web: www.elenos.com E-mail: info@elenos.com	
Description Exciter interface		Project TG2U0A899 Eng: G. Vallini	
Size A4	Document Number Revision: 8	Create Date Monday, May 25, 2009	Revision Date Wednesday, June 01, 2011
	Pcb code: 2PCB0503 6	Sheet: 5	of: 6

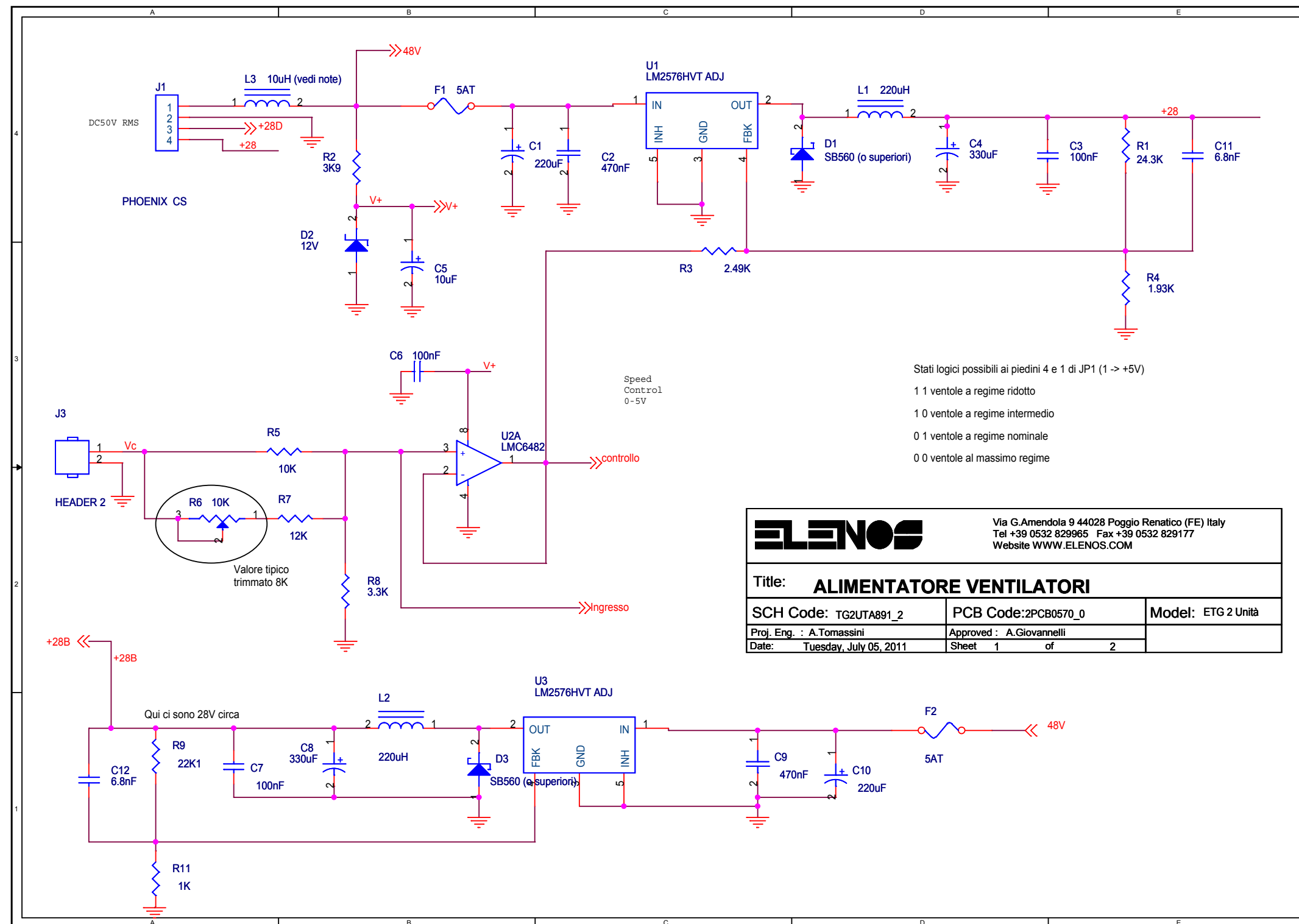
VCO (TG2UDA890-4)

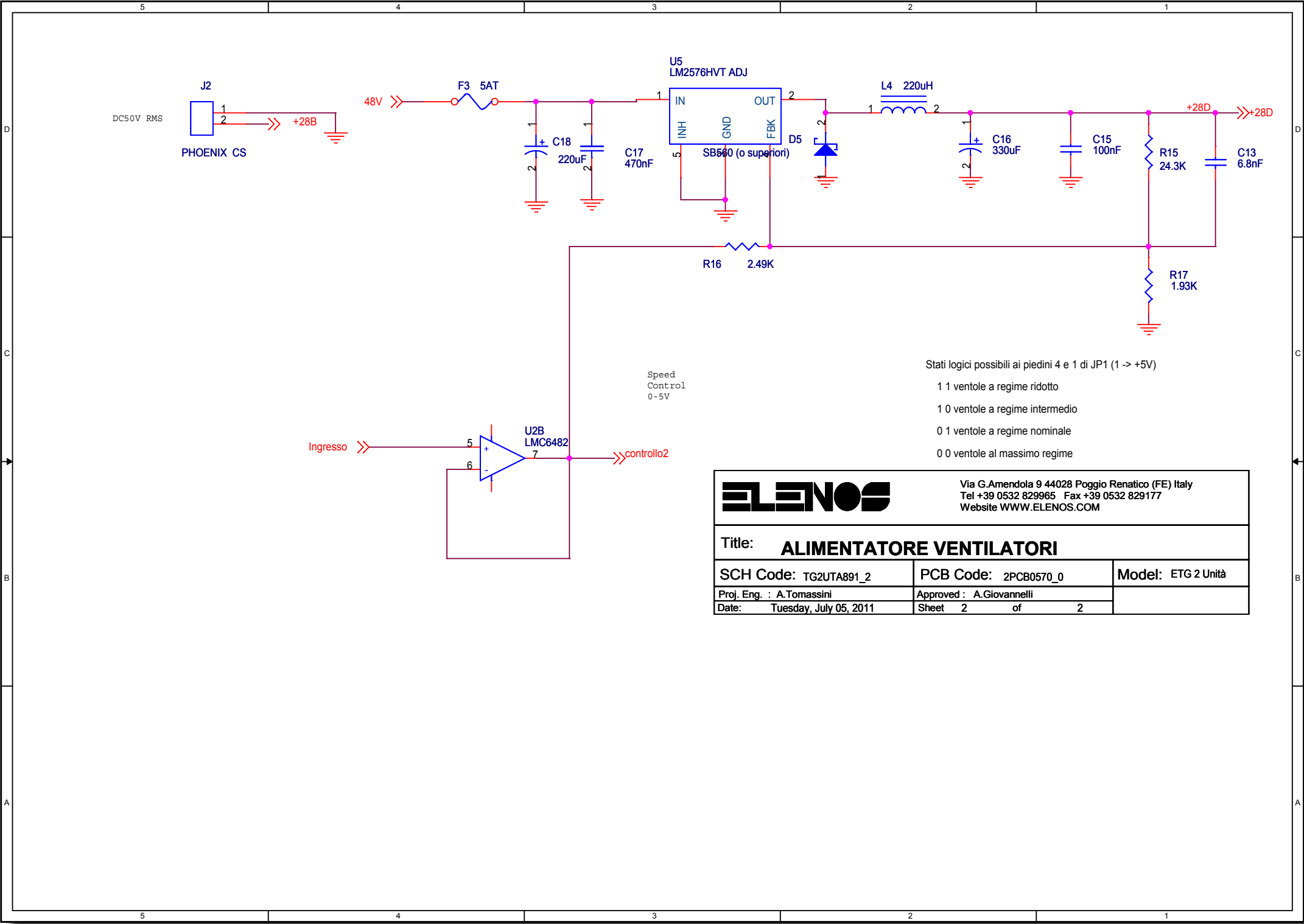


ENHANCED VCO (TG2USA891-0)

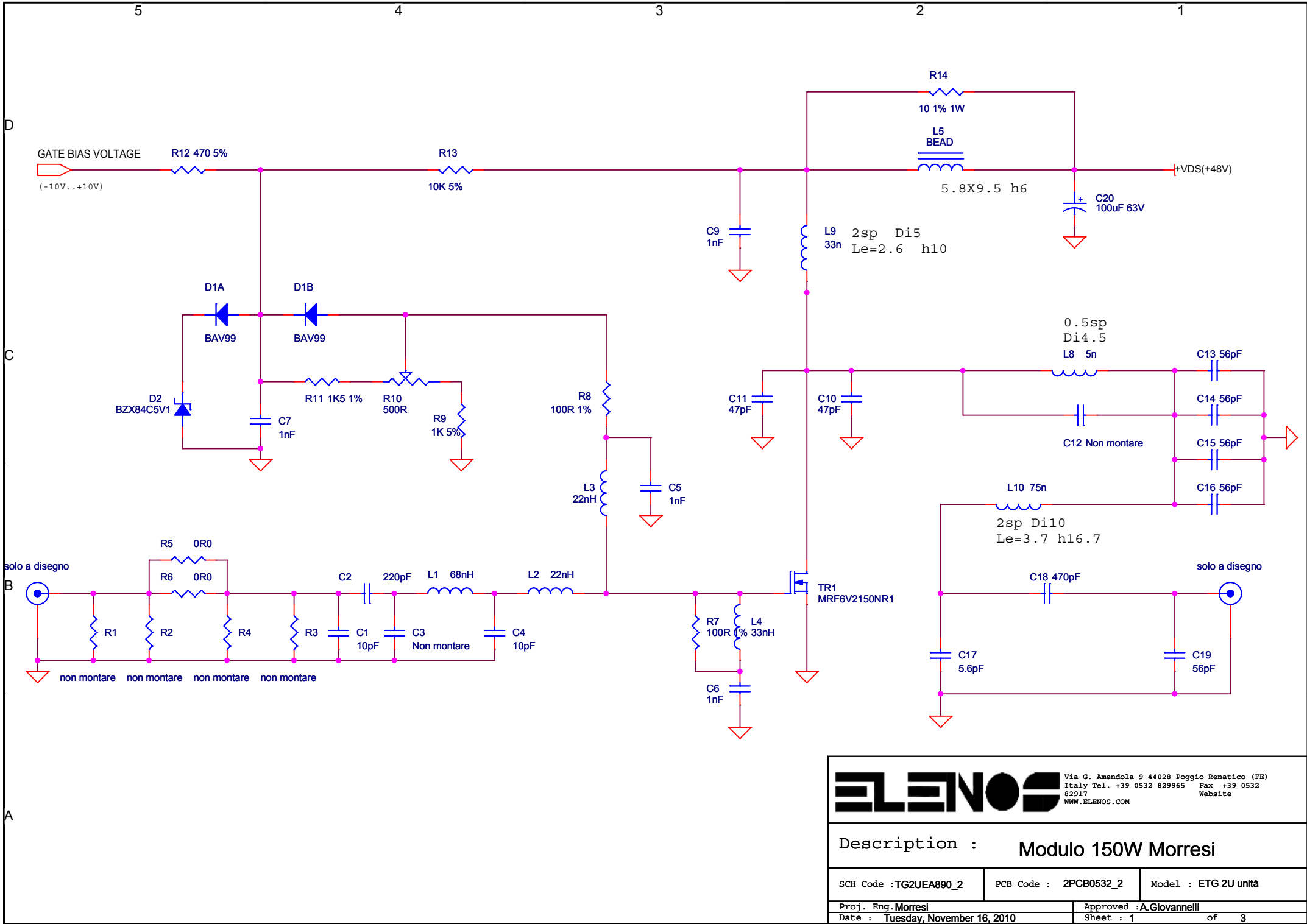


FAN & DRIVER POWER SUPPLY (TG2UTA891-2)





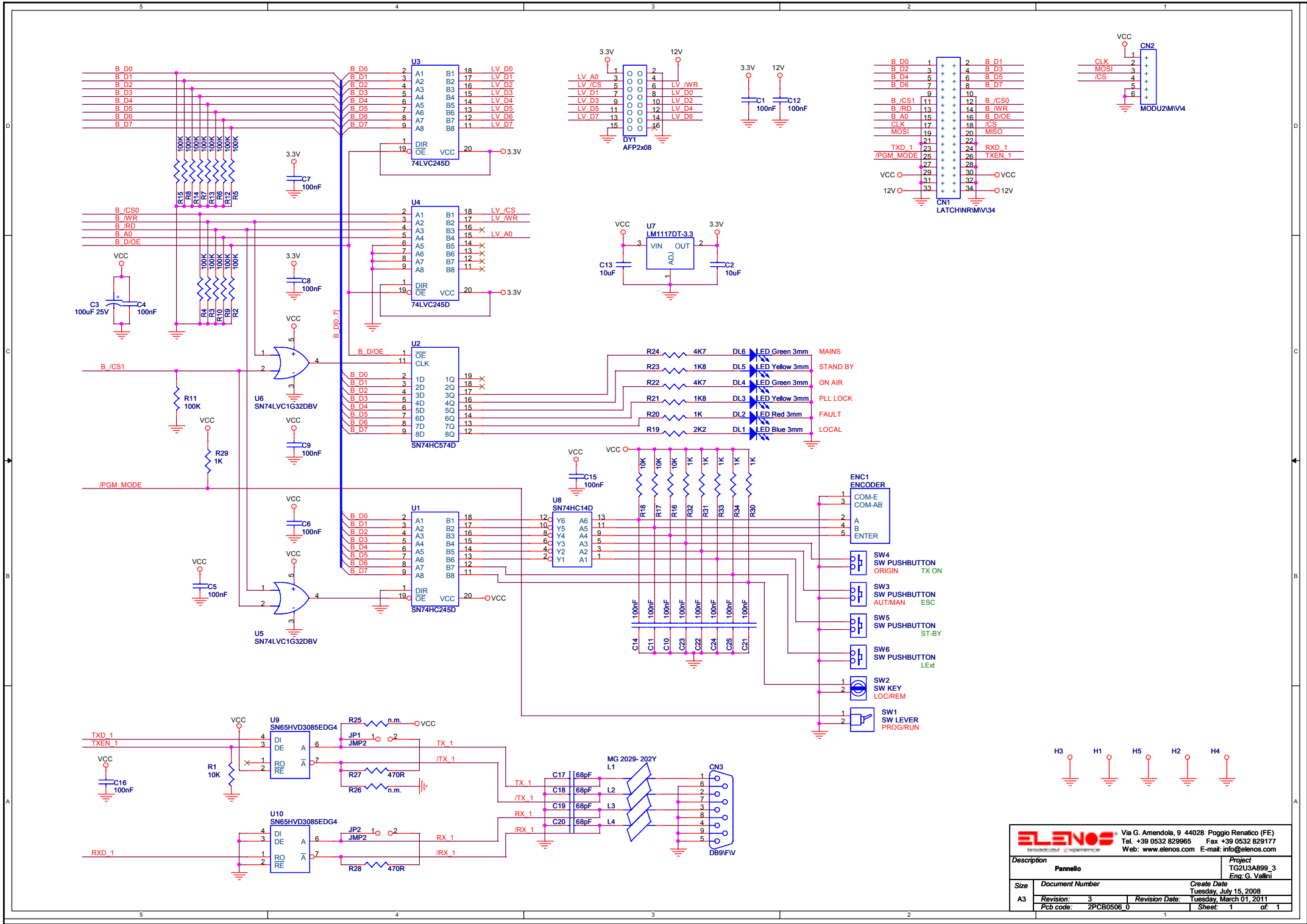
150W RF AMPLIFIER MODULE (TG2UEA890-2)





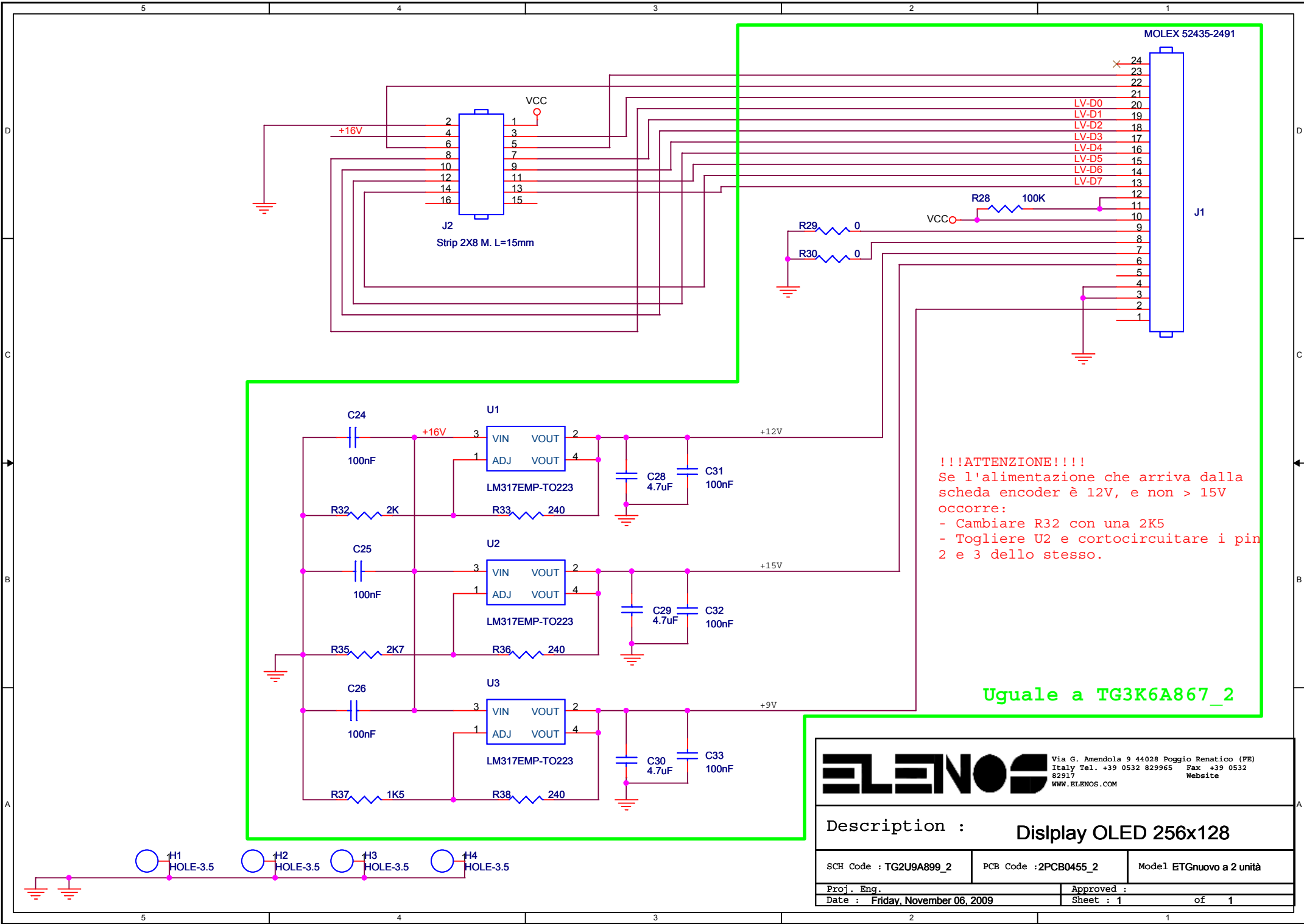


CONTROL PANEL (TG2U3A899)

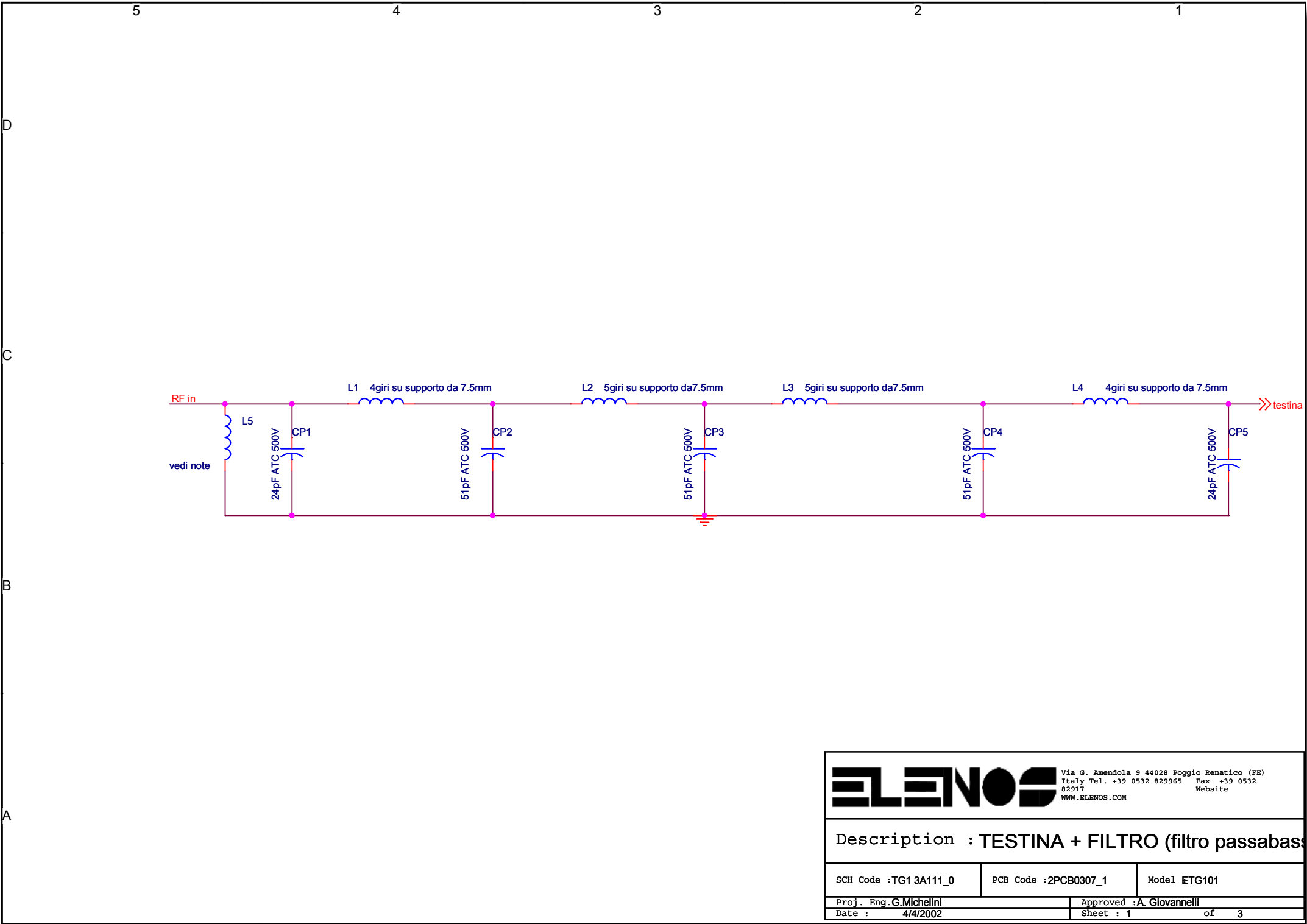


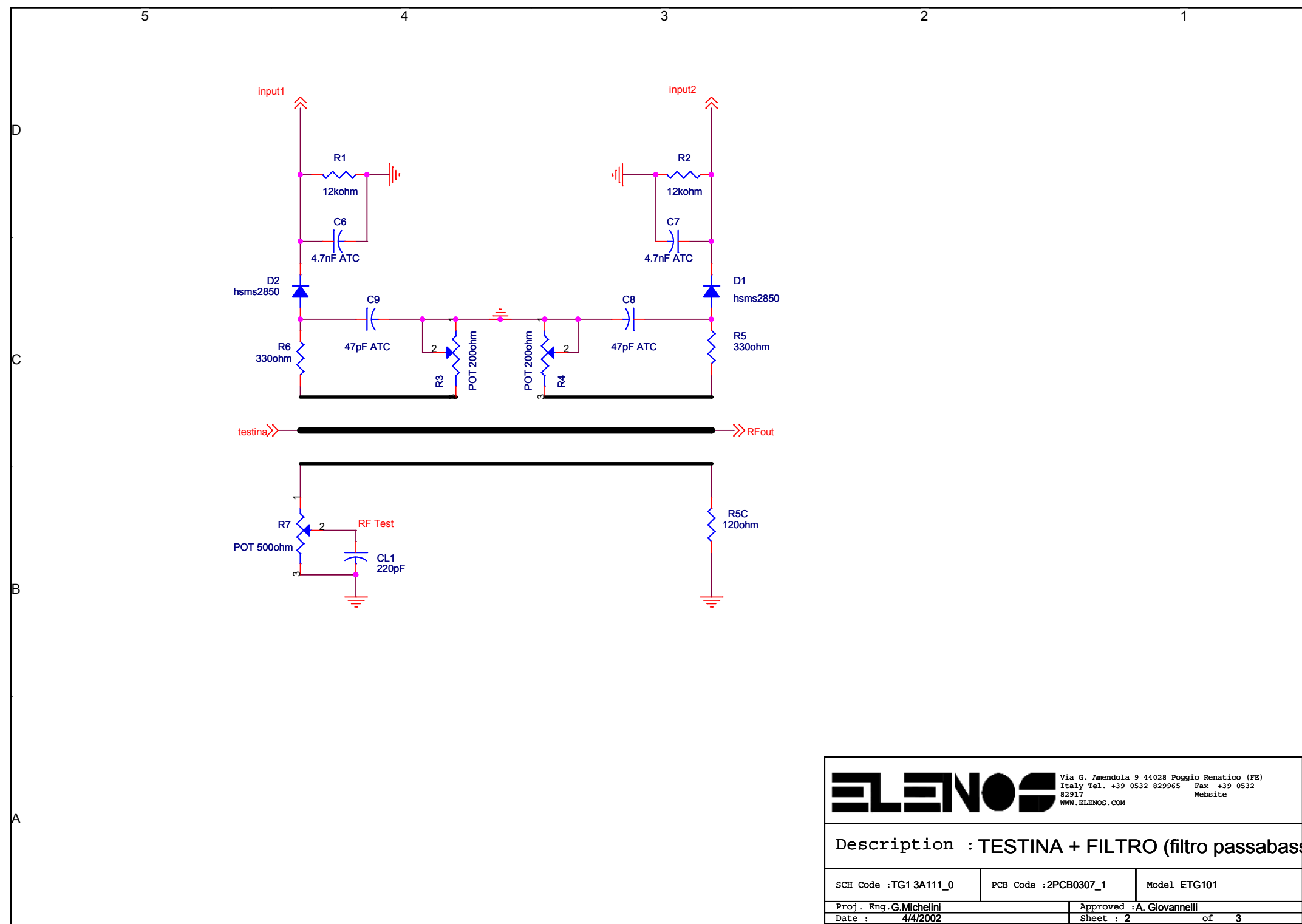
ELENOS		Via G. Amendola, 9 44028 Poggio Renatico (FE)	
		Tel. +39 0532 829965 Fax +39 0532 829177	
		Web: www.elekos.com E-mail: info@elekos.com	
Description		Pannello	
Size		Document Number	
A3		Revision: 3	
		Revision Date: Tuesday, March 01, 2011	
		Pcb code: 2PCB0506 0	
		Sheet: 1 of 1	

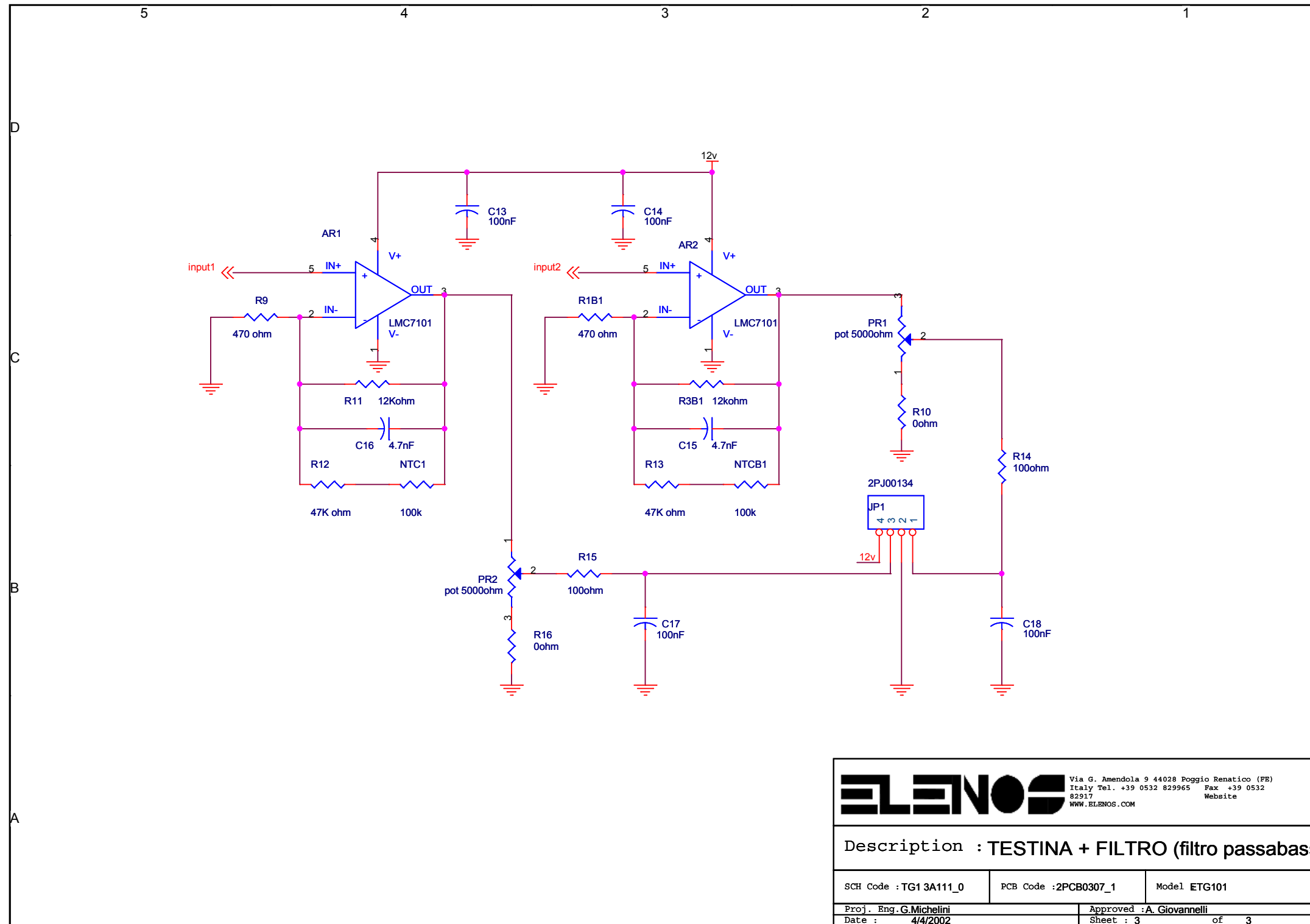
OLED DISPLAY ADAPTER (TG2U9A899)



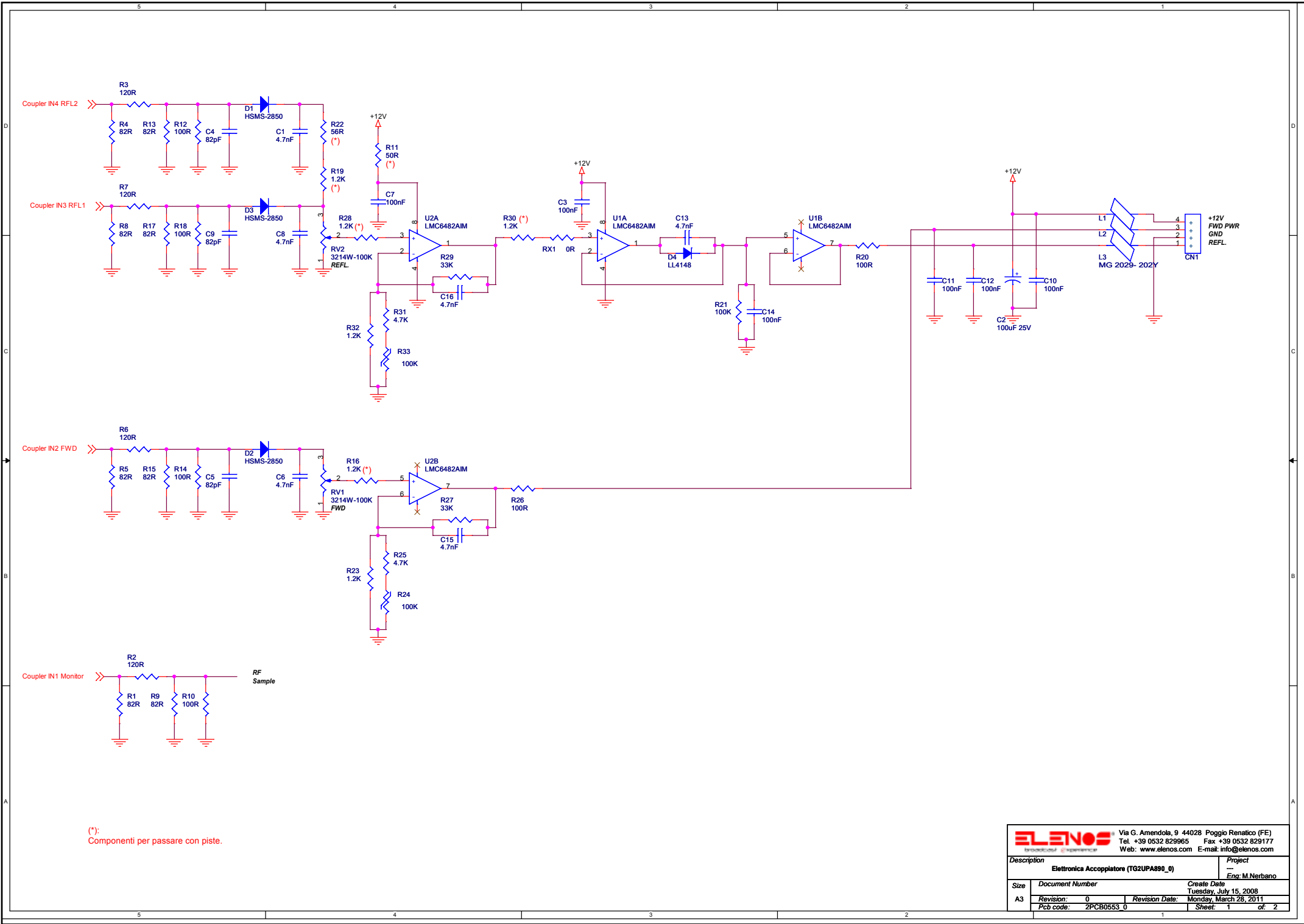
LOW PASS FILTER WITH DIRECTIONAL COUPLER (TG1 3A111-0)

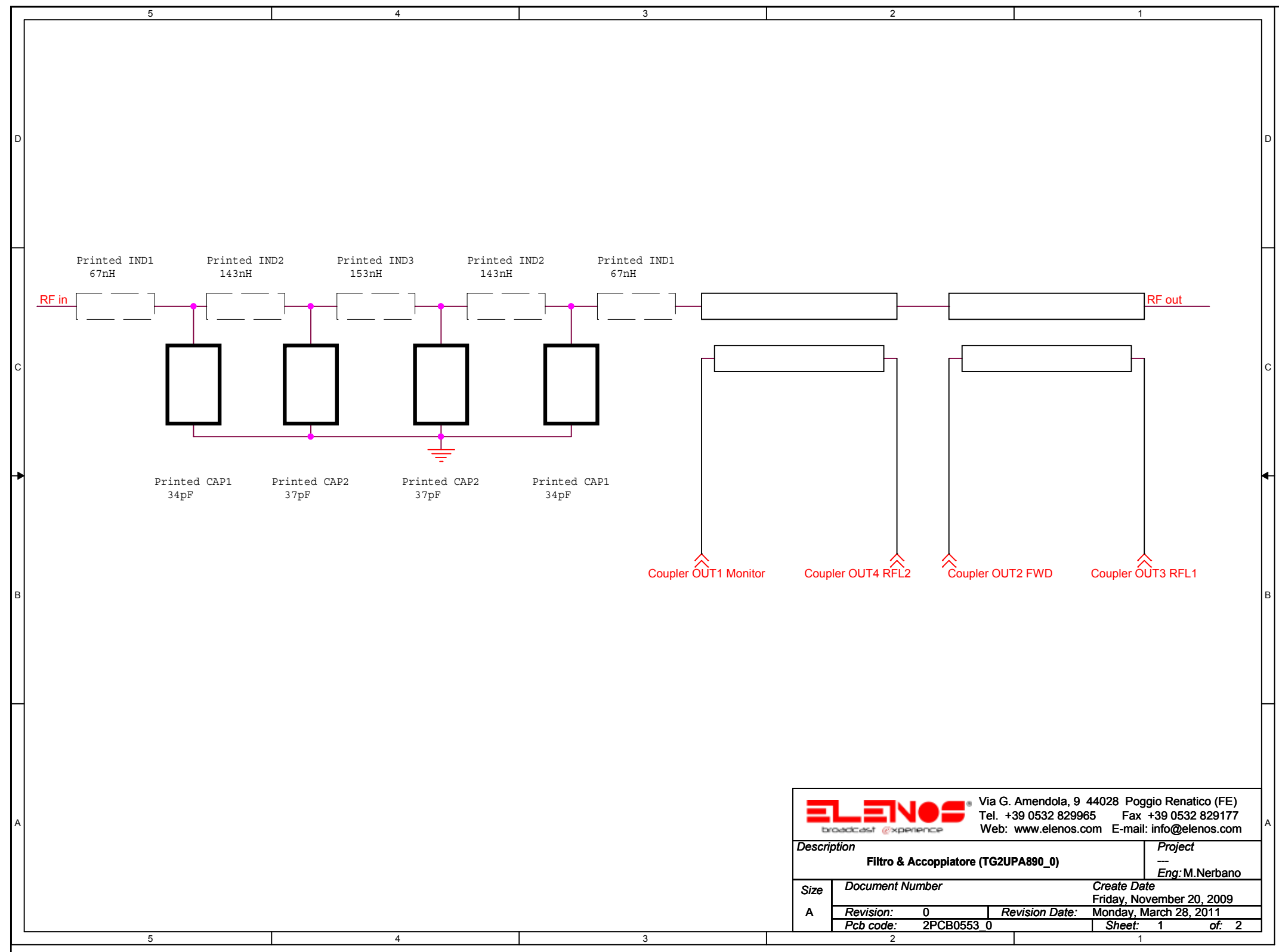




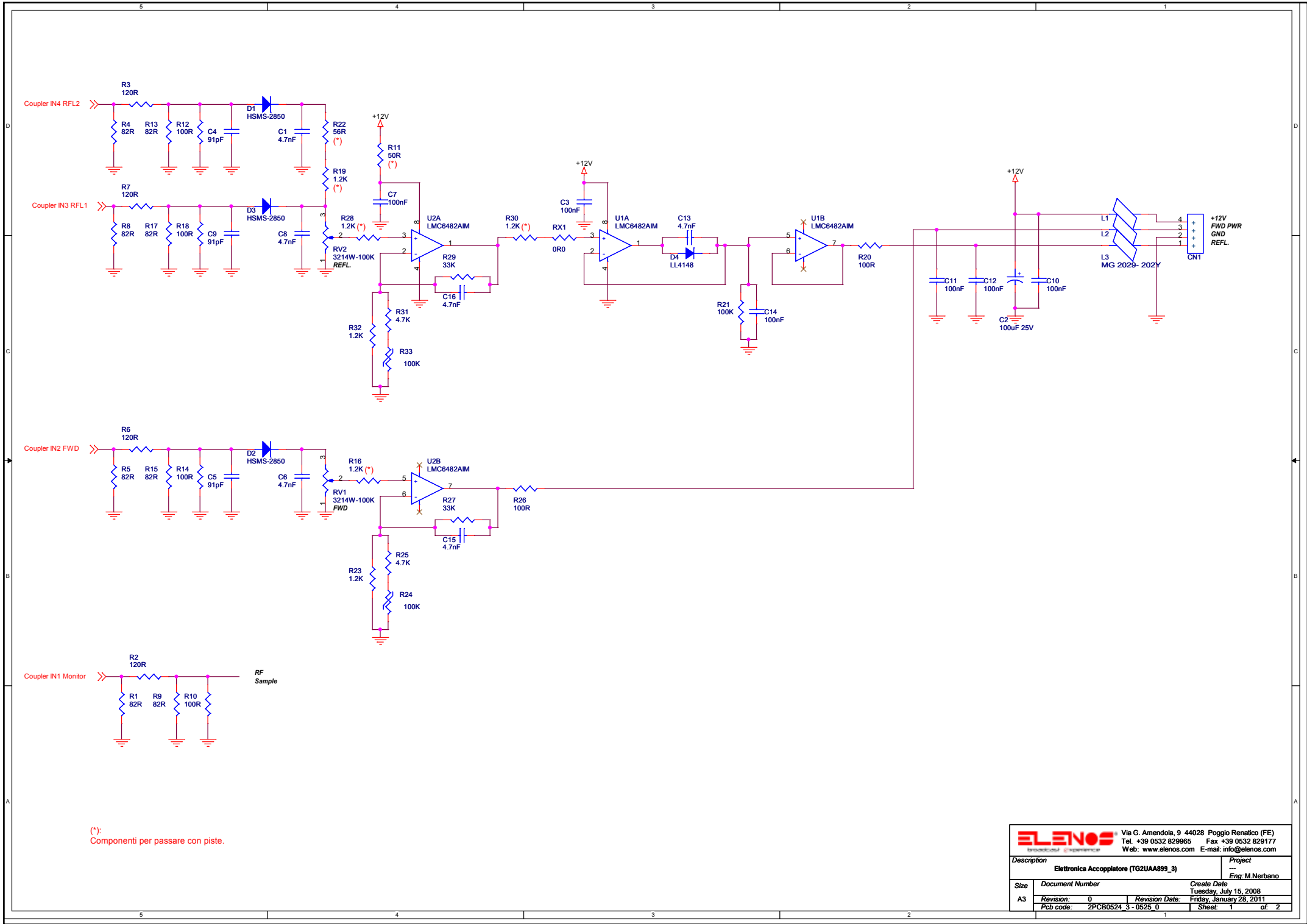


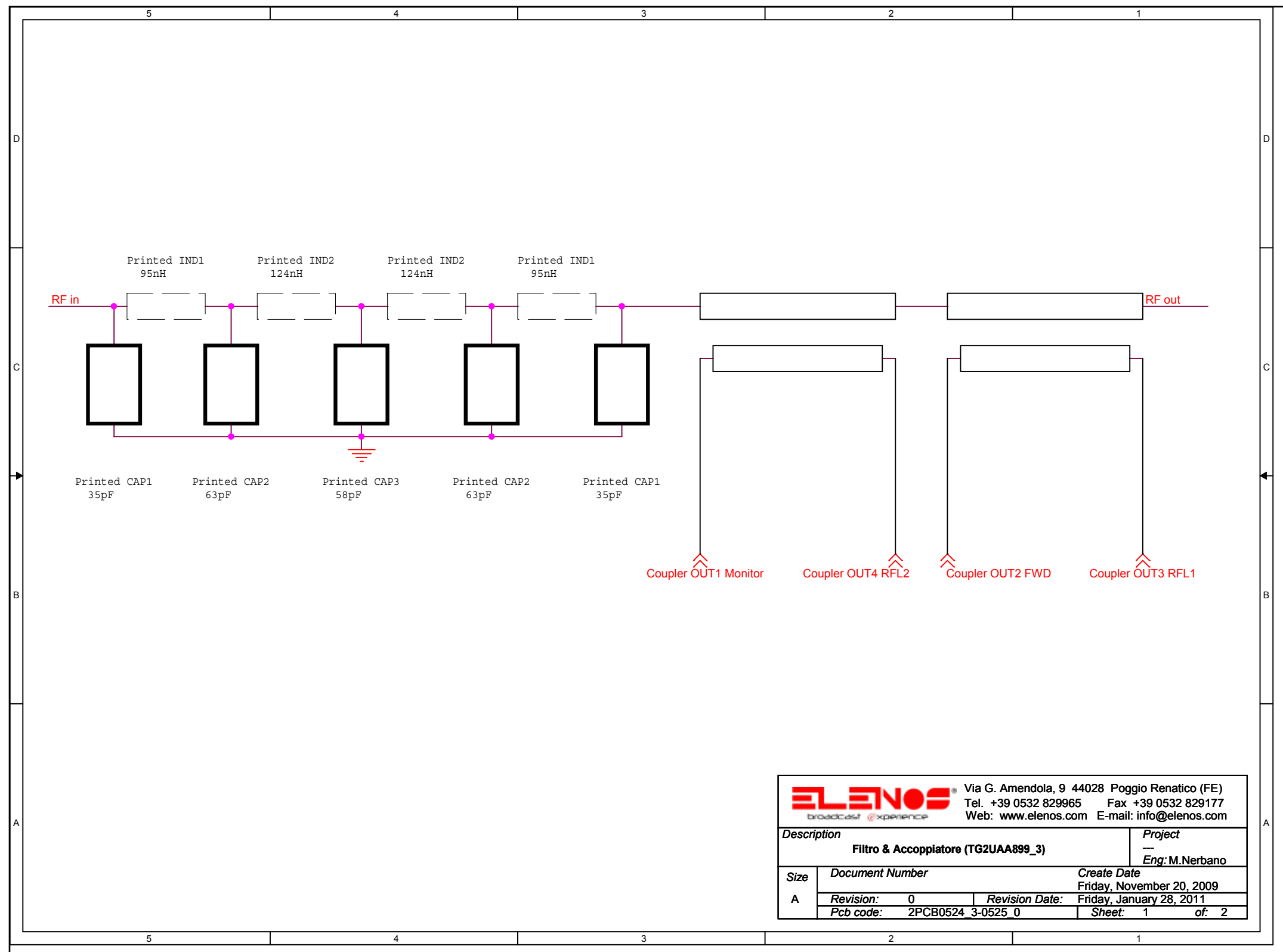
LOW PASS FILTER WITH DIRECTIONAL COUPLER (TG2UPA890-0)



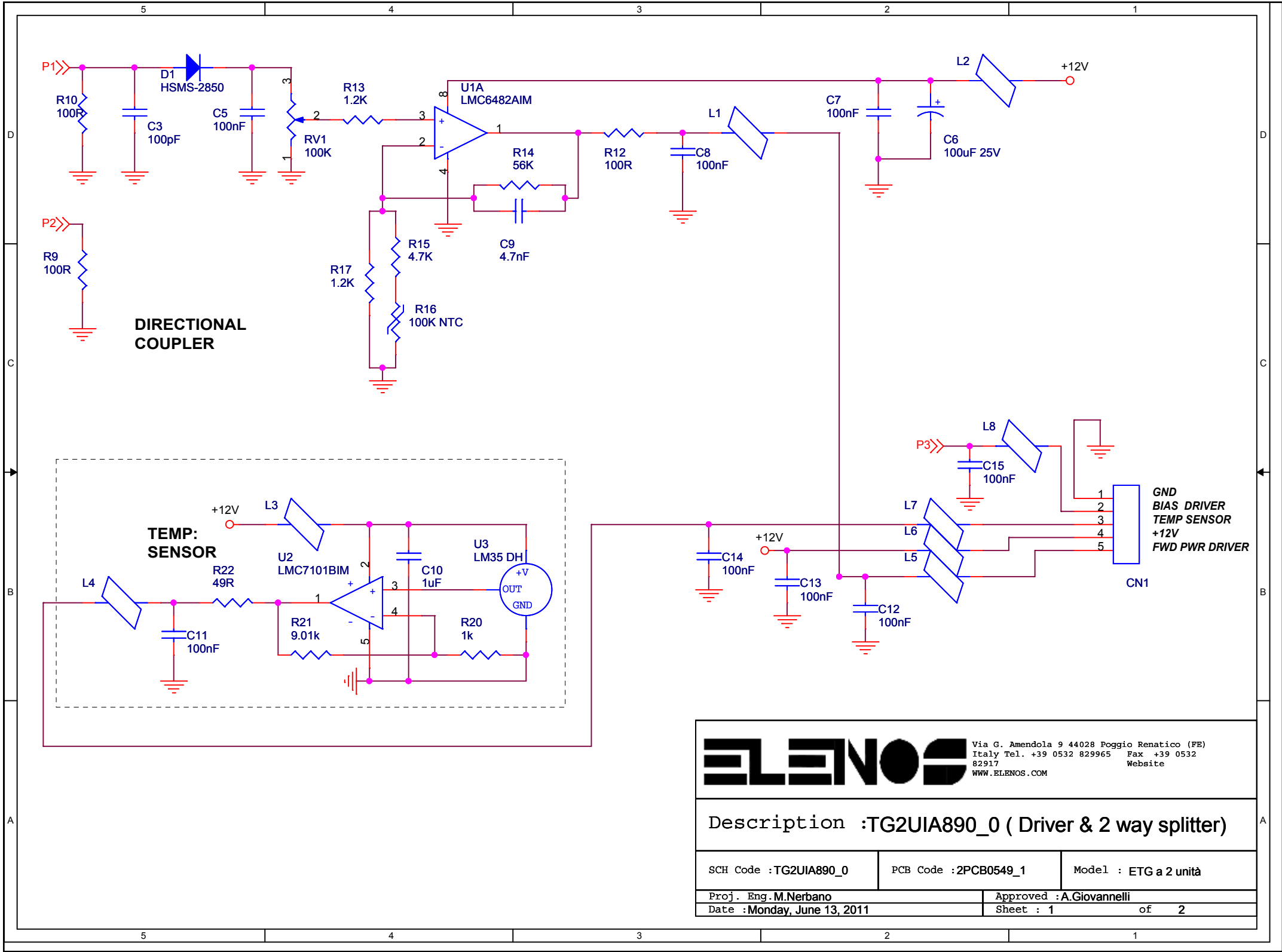


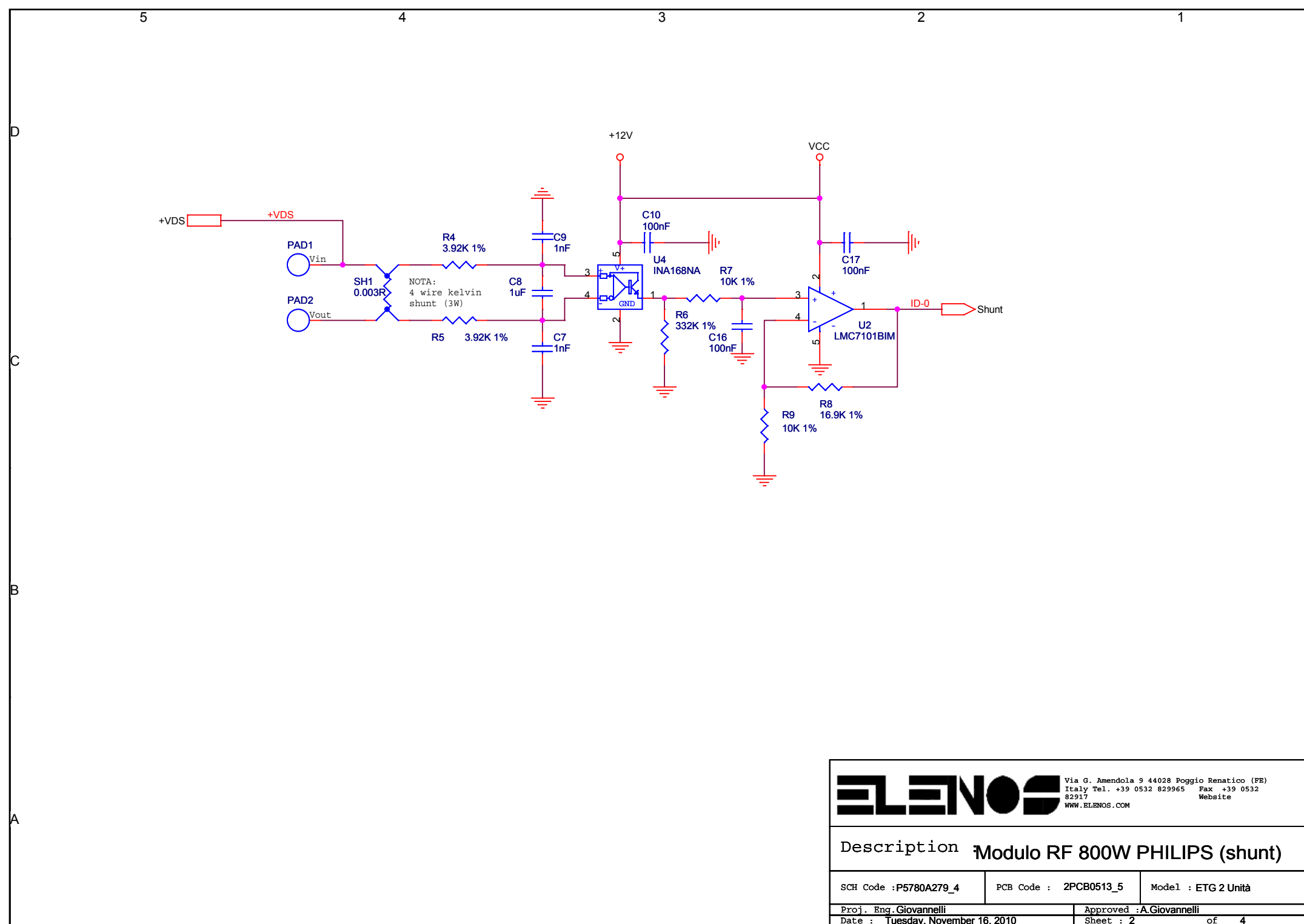
LOW PASS FILTER WITH DIRECTIONAL COUPLER (TG2UAA890-0)

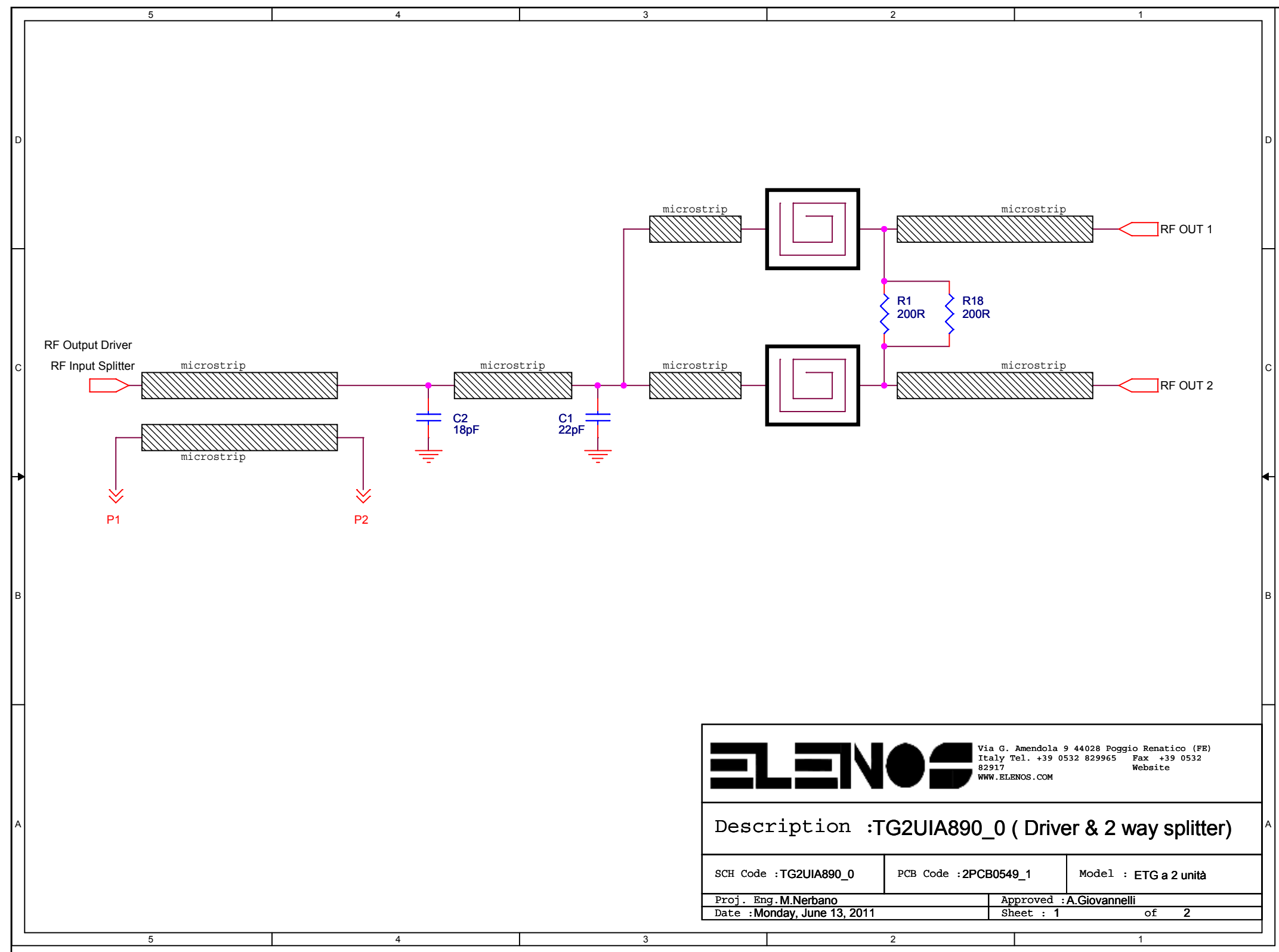




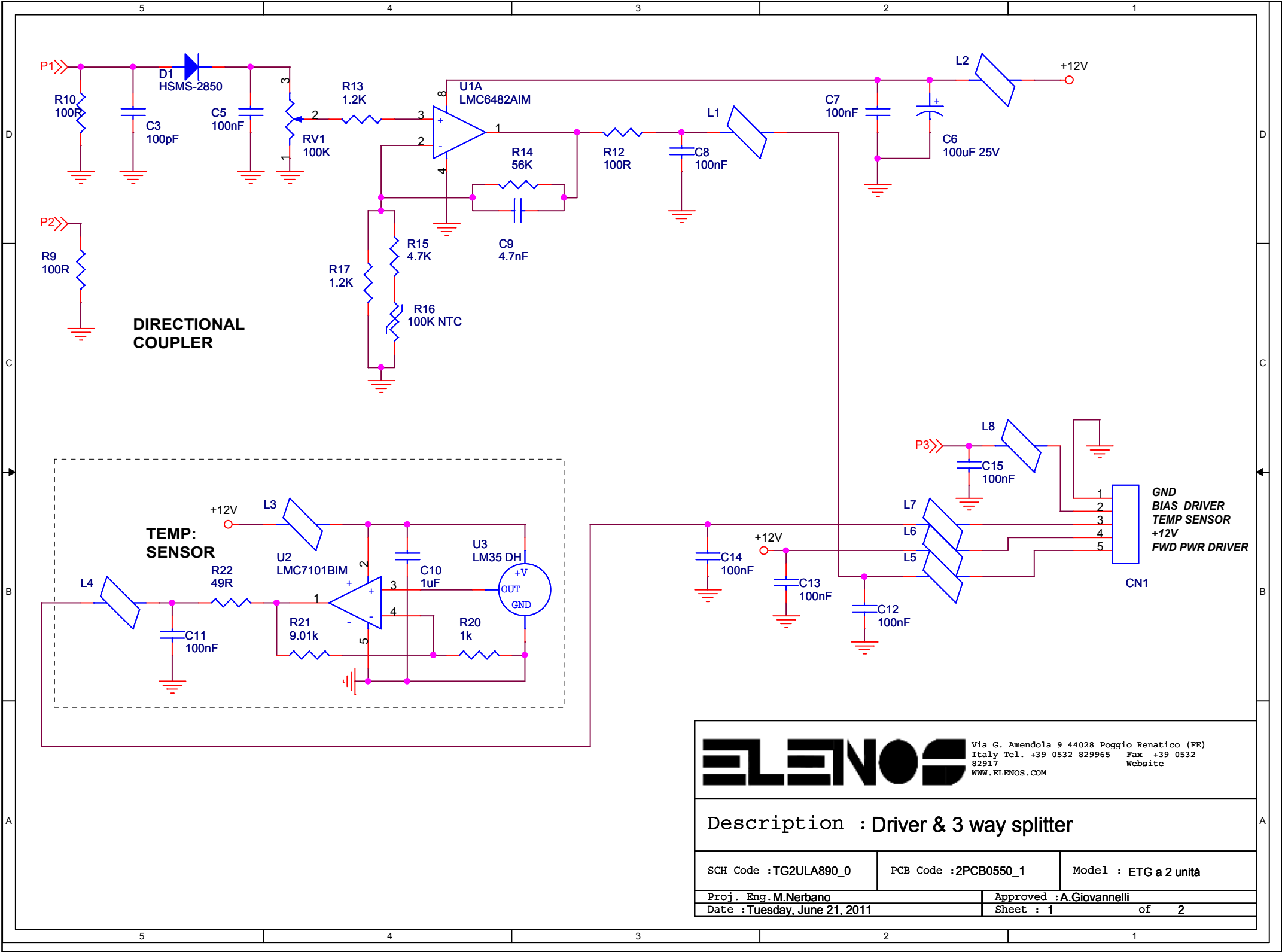
2-WAY DIVIDER WITH DRIVER (TG2UIA890-0)

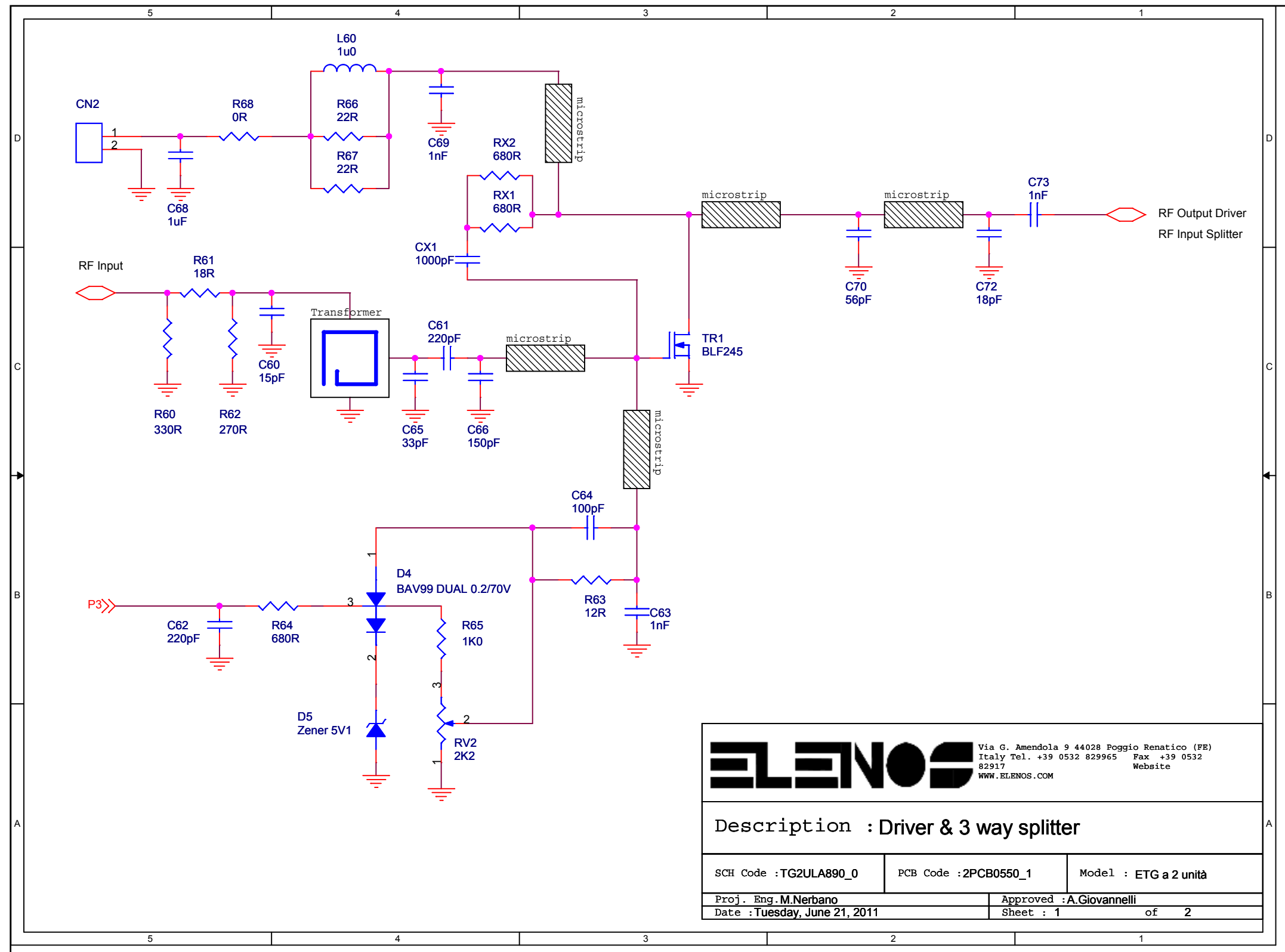


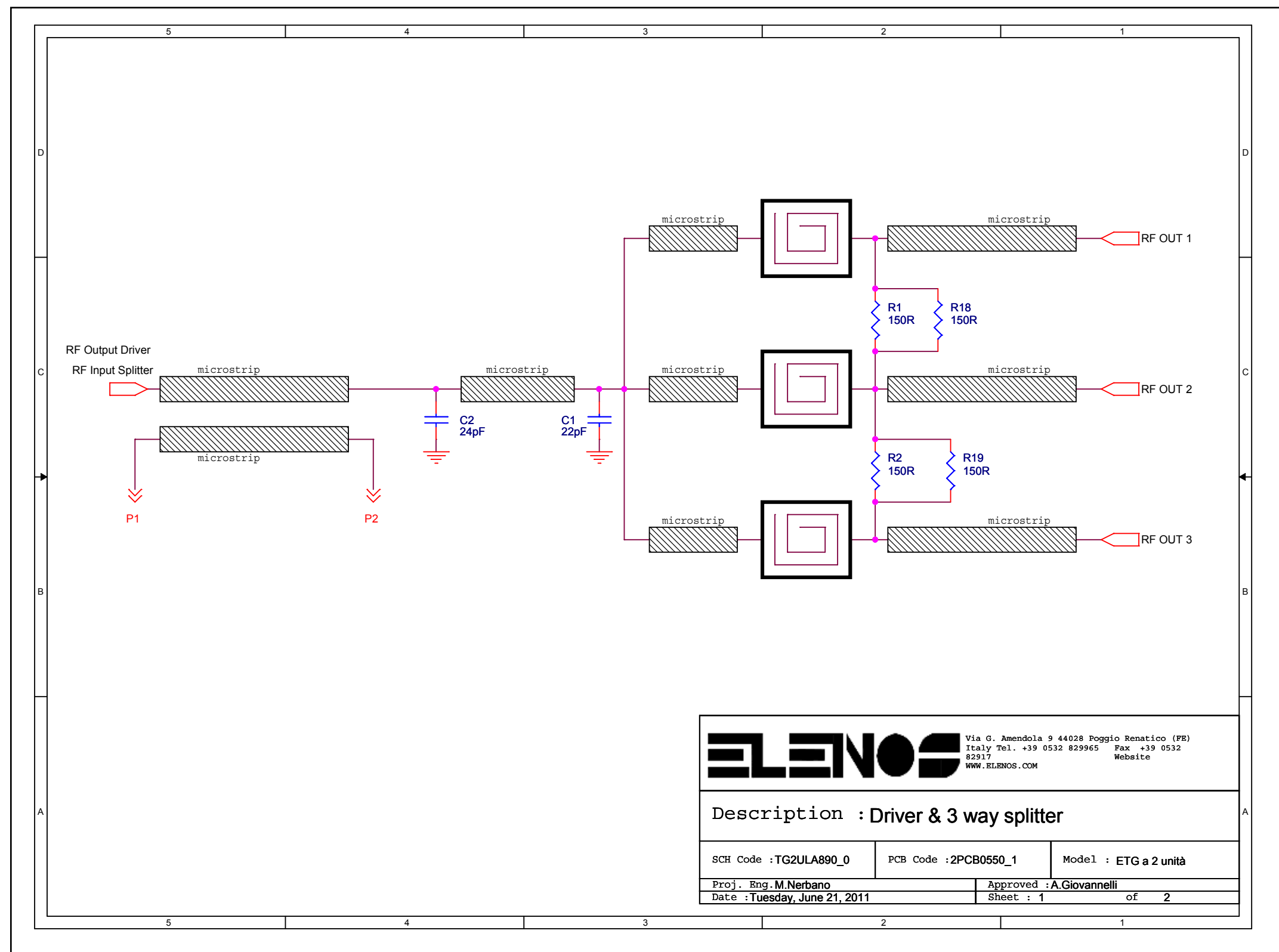




3-WAY DIVIDER WITH DRIVER (TG2ULA890-0)







Via G. Amendola 9 44028 Poggio Renatico (FE)
Italy Tel. +39 0532 829965 Fax +39 0532 82917
WWW.ELENNOS.COM Website

Description : Driver & 3 way splitter

SCH Code : TG2ULA890_0

PCB Code : 2PCB0550_1

Model : ETG a 2 unità

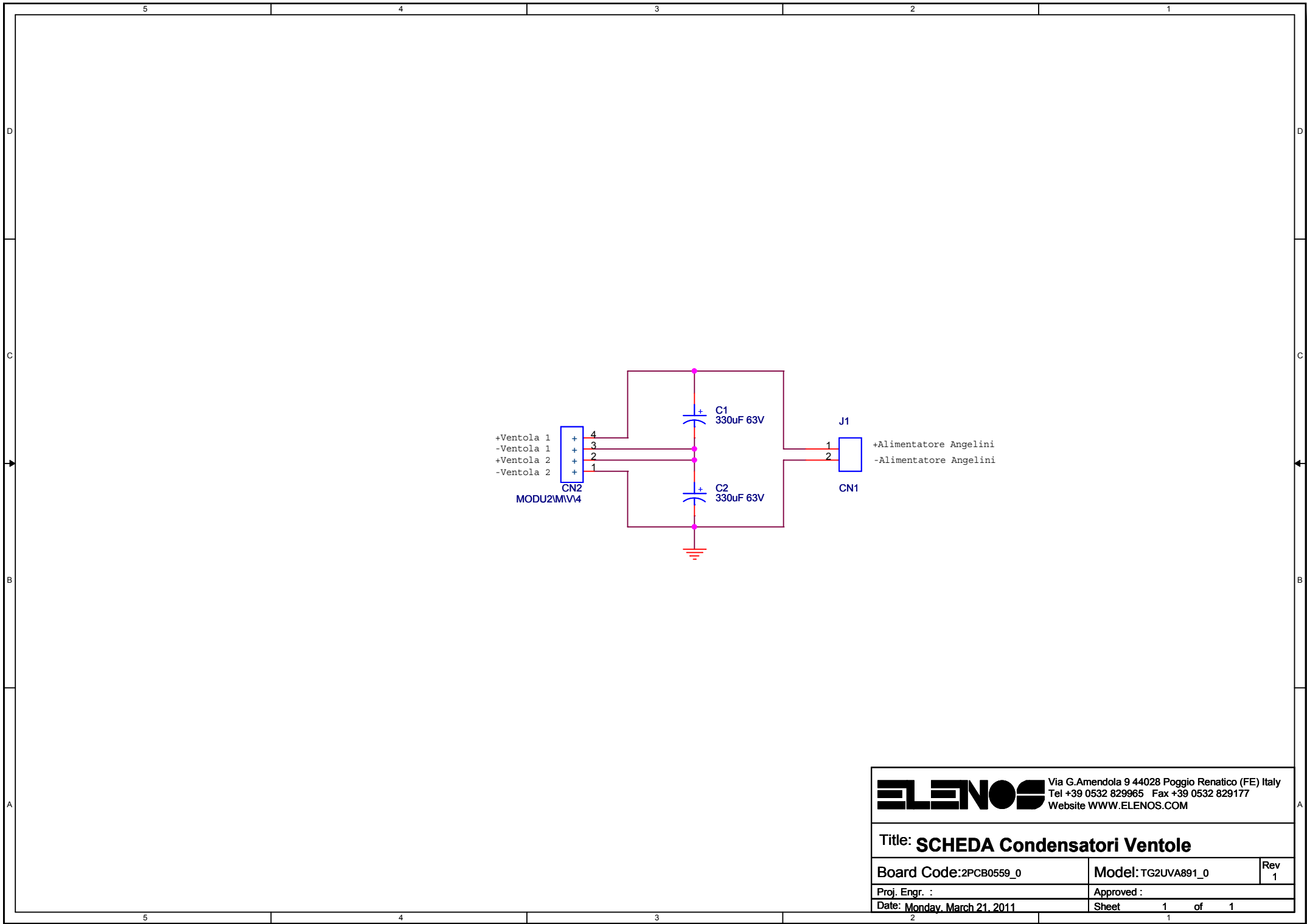
Proj. Eng. M. Nerbano

Approved : A. Giovannelli

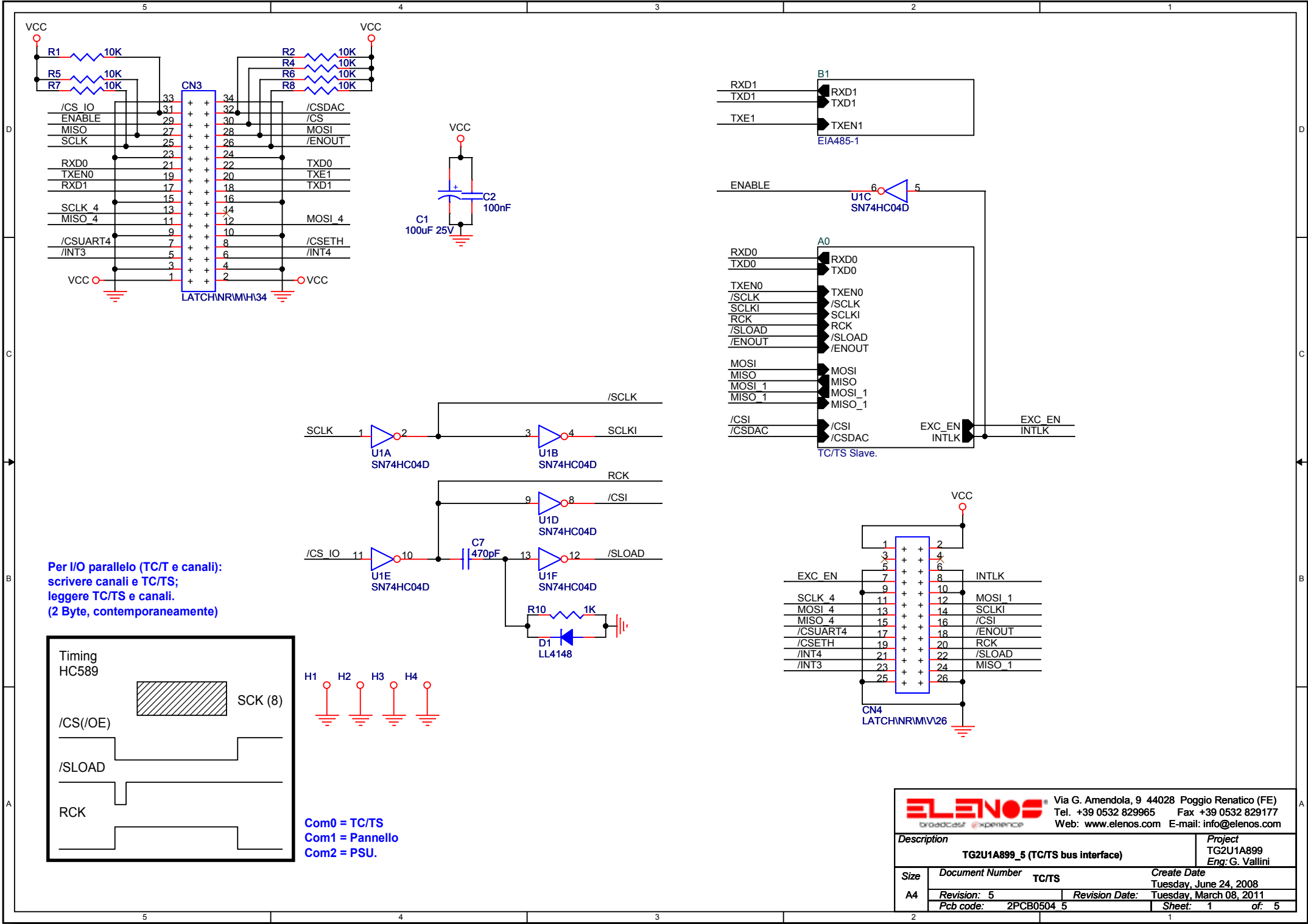
Date : Tuesday, June 21, 2011

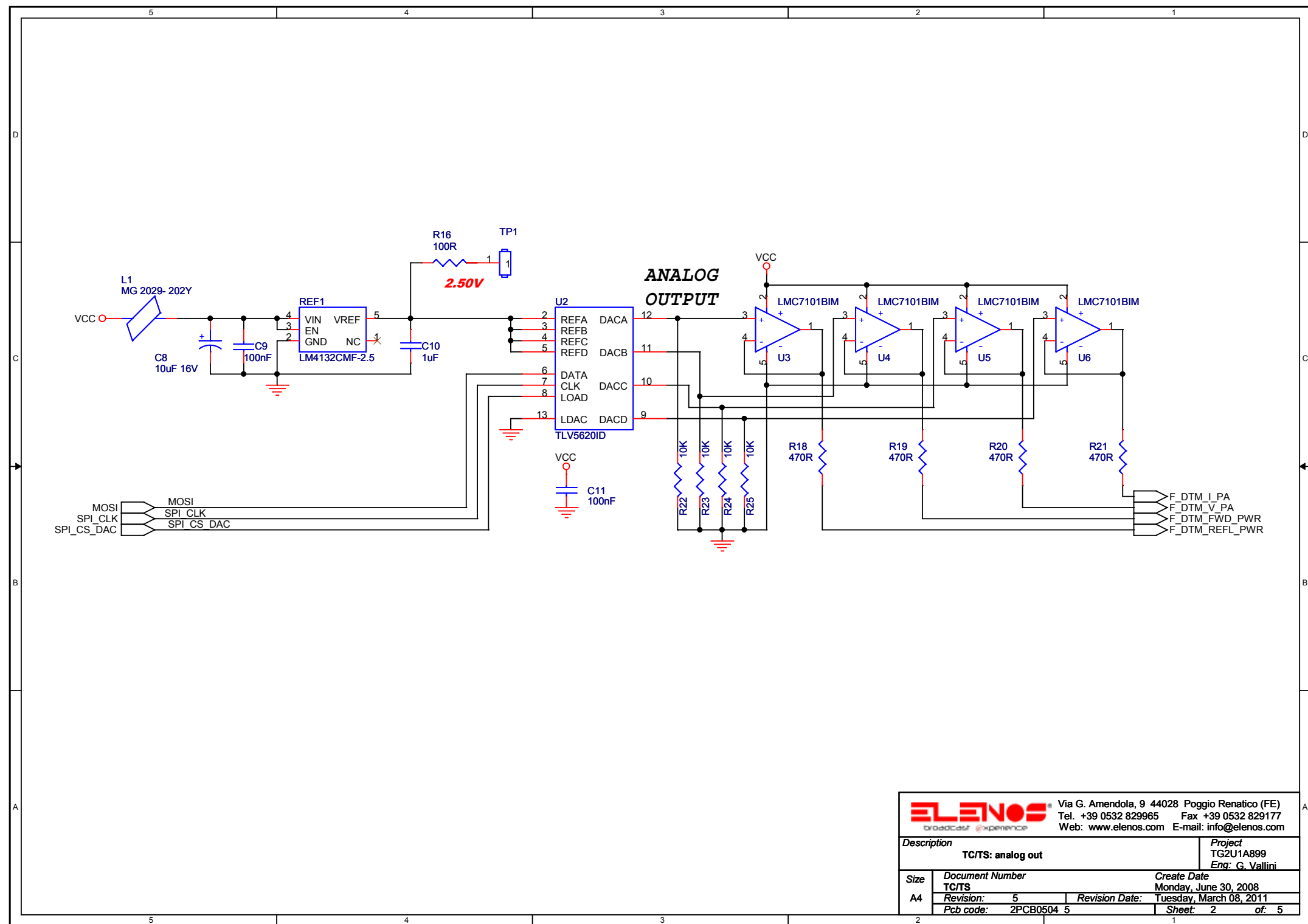
Sheet : 1 of 2

FAN STARTER (TG2UVA891-0)

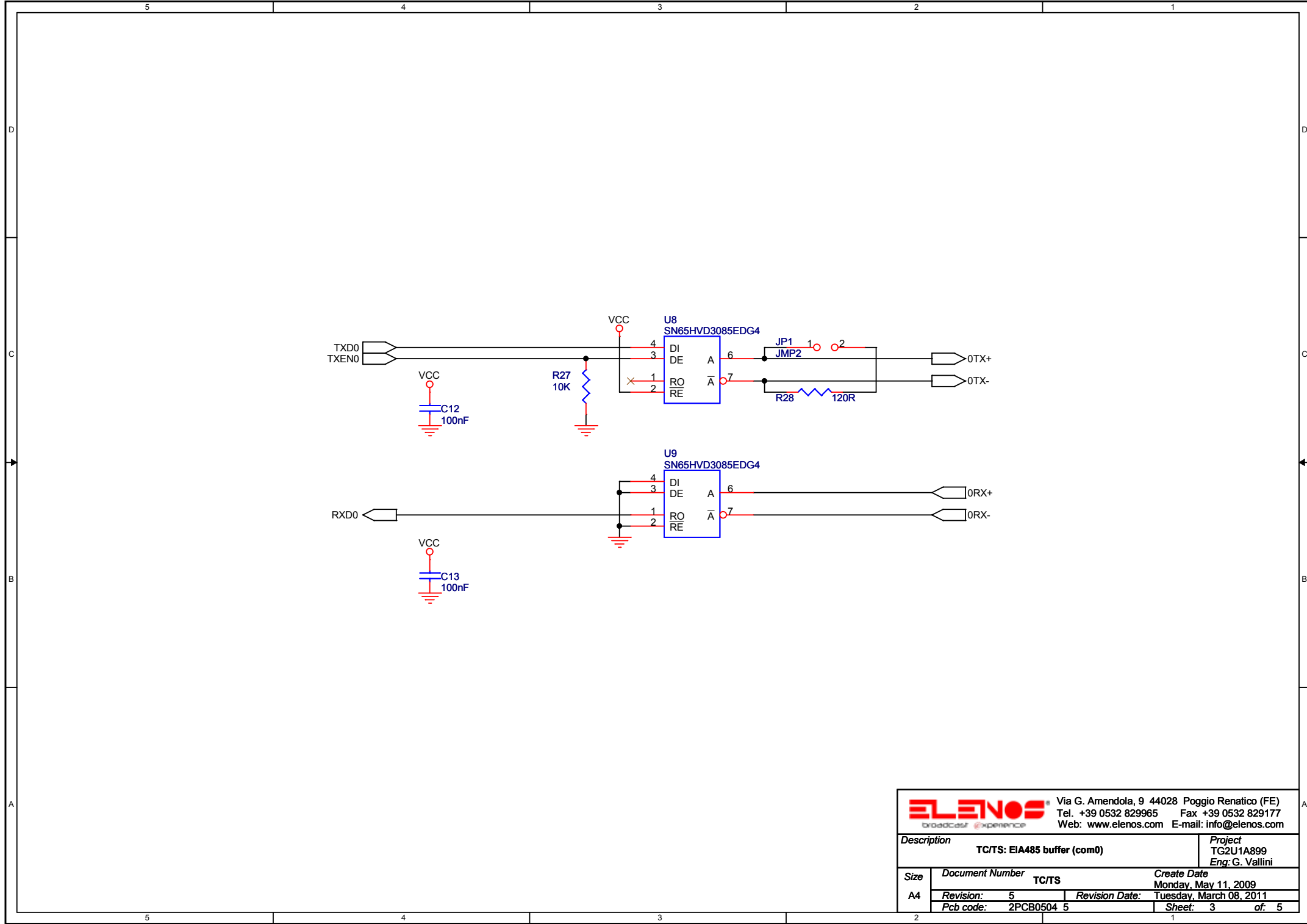


TC/TS BOARD (TG2U1A899_5)





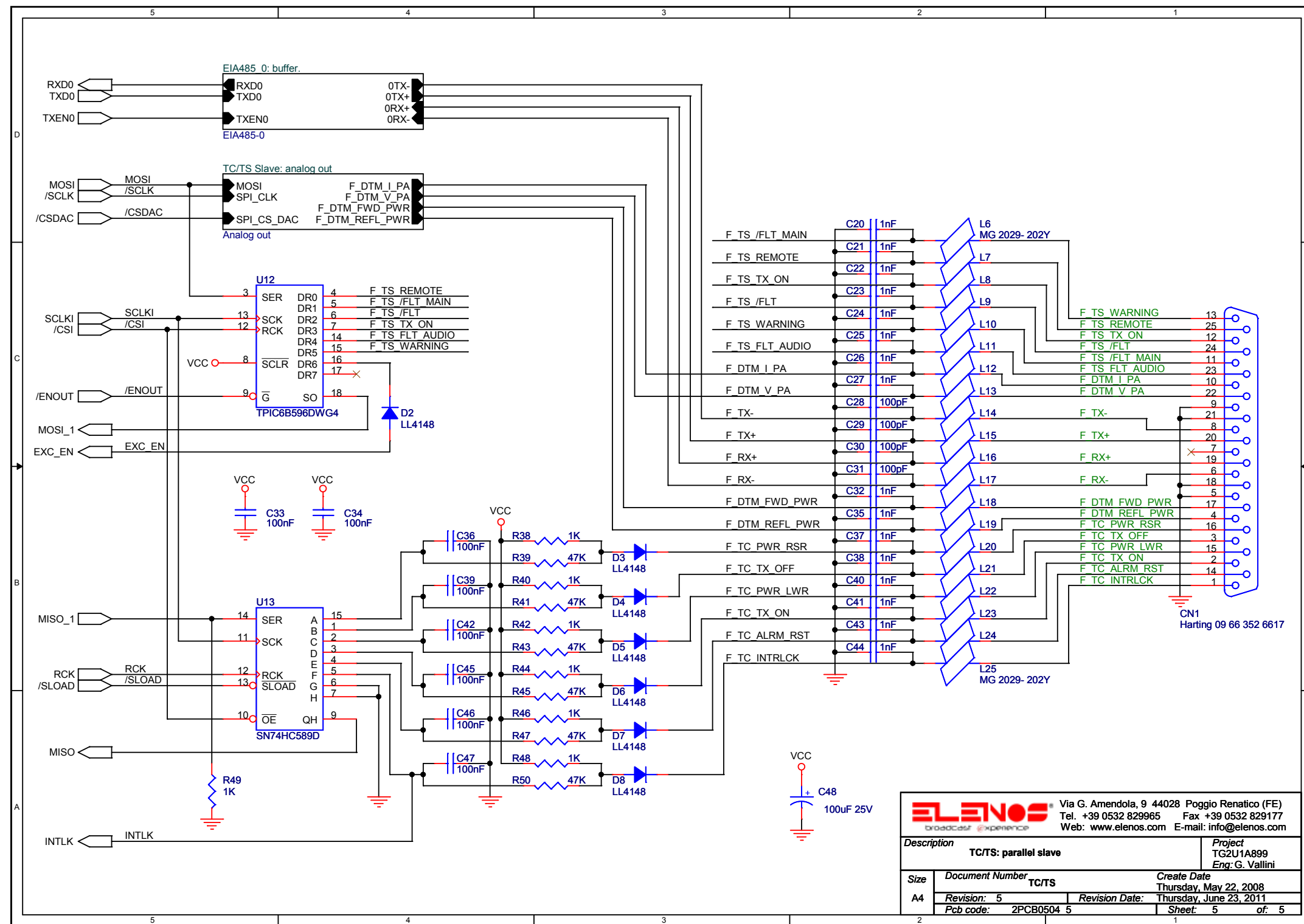
ELENOS broader experience		Via G. Amendola, 9 44028 Poggio Renatico (FE) Tel. +39 0532 829965 Fax +39 0532 829177 Web: www.eLENOS.com E-mail: info@elenos.com	
Description TC/TS: analog out		Project TG2U1A899 Eng: G. Vallini	
Size	Document Number	Create Date	
A4	TC/TS	Monday, June 30, 2008	
	Revision: 5	Revision Date: Tuesday, March 08, 2011	
	Pcb code: 2PCB0504 5	Sheet: 2	of: 5





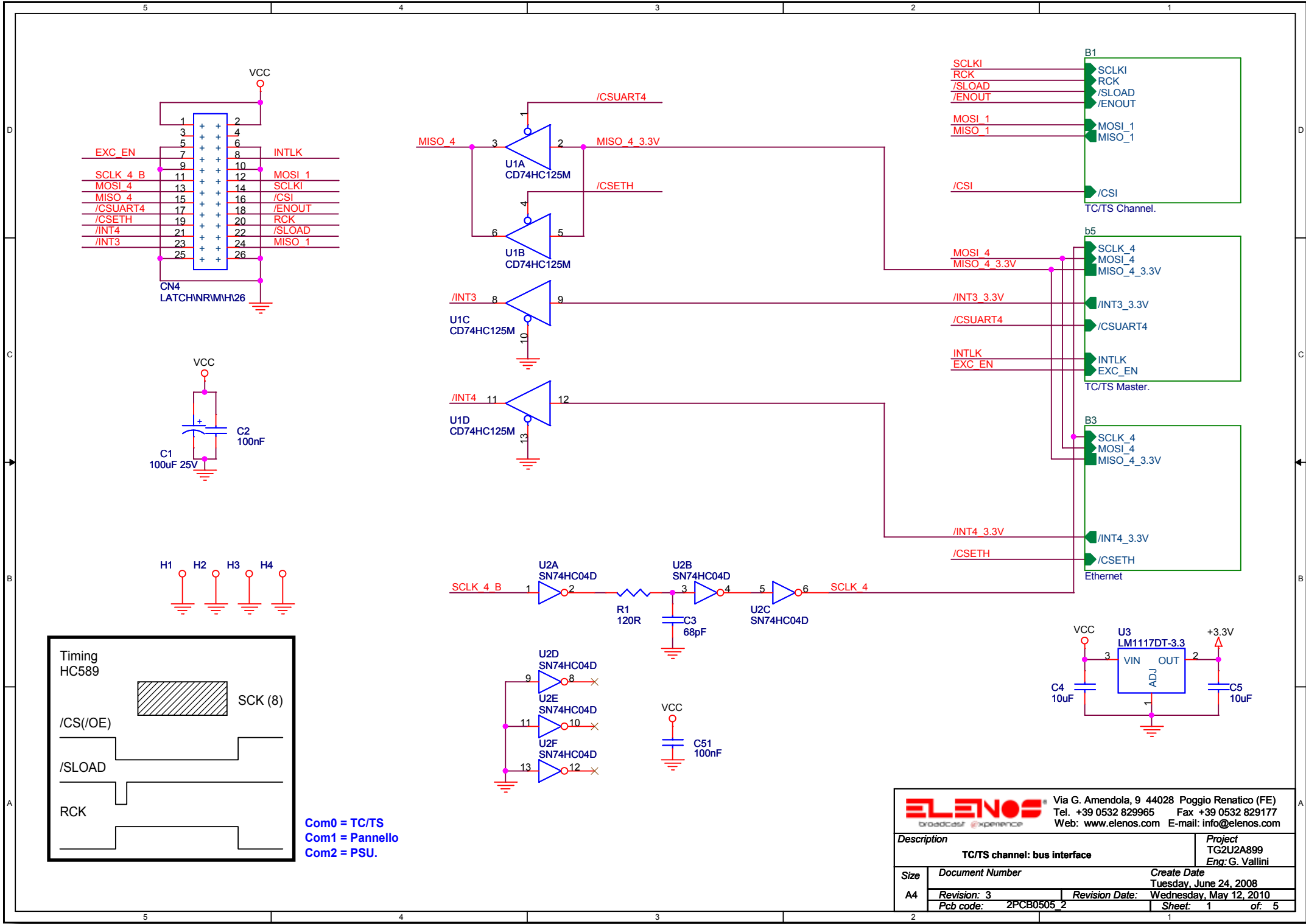
Via G. Amendola, 9 44028 Poggio Renatico (FE)
Tel. +39 0532 829965 Fax +39 0532 829177
Web: www.elenos.com E-mail: info@elenos.com

Description		TC/TS: EIA485 buffer (com0)		Project TG2U1A899 Eng: G. Vallini	
Size A4	Document Number	TC/TS		Create Date Monday, May 11, 2009	
	Revision:	5	Revision Date:	Tuesday, March 08, 2011	
	Pcb code:	2PCB0504	5	Sheet:	3 of 5

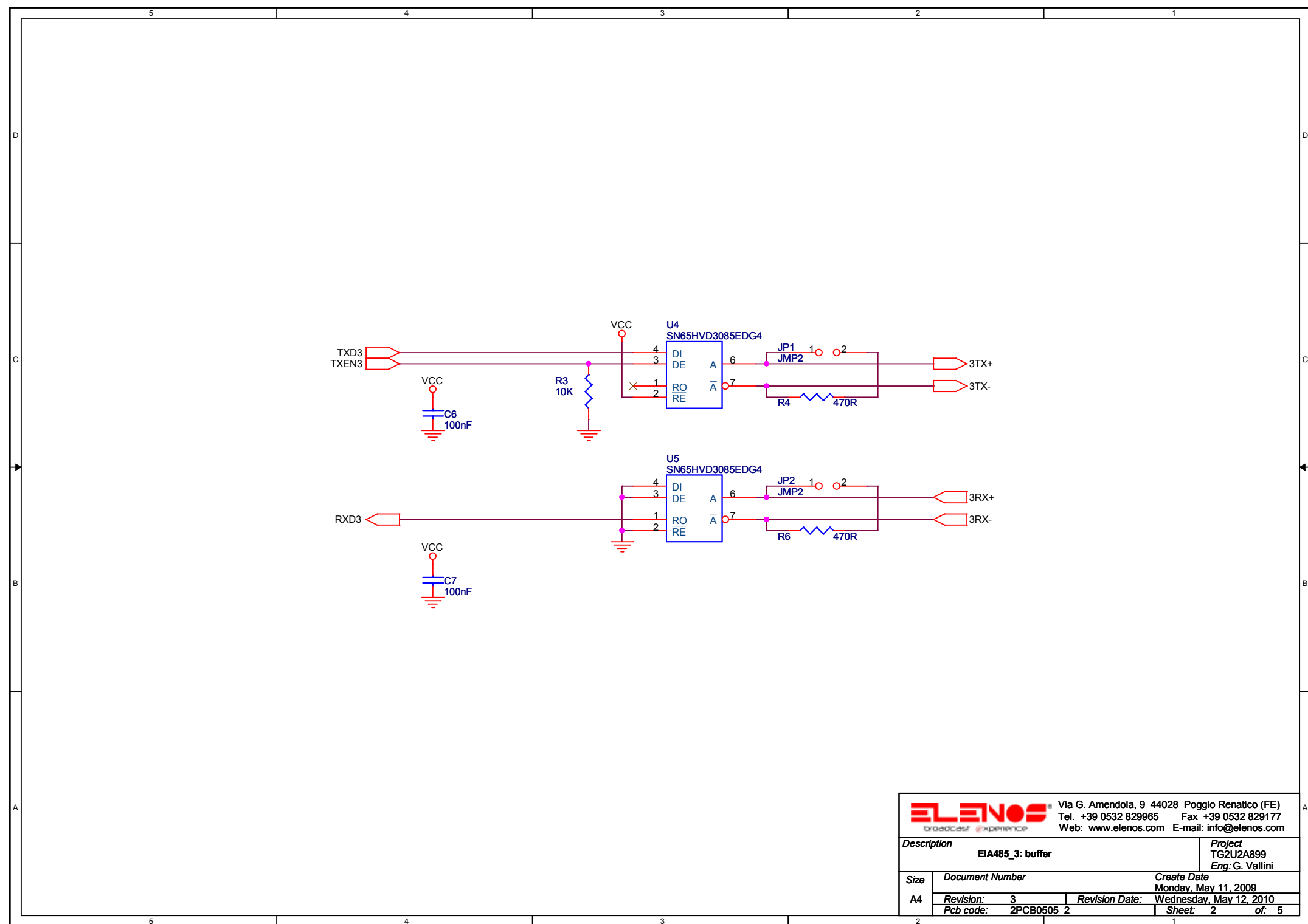


ELENOS broadcast experience		Via G. Amendola, 9 44028 Poggio Renatico (FE) Tel. +39 0532 829965 Fax +39 0532 829177 Web: www.eLENOS.com E-mail: info@elenos.com	
Description		TC/TS: parallel slave	
Size		Document Number	
A4		Revision: 5	
		Pcb code: 2PCB0504 5	
		Create Date	
		Thursday, May 22, 2008	
		Revision Date:	
		Thursday, June 23, 2011	
		Sheet: 5 of: 5	

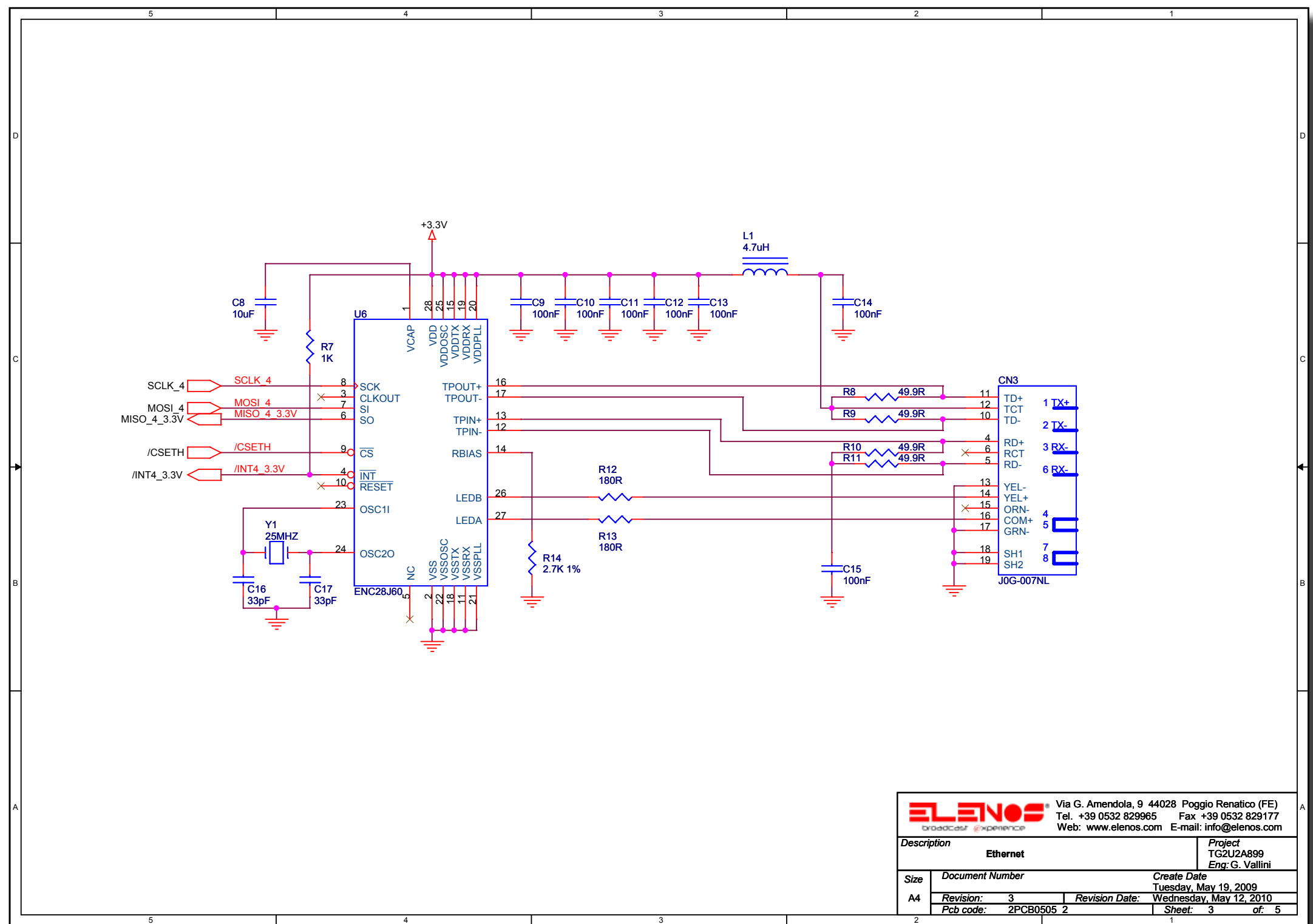
CHANNEL BOARD (TG2U2A899_3)



ELENOS broadcast experience		Via G. Amendola, 9 44028 Poggio Renatico (FE) Tel. +39 0532 829965 Fax +39 0532 829177 Web: www.elenos.com E-mail: info@elenos.com	
Description		Project TG2U2A899 Eng: G. Vallini	
Size A4	Document Number		Create Date
	Revision: 3		Tuesday, June 24, 2008
	Pcb code: 2PCB0505_2		Revision Date: Wednesday, May 12, 2010
		Sheet: 1	of: 5



ELENOS broader experience		Via G. Amendola, 9 44028 Poggio Renatico (FE) Tel. +39 0532 829965 Fax +39 0532 829177 Web: www.elenos.com E-mail: info@elenos.com	
Description EIA485_3: buffer		Project TG2U2A899 Eng: G. Vallini	
Size A4	Document Number		Create Date Monday, May 11, 2009
	Revision: 3	Revision Date:	Wednesday, May 12, 2010
	Pcb code: 2PCB0505 2	Sheet: 2	of: 5



ELENOS broadcast experience		Via G. Amendola, 9 44028 Poggio Renatico (FE) Tel. +39 0532 829965 Fax +39 0532 829177 Web: www.eLENOS.com E-mail: info@elenos.com	
Description		Ethernet	
Size		Create Date	
A4		Tuesday, May 19, 2009	
Revision:		Revision Date:	
3		Wednesday, May 12, 2010	
Pcb code:		Sheet:	
2PCB0505 2		3 of 5	

