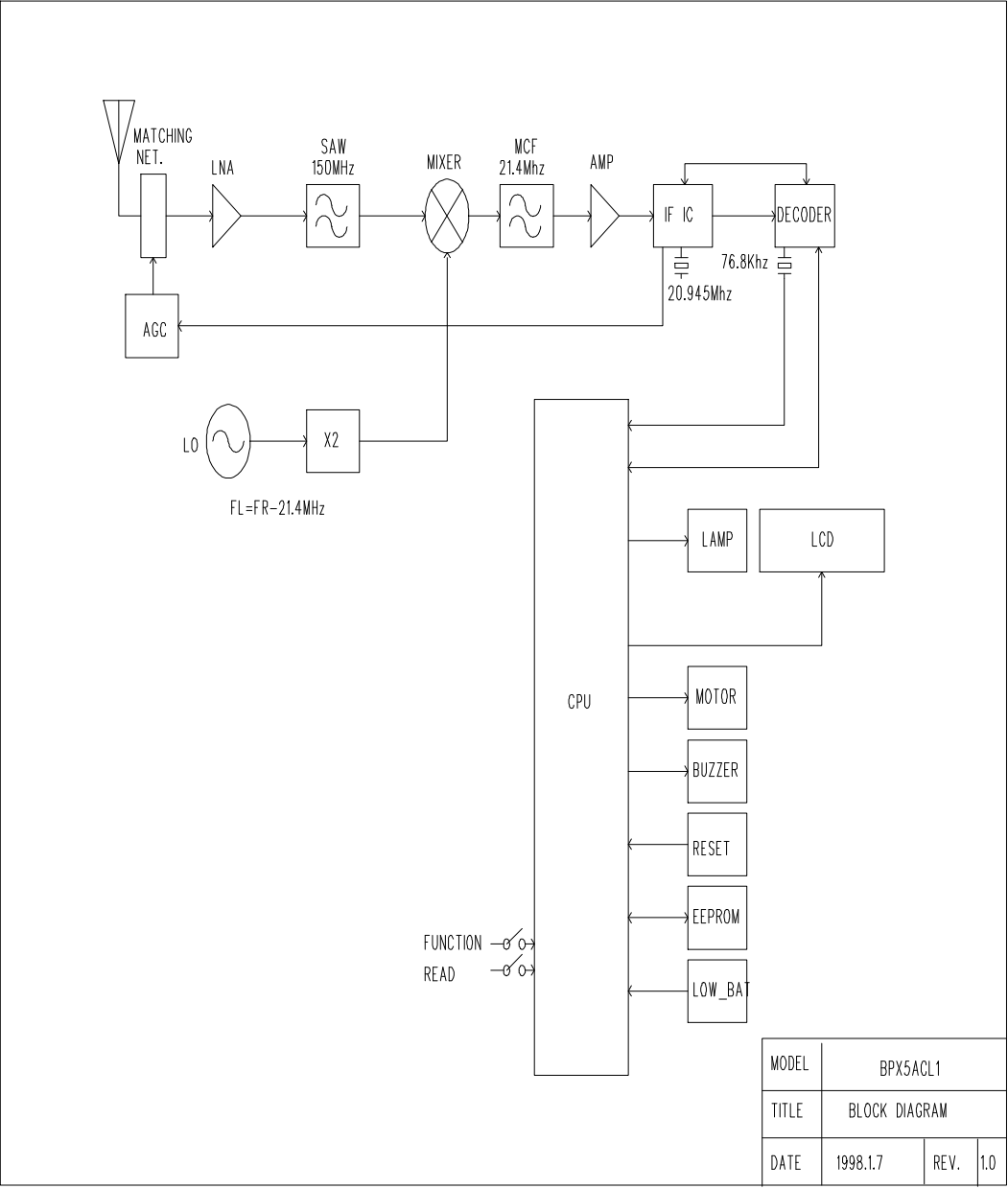
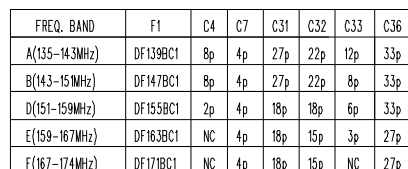
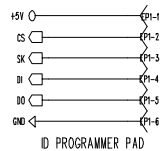
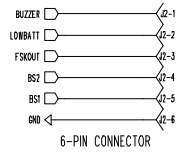
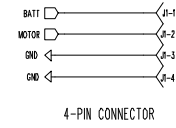
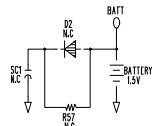
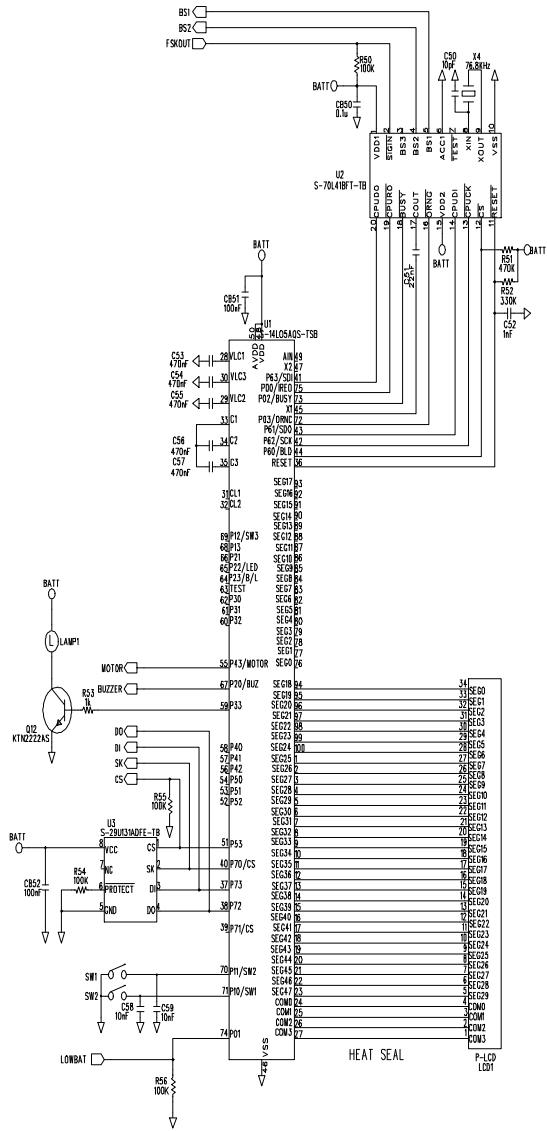


BLOCK DIAGRAM & SCHEMATIC DIAGRAM





MODEL	BPX5ACL1		
TITLE	RF BOARD		
DATED	1999.1.7	REV.	01
SHEET	1 OF 2	HANTEL	



MODEL	BPX5ACL1		
TITLE	LOGIC BOARD		
DATED	1999.1.7	REV.	02
SHEET	2 OF 2	HANTEL	