SPECIFICATIONS

Customer				_	
Product Name		C	hip Antenna		
Sunlord Part N	umber	SLDA	52-2R540G-S1TI	F	
Customer Part	Number				
[⊠New Release	⊠New Released, ☐Revised] SPEC No.: SLDA02150000				
This SPEC is total	al 8 pages includin	g specifications ar	nd appendix.		
[ROHS Compliant	Parts]				
				ı	
	Approved By	Checked By	Issued By		

Shenzhen Sunlord Electronics Co., Ltd.

Address: Sunlord Industrial Park, Dafuyuan Industrial Zone, Baoan, Shenzhen, China
Tel: 0086-755-29832660 Fax: 0086-755-82269029 E-Mail: sunlord@sunlordinc.com

[For Customer app	roval Only]	Date:		
Qualification Status:	Full	Restricted	Rejected	
Approved By	Verified By	Re-checked By	Checked By	
Comments:				

[Version change history]

Rev.	Effective Date	Changed Contents	Change reasons	Approved By
01	l	New release	I	Hai Guo

Scope

This specification applies to SLDA52-2R540G-S1TF of Multi-layer Chip Antenna.

2. Product Description and Identification (Part Number)

1) Description:

Multi-layer Chip Antenna

2) Product Identification (Part Number)

<u>SLDA</u>	<u>52</u>	-2R540G	<u>-S1</u>	<u>T</u>	<u> </u>
1	2	3	4	(5)	6

1	Туре
SLDA	Multilayer Chip Antenna

2	External Dimensions (L×W) (mm)					
	52	5.2×2.1				

③ Center Frequency				
2R540G	2540.0MHz			

4	Series Code	
	S1	•

⑤ Packing			
Т	Tape Carrier Package		

6	Hazardous Substance				
Free Products					
	F				

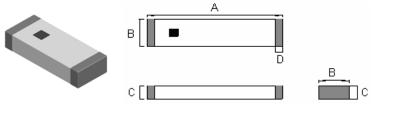
3. Electrical Characteristics

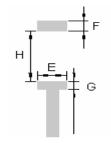
Part Number	Band Width	Peak Gain	Average Gain	VSWR	Impedance	Power Capacity
	MHz	V-XZ	V-XZ	In BW	Ω	W
SLDA52-2R540G-S1	≥200	2.5 dBi typ.	0.5 dBi typ	< 2	50	3 W max

- 1) Operating and storage temperature range (individual chip without packing): -40 $^{\circ}$ C ~ +85 $^{\circ}$ C.
- 2) Storage temperature range (packaging conditions): -10 °C ~ +40 °C and RH 70% (Max.).
- 3) Test equipment: Network Analyzer: HP8719ES.
- 4) Measuring diagram, see appendix 1.

4. Shape and Dimensions

1) Dimensions and recommended PCB pattern for reflow soldering:





								Unit: mm
	Α	В	С	D.	Е	F	G	Н
SLDA52	5.2±0.2	2.1±0.2	1.0±0.2	0.5±0.2	2.0±0.2	1.5±0.2	1.0±0.2	4.0±0.2

Terminal Configuration:



No.	Terminal Name	No.	Terminal Name
(1)	Feeding Point	(2)	NC

5. Test and Measurement Procedures

5.1 Test Conditions

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

a. Ambient Temperature: 20±15℃

b. Relative Humidity: 65±20%

c. Air Pressure: 86 Pa to 106KPa

If any doubt on the results, measurements/tests should be made within the following limits:

a. Ambient Temperature: 20±2°C

b. Relative Humidity: 65±5%

c. Air Pressure: 86KPa to 106KPa

5.2 Visual Examination

a. Inspection Equipment: 20 X magnifier

2 Poliobility Toot							
5.3 Reliability Test		<u> </u>					
Items	Requirements	Test Methods and Remarks					
5.3.1 Terminal Strength	No visible mechanical damage.	 Solder the inductor to the testing jig (glass epoxy board shown as the following figure) using eutectic solder. Then apply a force in the direction of the arrow. 15N force for 5020 series. Keep time: 10±1sec. Chip 15N/10±1s Speed: 1.0mm/s Glass Epoxy Board Mounting Pad					
5.3.2 Resistance to Flexure	No visible mechanical damage.	 Solder the chip to the test jig (glass epoxy board) using a eutectic solder. Then apply a force in the direction shown as the following figure. Solder the chip to the test jig (glass epoxy board) using eutectic solder. Then apply a force in the direction. Flexure: 2mm Pressurizing Speed: 0.5mm/sec Keep time: ≥30 sec 					
	Unit: mm R10	20 10 Flexure: 2					
5.3.3 Dropping	No visible mechanical damage.	Drop the chip 5 times on a wood floor from a height of 50 cm.					
5.3.4 Solderability	 No visible mechanical damage. Wetting shall be exceeded 75% coverage. 	 Solder temperature: 240±2℃ Duration: 3sec Solder: Sn/3.0Ag/0.5Cu Flux: 25% Resin and 75% ethanol in weight 					
5.3.5 Resistance to Soldering Heat	No visible mechanical damage.	 Solder temperature: 260±5℃ Duration: 5 sec Solder: Sn/3.0Ag/0.5Cu Flux: 25% Resin and 75% ethanol in weight The chip shall be stabilized at normal condition for 1~2 hours before measuring. 					
5.3.6 Thermal Shock	No visible mechanical damage. Satisfy electrical Characteristic.	1 Temperature and time: -40 °C for 30±3 min→85 °C for 30±3min 2 Transforming interval: Max. 20 sec. 3 Tested cycle: 10 cycles. 4 The chip shall be stabilized at normal condition for 1~2 hours before measuring. 30 min. 85 °C Ambient Temperature -40 °C 20sec. (max.)					

				19:11		
5.3.7	1	No visible mechanical	1	Temperature: 60±2°C		
Damp Heat		damage.	2	Humidity: 90% to 95% RH		
(Steady States)	2	Satisfy electrical	Duration: 96 ⁺²⁴ hours			
		Characteristic.	④ The chip shall be stabilized at normal condition for			
				hours before measuring.		
5.3.8	1	No visible mechanical	1	Temperature: 85±2°C		
Resistance to High temperature		damage.	2	Duration: 96 ⁺²⁴ hours		
	2	Satisfy electrical	3	The chip shall be stabilized at normal condition for 1~2		
		Characteristic.		hours before measuring.		

6. Packaging and Storage

6.1 Packaging

There is one type of packaging for the Multi-layer Chip Antennas. Please specify the packing code when ordering.

6.1.1 Tape Carrier Packaging:

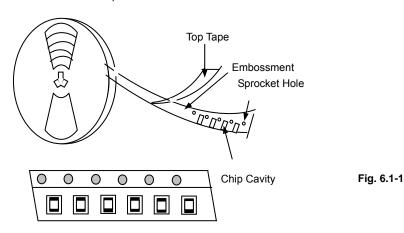
Packaging code: T

- a. Tape carrier packaging are specified in attached figure Fig. 6.1-1~3
- b. Tape carrier packaging quantity please see the following table:

Туре	5020			
Tape	Embossed Tape			
Quantity	4K			

(1) Taping Drawings (Unit: mm)

Embossed Tape



Remark: The sprocket holes are to the right as the tape is pulled toward the user.

(2) Taping Dimensions (Unit: mm)

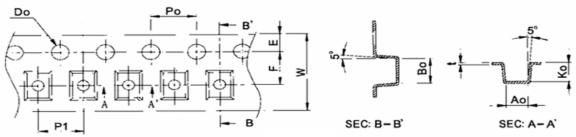
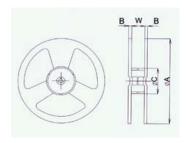


Fig. 6.1-2

Туре	W	P1	E	F	D0	P0	K0	A0	В0	t
Tolerance	±0.1	±0.1	±0.1	±0.15	+0.1/	±0.1	±0.1	±0.1	±0.1	±0.05
SLDA52	12.0	8.00	1.75	5.5	1.5	4.0	1.20	2.35	5.50	0.3

(3) Reel Dimensions (Unit: mm)



Type	Spec.	Dimensions(mm)					
Турс	Орсс.	Α	W	С	В		
SLDA52	13"*12mm	330±1	12.5±0.2	100±0.5	2.3±0.2		

Fig. 6.1-3

6.2 Storage

- a. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40°C or less and 70% RH or less.
- b. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H₂S).
- c. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- d. Solderability specified in **Clause 5.3.5** shall be guaranteed for 12 months from the date of delivery on condition that they are stored at the environment specified in **Clause 3**. For those parts, which passed more than 12 months shall be checked solder-ability before use.

7. Recommended Soldering Technologies

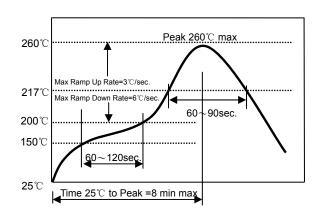
7.1 Reflow Profile

△ Preheat condition: 150 ~200 °C/60~120sec.

△ Allowed time above 217°C: 60~90sec.

△ Max temp: 260°C

[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]



7.2 Iron Soldering Profile

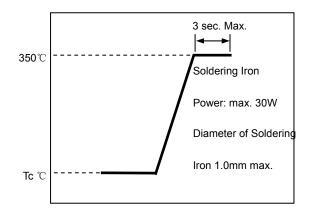
△ Iron soldering power: Max.30W

△ Pre-heating: 150 °C / 60 sec.

△ Soldering Tip temperature: 350 °C Max.

△ Soldering time: 3 sec Max.
 △ Solder paste: Sn/3.0Ag/0.5Cu
 △ Max.1 times for iron soldering

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]



8. Supplier Information

a) Supplier:

Shenzhen Sunlord Electronics Co., Ltd.

b) Manufacturer:

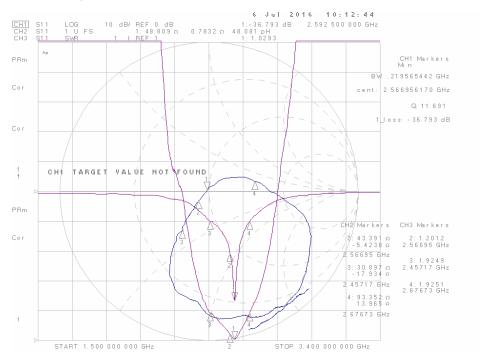
Shenzhen Sunlord Electronics Co., Ltd.

c) Manufacturing Address:

Sunlord Industrial Park, Dafuyuan Industrial Zone, Guanlan, Shenzhen, China Zip: 518110

Appendix 1

1. Without Matching circuit electrical performance:

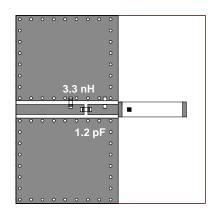


2.With Matching Circuit:

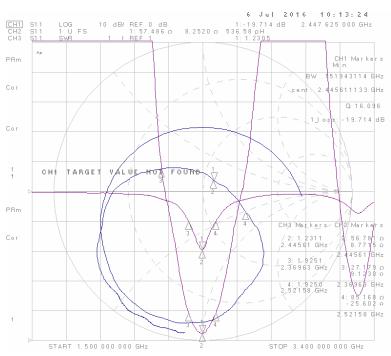
Evaluation board:25*25mm

*Line width should be designed to match 50Ω characteristic impedance, depending on PCB material and thickness.

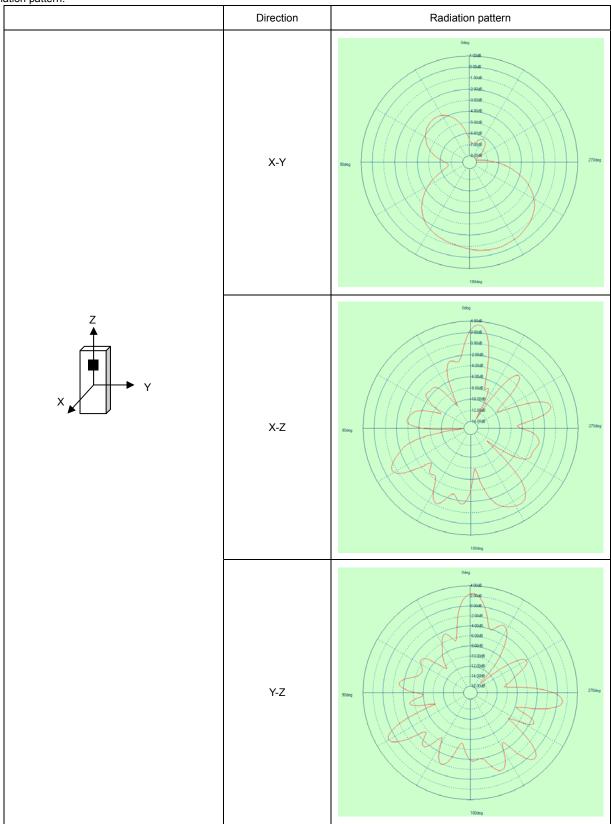
(Matching circuit and component values will be different, depending on PCB layout)



electrical performance:



Radiation pattern:



Note:

Radiation pattern will be different depending on PCB layout, the bigger PCB's size, the better radiation performance.